

./api/api.c

api.c

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* Written by: Rafal Jaworowski <raj@semihalf.com>

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*

*/

./api/api_display.c

api_display.c

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*/

./api/api_net.c

api_net.c

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*/

./api/api_platform-arm.c

api_platform-arm.c

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```
* MA 02111-1307 USA
*
*
* This file contains routines that fetch data from ARM-dependent sources
* (bd_info etc.)
*
*/
```

```
./api/api_platform-powerpc.c
api_platform-powerpc.c
```

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* MA 02111-1307 USA
*
*
* This file contains routines that fetch data from PowerPC-dependent sources
* (bd_info etc.)
*
*/
```

```
./api/api_private.h
api_private.h
```

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*/
```

```
./api/api_storage.c
```

```
api_storage.c
```

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*
*/
```

```
./arch/arm/cpu/arm1136/cpu.c
```

```
cpu.c
```

```
/*
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*
* (C) Copyright 2002
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
```

```
* Marius Groeger <mgroeger@sysgo.de>
*
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* Gary Jennejohn, DENX Software Engineering, <garyj@denx.de>
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*/
```

```
./arch/arm/cpu/arm1136/mx31/devices.c
```

```
devices.c
```

```
/*
*
* (C) Copyright 2009 Magnus Lilja <lilja.magnus@gmail.com>
*
* (c) 2007 Pengutronix, Sascha Hauer <s.hauer@pengutronix.de>
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*/
```

```
./arch/arm/cpu/arm1136/mx31/generic.c
```

```
generic.c
```

```
/*  
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 * MA 02111-1307 USA  
 */
```

```
./arch/arm/cpu/arm1136/mx31/timer.c
```

```
timer.c
```

```
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 * MA 02111-1307 USA  
 */
```

```
./arch/arm/cpu/arm1136/mx35/asm-offsets.c
```

```
asm-offsets.c
```

```
/*  
 * Adapted from Linux v2.6.36 kernel: arch/powerpc/kernel/asm-offsets.c  
 *  
 * This program is used to generate definitions needed by  
 * assembly language modules.  
 *  
 * We use the technique used in the OSF Mach kernel code:  
 * generate asm statements containing #defines,  
 * compile this file to assembler, and then extract the  
 * #defines from the assembly-language output.  
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 */
```

```
./arch/arm/cpu/arm1136/mx35/generic.c
```

```
generic.c
```

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 */
```

```
./arch/arm/cpu/arm1136/mx35/iomux.c
```

```
iomux.c
```

```
/*
```

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```

```
./arch/arm/cpu/arm1136/mx35/mx35_sdram.c
```

```
mx35_sdram.c
```

```
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```

```
./arch/arm/cpu/arm1136/mx35/timer.c
```

```
timer.c
```

```
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```

```
./arch/arm/cpu/arm1136/omap24xx/timer.c
```

```
timer.c
```

```
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* Richard Woodruff <r-woodruff2@ti.com>
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* Alex Zuepke <azu@sysgo.de>
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```

```
./arch/arm/cpu/arm1176/bcm2835/init.c
init.c
```

```
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```

```
./arch/arm/cpu/arm1176/bcm2835/reset.c
reset.c
```

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```
./arch/arm/cpu/arm1176/bcm2835/timer.c
timer.c
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```
./arch/arm/cpu/arm1176/cpu.c
```

```
cpu.c
```

```
/*
```

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```

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```
*/
```

```
./arch/arm/cpu/arm1176/s3c64xx/init.c
```

```
init.c
```

```
/*
```

```
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```

```
* ashokkourla2000@gmail.com
```

```
*
```

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```
./arch/arm/cpu/arm1176/s3c64xx/speed.c
```

```
speed.c
```

```
/*
```

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```
*/
```

```
./arch/arm/cpu/arm1176/s3c64xx/timer.c
```

```
timer.c
```

```
/*
```

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/*
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*/
```

```
./arch/arm/cpu/arm1176/tnetv107x/aemif.c
```

```
aemif.c
```

```
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* TNETV107X: Asynchronous EMIF Configuration
*
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```
./arch/arm/cpu/arm1176/tnetv107x/clock.c
clock.c
```

```
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 * TNETV107X: Clock management APIs
 *
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*/
```

```
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init.c
```

```
/*
 * TNETV107X: Architecture initialization
 *
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```

```
./arch/arm/cpu/arm1176/tnetv107x/mux.c
```

```
mux.c
```

```
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 * TNETV107X: Pinmux configuration  
 *  
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```

```
./arch/arm/cpu/arm1176/tnetv107x/timer.c
```

```
timer.c
```

```
/*  
 * TNETV107X: Timer implementation  
 *  
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 */
```

```
./arch/arm/cpu/arm720t/cpu.c
```

```
cpu.c
```

```
/*  
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*/
```

```
./arch/arm/cpu/arm720t/interrupts.c
interrupts.c
```

```
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*/
```

```
./arch/arm/cpu/arm720t/tegra-common/cpu.h
```

```
cpu.h
```

```
/*
```

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*/
```

```
./arch/arm/cpu/arm720t/tegra-common/spl.c
```

```
spl.c
```

```
/*
```

```
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```
./arch/arm/cpu/arm720t/tegra20/cpu.c
cpu.c
```

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*/
```

```
./arch/arm/cpu/arm920t/a320/timer.c
timer.c
```

```
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```

```
./arch/arm/cpu/arm920t/at91/at91rm9200_devices.c
at91rm9200_devices.c
```

```
/*
 * [partely copied from arch/arm/cpu/arm926ejs/at91/arm9260_devices.c]
 *
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 */
```

```
./arch/arm/cpu/arm920t/at91/clock.c
clock.c
```

```
/*
 * [origin: Linux kernel linux/arch/arm/mach-at91/clock.c]
 *
 * Copyright (C) 2011 Andreas Bießmann
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 */
```

./arch/arm/cpu/arm920t/at91/cpu.c

cpu.c

/*

* [origin: arch/arm/cpu/arm926ejs/at91/cpu.c]

*

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*/

./arch/arm/cpu/arm920t/at91/reset.c

reset.c

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*/
```

```
./arch/arm/cpu/arm920t/at91/timer.c
timer.c
```

```
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```

./arch/arm/cpu/arm920t/cpu.c

cpu.c

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*/

./arch/arm/cpu/arm920t/ep93xx/cpu.c

cpu.c

/*

* Cirrus Logic EP93xx CPU-specific support.

*

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```

```
./arch/arm/cpu/arm920t/ep93xx/led.c
led.c
```

```
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```

```
./arch/arm/cpu/arm920t/ep93xx/speed.c
speed.c
```

```
/*
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*/
```

```
./arch/arm/cpu/arm920t/ep93xx/timer.c
timer.c
```

```
/*
 * Cirrus Logic EP93xx timer support.
 *
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 *
 * Copyright (C) 2004, 2005
 * Cory T. Tusar, Videon Central, Inc., <ctusar@videon-central.com>
 *
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*/
```

```
./arch/arm/cpu/arm920t/imx/generic.c
generic.c
```

```
/*
 * arch/arm/mach-imx/generic.c
 *
 * author: Sascha Hauer
 * Created: april 20th, 2004
 * Copyright: Synertronixx GmbH
 *
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```

```
./arch/arm/cpu/arm920t/imx/speed.c
```

```
speed.c
```

```
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```
./arch/arm/cpu/arm920t/imx/timer.c
```

```
timer.c
```

```
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```
./arch/arm/cpu/arm920t/interrupts.c
interrupts.c
```

```
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```

```
*/  
  
./arch/arm/cpu/arm920t/ks8695/timer.c  
timer.c  
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*/
```

```
./arch/arm/cpu/arm920t/s3c24x0/cpu_info.c  
cpu_info.c  
/*  
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```

./arch/arm/cpu/arm920t/s3c24x0/interrupts.c
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```
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```

./arch/arm/cpu/arm920t/s3c24x0/speed.c
speed.c

```
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```
./arch/arm/cpu/arm920t/s3c24x0/timer.c
timer.c
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm925t/cpu.c
cpu.c
```

```
/*
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* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
```

```
* Marius Groeger <mgroeger@sysgo.de>
*
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* Gary Jennejohn, DENX Software Engineering, <garyj@denx.de>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm925t/omap925.c
omap925.c
```

```
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* Texas Instruments <www.ti.com>
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm925t/timer.c
```

timer.c

```
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/arm/cpu/arm926ejs/armada100/cpu.c

cpu.c

```
/*
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 * Marvell Semiconductor <www.marvell.com>
 * Written-by: Prafulla Wadaskar <prafulla@marvell.com>
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*/
```

```
./arch/arm/cpu/arm926ejs/armada100/dram.c
dram.c
```

```
/*
* (C) Copyright 2010
* Marvell Semiconductor <www.marvell.com>
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>,
* Contributor: Mahavir Jain <mjain@marvell.com>
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* MA 02110-1301 USA
*/
```

```
./arch/arm/cpu/arm926ejs/armada100/timer.c
timer.c
```

```
/*
* (C) Copyright 2010
* Marvell Semiconductor <www.marvell.com>
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
* Contributor: Mahavir Jain <mjain@marvell.com>
*
*/
```

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* MA 02110-1301 USA
*/
```

```
./arch/arm/cpu/arm926ejs/at91/at91cap9_devices.c
at91cap9_devices.c
```

```
/*
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* Lead Tech Design <www.leadtechdesign.com>
*
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* esd electronic system design gmbh <www.esd.eu>
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*/
```

```
./arch/arm/cpu/arm926ejs/at91/at91sam9260_devices.c
```

at91sam9260_devices.c

```
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 */
```

./arch/arm/cpu/arm926ejs/at91/at91sam9261_devices.c

at91sam9261_devices.c

```
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 * MA 02111-1307 USA
 */
```

```
./arch/arm/cpu/arm926ejs/at91/at91sam9263_devices.c
at91sam9263_devices.c
```

```
/*
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 * Lead Tech Design <www.leadtechdesign.com>
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 */
```

```
./arch/arm/cpu/arm926ejs/at91/at91sam9m10g45_devices.c
at91sam9m10g45_devices.c
```

```
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```

```
./arch/arm/cpu/arm926ejs/at91/at91sam9rl_devices.c
at91sam9rl_devices.c
```

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*/
```

```
./arch/arm/cpu/arm926ejs/at91/at91sam9x5_devices.c
at91sam9x5_devices.c
```

```
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*/
```

```
./arch/arm/cpu/arm926ejs/at91/clock.c
clock.c
```

```
/*
* [origin: Linux kernel linux/arch/arm/mach-at91/clock.c]
*
* Copyright (C) 2005 David Brownell
* Copyright (C) 2005 Ivan Kokshaysky
* Copyright (C) 2009 Jean-Christophe PLAGNIOL-VILLARD <plagnioj@jcrosoft.com>
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*/
```

```
./arch/arm/cpu/arm926ejs/at91/cpu.c
cpu.c
```

```
/*
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*/
```

```
./arch/arm/cpu/arm926ejs/at91/eflash.c
```

eflash.c

```
/*
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 */
```

./arch/arm/cpu/arm926ejs/at91/led.c

led.c

```
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 */
```

```
./arch/arm/cpu/arm926ejs/at91/reset.c
```

```
reset.c
```

```
/*
```

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```
*/
```

```
./arch/arm/cpu/arm926ejs/at91/timer.c
```

```
timer.c
```

```
/*
```

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```

```
*/  
  
./arch/arm/cpu/arm926ejs/cache.c  
cache.c  
/*  
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```

```
./arch/arm/cpu/arm926ejs/cpu.c  
cpu.c  
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* Marius Groeger <mgroeger@sysgo.de>  
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*/
```

```
./arch/arm/cpu/arm926ejs/davinci/cpu.c
cpu.c
```

```
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*/
```

```
./arch/arm/cpu/arm926ejs/davinci/da850_lowlevel.c
da850_lowlevel.c
```

```
/*
 * SoC-specific lowlevel code for DA850
 *
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```

```
./arch/arm/cpu/arm926ejs/davinci/da850_pinmux.c
da850_pinmux.c
```

```
/*
 * Pinmux configurations for the DA850 SoCs
 *
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```

```
./arch/arm/cpu/arm926ejs/davinci/dm355.c
dm355.c
```

```
/*
 * SoC-specific code for tms320dm355 and similar chips
 *
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*/
```

```
*/  
  
./arch/arm/cpu/arm926ejs/davinci/dm365.c  
dm365.c  
/*  
* SoC-specific code for tms320dm365 and similar chips  
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```

```
./arch/arm/cpu/arm926ejs/davinci/dm365_lowlevel.c  
dm365_lowlevel.c  
/*  
* SoC-specific lowlevel code for tms320dm365 and similar chips  
* Actually used for booting from NAND with nand_spl.  
*  
* Copyright (C) 2011  
* Heiko Schocher, DENX Software Engineering, hs@denx.de.  
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```

*/

./arch/arm/cpu/arm926ejs/davinci/dm644x.c
dm644x.c

/*

* SoC-specific code for tms320dm644x chips

*

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* Copyright (C) 2008 Lyrtech <www.lyrtech.com>

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*/

./arch/arm/cpu/arm926ejs/davinci/dm646x.c
dm646x.c

/*

* SoC-specific code for TMS320DM646x chips

*

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*/

./arch/arm/cpu/arm926ejs/davinci/dp83848.c
dp83848.c

/*

* National Semiconductor DP83848 PHY Driver for TI DaVinci
* (TMS320DM644x) based boards.

*

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*

* -----

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* MA 02111-1307 USA

*/

./arch/arm/cpu/arm926ejs/davinci/et1011c.c
et1011c.c

/*

* LSI ET1011C PHY Driver for TI DaVinci(TMS320DM6467) board.

*

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*/

./arch/arm/cpu/arm926ejs/davinci/ksz8873.c
ksz8873.c

```
/*
 * Micrel KSZ8873 PHY Driver for TI DaVinci
 * (TMS320DM644x) based boards.
 *
 * Copyright (C) 2011 Heiko Schocher <hsdenx.de>
 *
 * based on:
 * National Semiconductor DP83848 PHY Driver for TI DaVinci
 * (TMS320DM644x) based boards.
 *
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 *
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 * MA 02111-1307 USA
 */
```

./arch/arm/cpu/arm926ejs/davinci/lxt972.c
lxt972.c

```
/*
 * Intel LXT971/LXT972 PHY Driver for TI DaVinci
 * (TMS320DM644x) based boards.
 *
 * Copyright (C) 2007 Sergey Kubushyn <ksi@koi8.net>
 *
 * -----
 *
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* MA 02111-1307 USA
*/

./arch/arm/cpu/arm926ejs/davinci/misc.c
misc.c
/*
* Miscellaneous DaVinci functions.
*
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* Copyright (C) 2007 Sergey Kubushyn <ksi@koi8.net>
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*/

./arch/arm/cpu/arm926ejs/davinci/pinmux.c
pinmux.c
/*
* DaVinci pinmux functions.
*
```

```
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*/
```

```
./arch/arm/cpu/arm926ejs/davinci/psc.c
```

```
psc.c
```

```
/*
```

```
* Power and Sleep Controller (PSC) functions.
```

```
*
```

```
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```
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```

```
*/
```

./arch/arm/cpu/arm926ejs/davinci/reset.c

reset.c

```
/*
 * Processor reset using WDT.
 *
 * Copyright (C) 2012 Dmitry Bondar <bond@inmys.ru>
 * Copyright (C) 2007 Sergey Kubushyn <ksi@koi8.net>
 *
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 */
```

./arch/arm/cpu/arm926ejs/davinci/spl.c

spl.c

```
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 * MA 02111-1307 USA
 */
```

./arch/arm/cpu/arm926ejs/davinci/timer.c

timer.c

```
/*
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 *
 * (C) Copyright 2002
 * Sysgo Real-Time Solutions, GmbH <www.elinos.com>
 * Marius Groeger <mgroeger@sysgo.de>
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 * (C) Copyright 2002
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 * Alex Zuepke <azu@sysgo.de>
```

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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm926ejs/kirkwood/cache.c
cache.c
```

```
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```

*/

./arch/arm/cpu/arm926ejs/kirkwood/cpu.c

cpu.c

/*

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* Written-by: Prafulla Wadaskar <prafulla@marvell.com>

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*/

./arch/arm/cpu/arm926ejs/kirkwood/dram.c

dram.c

/*

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```
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*/
```

```
./arch/arm/cpu/arm926ejs/kirkwood/mpp.c
```

```
mpp.c
```

```
/*  
* arch/arm/mach-kirkwood/mpp.c  
*  
* MPP functions for Marvell Kirkwood SoCs  
* Referenced from Linux kernel source  
*  
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*/
```

```
./arch/arm/cpu/arm926ejs/kirkwood/timer.c
```

```
timer.c
```

```
/*  
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* MA 02110-1301 USA  
*/
```

```
./arch/arm/cpu/arm926ejs/lpc32xx/clk.c
```

```
clk.c
```

```
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*/
```

```
./arch/arm/cpu/arm926ejs/lpc32xx/cpu.c
```

```
cpu.c
```

```
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```

```
./arch/arm/cpu/arm926ejs/lpc32xx/devices.c
```

```
devices.c
```

```
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```

```
./arch/arm/cpu/arm926ejs/lpc32xx/timer.c
```

```
timer.c
```

```
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```

```
./arch/arm/cpu/arm926ejs/mb86r0x/asm-offsets.c
```

```
asm-offsets.c
```

```
/*
 * Adapted from Linux v2.6.36 kernel: arch/powerpc/kernel/asm-offsets.c
 *
 * This program is used to generate definitions needed by
 * assembly language modules.
 *
 * We use the technique used in the OSF Mach kernel code:
 * generate asm statements containing #defines,
 * compile this file to assembler, and then extract the
 * #defines from the assembly-language output.
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*/
```

```
./arch/arm/cpu/arm926ejs/mb86r0x/clock.c
```

```
clock.c
```

```
/*
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 * Matthias Weisser <weisserm@arcor.de>
```

```
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```

```
./arch/arm/cpu/arm926ejs/mb86r0x/reset.c
reset.c
```

```
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```

```
./arch/arm/cpu/arm926ejs/mb86r0x/timer.c
timer.c
```

```
/*
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```

```
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm926ejs/mx25/asm-offsets.c
asm-offsets.c
```

```
/*
* Adapted from Linux v2.6.36 kernel: arch/powerpc/kernel/asm-offsets.c
*
* This program is used to generate definitions needed by
* assembly language modules.
*
* We use the technique used in the OSF Mach kernel code:
* generate asm statements containing #defines,
* compile this file to assembler, and then extract the
* #defines from the assembly-language output.
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*/
```

```
./arch/arm/cpu/arm926ejs/mx25/generic.c
generic.c
```

```
/*
* (C) Copyright 2009 DENX Software Engineering
* Author: John Rigby <jrigby@gmail.com>
*
* Based on mx27/generic.c:
```

```
* Copyright (c) 2008 Eric Jarrige <eric.jarrige@armadeus.org>
* Copyright (c) 2009 Ilya Yanok <yanok@emcraft.com>
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm926ejs/mx25/reset.c
```

```
reset.c
```

```
/*
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* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*
* (C) Copyright 2002
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Alex Zuepke <azu@sysgo.de>
*
* (C) Copyright 2002
* Gary Jennejohn, DENX Software Engineering, <gj@denx.de>
*
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* Ilya Yanok, Emcraft Systems Ltd, <yanok@emcraft.com>
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*/
```

```
./arch/arm/cpu/arm926ejs/mx25/timer.c
```

```
timer.c
```

```
/*
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 * Marius Groeger <mgroeger@sysgo.de>
 *
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 *
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 * Ilya Yanok, Emcraft Systems Ltd, <yanok@emcraft.com>
 *
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 * Author: John Rigby <jrigby@gmail.com>
 *      Add support for MX25
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm926ejs/mx27/asm-offsets.c
```

```
asm-offsets.c
```

```
/*
 * Adapted from Linux v2.6.36 kernel: arch/powerpc/kernel/asm-offsets.c
 *
 */
```

```
* This program is used to generate definitions needed by
* assembly language modules.
*
* We use the technique used in the OSF Mach kernel code:
* generate asm statements containing #defines,
* compile this file to assembler, and then extract the
* #defines from the assembly-language output.
*
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* modify it under the terms of the GNU General Public License
* as published by the Free Software Foundation; either version
* 2 of the License, or (at your option) any later version.
*/
```

```
./arch/arm/cpu/arm926ejs/mx27/generic.c
```

```
generic.c
```

```
/*
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* Copyright (c) 2009 Ilya Yanok <yanok@emcraft.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm926ejs/mx27/reset.c
```

```
reset.c
```

```
/*
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* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*
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* Alex Zuepke <azu@sysgo.de>
*
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* Gary Jennejohn, DENX Software Engineering, <gj@denx.de>
*/
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm926ejs/mx27/timer.c
```

```
timer.c
```

```
/*
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* Marius Groeger <mgroeger@sysgo.de>
*
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* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm926ejs/mxs/clock.c
clock.c
```

```
/*
 * Freescale i.MX28 clock setup code
 *
 * Copyright (C) 2011 Marek Vasut <marek.vasut@gmail.com>
 * on behalf of DENX Software Engineering GmbH
 *
 * Based on code from LTIB:
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm926ejs/mxs/iomux.c
iomux.c
```

```
/*
 * Copyright 2004-2006,2010 Freescale Semiconductor, Inc. All Rights Reserved.
 * Copyright (C) 2008 by Sascha Hauer <kernel@pengutronix.de>
 * Copyright (C) 2009 by Jan Weitzel Phytex Messtechnik GmbH,
 * <armlinux@phytec.de>
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,  
* MA 02110-1301, USA.  
*/
```

```
./arch/arm/cpu/arm926ejs/mxs/mxs.c
```

```
mxs.c
```

```
/*  
* Freescale i.MX28 common code  
*  
* Copyright (C) 2011 Marek Vasut <marek.vasut@gmail.com>  
* on behalf of DENX Software Engineering GmbH  
*  
* Based on code from LTIB:  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/arm/cpu/arm926ejs/mxs/mxs_init.h
```

```
mxs_init.h
```

```
/*  
* Freescale i.MX28 SPL functions  
*  
* Copyright (C) 2011 Marek Vasut <marek.vasut@gmail.com>  
* on behalf of DENX Software Engineering GmbH  
*  
* See file CREDITS for list of people who contributed to this
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/arm/cpu/arm926ejs/mxs/spl_boot.c
```

```
spl_boot.c
```

```
/*  
* Freescale i.MX28 Boot setup  
*  
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*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/arm/cpu/arm926ejs/mxs/spl_lradc_init.c
```

```
spl_lradc_init.c
```

```
/*  
* Freescale i.MX28 Battery measurement init  
*  
*/
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm926ejs/mxs/spl_mem_init.c
```

```
spl_mem_init.c
```

```
/*
 * Freescale i.MX28 RAM init
 *
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 * on behalf of DENX Software Engineering GmbH
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm926ejs/mxs/spl_power_init.c
```

spl_power_init.c

```
/*
 * Freescale i.MX28 Boot PMIC init
 *
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 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/arm/cpu/arm926ejs/mxs/timer.c

timer.c

```
/*
 * Freescale i.MX28 timer driver
 *
 * Copyright (C) 2011 Marek Vasut <marek.vasut@gmail.com>
 * on behalf of DENX Software Engineering GmbH
 *
 * Based on code from LTIB:
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*/
```

```
./arch/arm/cpu/arm926ejs/nomadik/gpio.c
```

```
gpio.c
```

```
/*
```

```
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```

```
*
```

```
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* project.
```

```
*
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```

```
*/
```

```
./arch/arm/cpu/arm926ejs/nomadik/timer.c
```

```
timer.c
```

```
/*
```

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```

```
*
```

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```

```
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* MA 02111-1307 USA  
*/
```

```
./arch/arm/cpu/arm926ejs/omap/cpuinfo.c  
cpuinfo.c
```

```
/*  
* OMAP1 CPU identification code  
*  
* Copyright (C) 2004 Nokia Corporation  
* Written by Tony Lindgren <tony@atomide.com>  
*  
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* it under the terms of the GNU General Public License version 2 as  
* published by the Free Software Foundation.  
*/
```

```
./arch/arm/cpu/arm926ejs/omap/timer.c  
timer.c
```

```
/*  
* (C) Copyright 2003  
* Texas Instruments <www.ti.com>  
*  
* (C) Copyright 2002  
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>  
* Marius Groeger <mgroeger@sysgo.de>  
*  
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* Sysgo Real-Time Solutions, GmbH <www.elinos.com>  
* Alex Zuepke <azu@sysgo.de>  
*  
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm926ejs/orion5x/cpu.c
```

```
cpu.c
```

```
/*
 * Copyright (C) 2010 Albert ARIBAUD <albert.u.boot@aribaud.net>
 *
 * Based on original Kirkwood support which is
 * (C) Copyright 2009
 * Marvell Semiconductor <www.marvell.com>
 * Written-by: Prafulla Wadaskar <prafulla@marvell.com>
 *
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 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
 * MA 02110-1301 USA
*/
```

```
./arch/arm/cpu/arm926ejs/orion5x/dram.c
```

```
dram.c
```

```
/*
 * Copyright (C) 2010 Albert ARIBAUD <albert.u.boot@aribaud.net>
 *
 * Based on original Kirkwood support which is
 * (C) Copyright 2009
 * Marvell Semiconductor <www.marvell.com>
 * Written-by: Prafulla Wadaskar <prafulla@marvell.com>
 *
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* MA 02110-1301 USA
*/
```

```
./arch/arm/cpu/arm926ejs/orion5x/timer.c
timer.c
```

```
/*
*
* Based on original Kirkwood support which is
* Copyright (C) Marvell International Ltd. and its affiliates
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
*
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* MA 02110-1301 USA
*/
```

```
./arch/arm/cpu/arm926ejs/pantheon/cpu.c
cpu.c
```

```
/*
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* Marvell Semiconductor <www.marvell.com>
* Written-by: Lei Wen <leiwen@marvell.com>
*
* See file CREDITS for list of people who contributed to this
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```

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* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./arch/arm/cpu/arm926ejs/pantheon/dram.c
dram.c
```

```
/*
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* Marvell Semiconductor <www.marvell.com>
* Written-by: Lei Wen <leiw@marvell.com>,
*
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* MA 02110-1301 USA
*/
```

```
./arch/arm/cpu/arm926ejs/pantheon/timer.c
timer.c
```

```
/*
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* Written-by: Lei Wen <leiw@marvell.com>
*
*/
```

```
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*/
```

```
./arch/arm/cpu/arm926ejs/spear/cpu.c
```

```
cpu.c
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm926ejs/spear/reset.c
```

```
reset.c
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm926ejs/spear/spear600.c
spear600.c
```

```
/*
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*/
```

```
./arch/arm/cpu/arm926ejs/spear/spl.c
spl.c
```

```
/*
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* Heiko Schocher, DENX Software Engineering, hs@denx.de.
```

```
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*/
```

```
./arch/arm/cpu/arm926ejs/spear/spl_boot.c
```

```
spl_boot.c
```

```
/*
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/arm926ejs/spear/spr600_mt47h128m8_3_266_cl5_async.c
```

spr600_mt47h128m8_3_266_cl5_async.c

```
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 */
```

./arch/arm/cpu/arm926ejs/spear/spr600_mt47h32m16_333_cl5_psync.c

spr600_mt47h32m16_333_cl5_psync.c

```
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 */
```

./arch/arm/cpu/arm926ejs/spear/spr600_mt47h32m16_37e_166_cl4_sync.c

spr600_mt47h32m16_37e_166_cl4_sync.c

```
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 */
```

./arch/arm/cpu/arm926ejs/spear/spr600_mt47h64m16_3_333_cl5_psync.c
spr600_mt47h64m16_3_333_cl5_psync.c

```
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 */
```

./arch/arm/cpu/arm926ejs/spear/timer.c

timer.c

```
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 */
```

./arch/arm/cpu/arm926ejs/versatile/timer.c

timer.c

```
/*
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 * Sysgo Real-Time Solutions, GmbH <www.elinos.com>
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*/
```

```
./arch/arm/cpu/arm946es/cpu.c
```

```
cpu.c
```

```
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* Marius Groeger <mgroeger@sysgo.de>
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*/
```

```
./arch/arm/cpu/armv7/am33xx/board.c
```

```
board.c
```

```
/*
* board.c
*
* Common board functions for AM33XX based boards
*
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```

```

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*/

./arch/arm/cpu/armv7/am33xx/clock.c
clock.c
/*
* clock.c
*
* clocks for AM33XX based boards
*
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*/

./arch/arm/cpu/armv7/am33xx/ddr.c
ddr.c
/*
* DDR Configuration for AM33xx devices.
*
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```

./arch/arm/cpu/armv7/am33xx/elm.c

elm.c

```
/*
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 * Mansoor Ahamed <mansoor.ahamed@ti.com>
 *
 * BCH Error Location Module (ELM) support.
 *
 * NOTE:
 * 1. Supports only continuous mode. Dont see need for page mode in uboot
 * 2. Supports only syndrome polynomial 0. i.e. poly local variable is
 *    always set to ELM_DEFAULT_POLY. Dont see need for other polynomial
 *    sets in uboot
 *
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 * MA 02111-1307 USA
 */
```

./arch/arm/cpu/armv7/am33xx/emif4.c

emif4.c

```
/*
 * emif4.c
 *
 * AM33XX emif4 configuration file
 *
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*/

./arch/arm/cpu/armv7/am33xx/mem.c

mem.c

/*

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* Initial Code from:
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* Richard Woodruff <r-woodruff2@ti.com>
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*/

./arch/arm/cpu/armv7/am33xx/mux.c

mux.c

/*

* mux.c
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*/

```
./arch/arm/cpu/armv7/am33xx/sys_info.c
```

```
sys_info.c
```

```
/*  
 * sys_info.c  
 *  
 * System information functions  
 *  
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 *  
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 */
```

```
./arch/arm/cpu/armv7/cache_v7.c
```

```
cache_v7.c
```

```
/*  
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```

./arch/arm/cpu/armv7/cpu.c

cpu.c

/*

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* Marius Groeger <mgroeger@sysgo.de>

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*/

./arch/arm/cpu/armv7/exynos/clock.c

clock.c

/*

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* Minkyu Kang <mk7.kang@samsung.com>

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```

```
./arch/arm/cpu/armv7/exynos/pinmux.c
```

```
pinmux.c
```

```
/*
```

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```

```
./arch/arm/cpu/armv7/exynos/power.c
```

```
power.c
```

```
/*
```

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```

```
./arch/arm/cpu/armv7/exynos/soc.c
```

```
soc.c
```

```
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*/
```

```
./arch/arm/cpu/armv7/exynos/system.c
```

```
system.c
```

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```

```
./arch/arm/cpu/armv7/highbank/timer.c
```

```
timer.c
```

```
/*
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 *
 * Based on arm926ejs/mx27/timer.c
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*/
```

```
./arch/arm/cpu/armv7/mx5/asm-offsets.c
```

```
asm-offsets.c
```

```
/*
 * Adapted from Linux v2.6.36 kernel: arch/powerpc/kernel/asm-offsets.c
 *
 * This program is used to generate definitions needed by
 * assembly language modules.
 *
 * We use the technique used in the OSF Mach kernel code:
 * generate asm statements containing #defines,
 * compile this file to assembler, and then extract the
 * #defines from the assembly-language output.
 *
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 * 2 of the License, or (at your option) any later version.
*/
```

```
./arch/arm/cpu/armv7/mx5/clock.c
```

```
clock.c
```

```
/*
 * (C) Copyright 2007
```

```
* Sascha Hauer, Pengutronix
*
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/mx5/iomux.c
iomux.c
```

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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/mx5/soc.c
soc.c
/*
```

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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/mx6/clock.c
clock.c
```

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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/mx6/soc.c
soc.c
```

```

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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */

./arch/arm/cpu/armv7/omap-common/boot-common.c
boot-common.c
/*
 * boot-common.c
 *
 * Common bootmode functions for omap based boards
 *
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 *
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 */

./arch/arm/cpu/armv7/omap-common/clocks-common.c
clocks-common.c
/*
 *
 * Clock initialization for OMAP4

```

```
*
* (C) Copyright 2010
* Texas Instruments, <www.ti.com>
*
* Aneesh V <aneesh@ti.com>
*
* Based on previous work by:
*   Santosh Shilimkar <santosh.shilimkar@ti.com>
*   Rajendra Nayak <rnayak@ti.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
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*/
```

```
./arch/arm/cpu/armv7/omap-common/emif-common.c
```

```
emif-common.c
```

```
/*
* EMIF programming
*
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*
* Aneesh V <aneesh@ti.com>
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/omap-common/hwinit-common.c
hwinit-common.c
```

```
/*
*
* Common functions for OMAP4/5 based boards
*
* (C) Copyright 2010
* Texas Instruments, <www.ti.com>
*
* Author :
*     Aneesh V           <aneesh@ti.com>
*     Steve Sakoman     <steve@sakoman.com>
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*/
```

```
./arch/arm/cpu/armv7/omap-common/mem-common.c
mem-common.c
```

```
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*
* Steve Sakoman <steve@sakoman.com>
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/omap-common/reset.c
```

```
reset.c
```

```
/*
*
* Common layer for reset related functionality of OMAP based socs.
*
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* Texas Instruments, <www.ti.com>
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* Sricharan R <r.sricharan@ti.com>
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/omap-common/timer.c
```

```
timer.c
```

```
/*
* (C) Copyright 2008
* Texas Instruments
*
*/
```

```
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* Syed Moahmmed Khasim <khasim@ti.com>
*
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* Marius Groeger <mgroeger@sysgo.de>
* Alex Zuepke <azu@sysgo.de>
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/omap-common/utils.c
```

```
utils.c
```

```
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*/
```

```
./arch/arm/cpu/armv7/omap-common/vc.c
```

```
vc.c
```

```
/*
 * Voltage Controller implementation for OMAP
 *
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 * Nishanth Menon
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*/
```

```
./arch/arm/cpu/armv7/omap3/am35x_musb.c
```

```
am35x_musb.c
```

```
/*
 * This file configures the internal USB PHY in AM35X.
 *
 * Copyright (C) 2012 Ilya Yanok <ilya.yanok@gmail.com>
 *
 * Based on omap_phy_internal.c code from Linux by
 * Hema HK <hemahk@ti.com>
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*/
```

```
./arch/arm/cpu/armv7/omap3/board.c
```

board.c

```
/*
 *
 * Common board functions for OMAP3 based boards.
 *
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 *
 * Author :
 *     Sunil Kumar <sunilsaini05@gmail.com>
 *     Shashi Ranjan <shashiranjanmca05@gmail.com>
 *
 * Derived from Beagle Board and 3430 SDP code by
 *     Richard Woodruff <r-woodruff2@ti.com>
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
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 */
```

./arch/arm/cpu/armv7/omap3/clock.c

clock.c

```
/*
 *
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 *     Manikandan Pillai <mani.pillai@ti.com>
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 * Derived from Beagle Board and OMAP3 SDP code by
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 *     Syed Mohammed Khasim <khasim@ti.com>
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/omap3/emac.c
```

```
emac.c
```

```
/*
*
* DaVinci EMAC initialization.
*
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```

```
./arch/arm/cpu/armv7/omap3/emif4.c
```

```
emif4.c
```

```
/*
* Author :
*   Vaibhav Hiremath <hvaibhav@ti.com>
*
* Based on mem.c and sdrc.c
*
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```

```
./arch/arm/cpu/armv7/omap3/mem.c
```

```
mem.c
```

```
/*
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* Texas Instruments, <www.ti.com>
*
* Author :
*   Manikandan Pillai <mani.pillai@ti.com>
*
* Initial Code from:
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*   Syed Mohammed Khasim <khasim@ti.com>
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/omap3/sdrc.c
```

```
sdrc.c
```

```
/*
```

```
* Functions related to OMAP3 SDRC.
*
* This file has been created after extracting and consolidating
* the SDRC related content from mem.c and board.c, also created
* generic init function (mem_init).
*
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* Corscience GmbH & Co. KG - Simon Schwarz <schwarz@corscience.de>
*
* Author :
*   Vaibhav Hiremath <hvaibhav@ti.com>
*
* Original implementation by (mem.c, board.c) :
*   Sunil Kumar <sunilsaini05@gmail.com>
*   Shashi Ranjan <shashiranjnca05@gmail.com>
*   Manikandan Pillai <mani.pillai@ti.com>
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*/
```

```
./arch/arm/cpu/armv7/omap3/spl_id_nand.c
```

```
spl_id_nand.c
```

```
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*
* Author :
*   Tom Rini <trini@ti.com>
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* Initial Code from:
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/omap3/sys_info.c
sys_info.c
```

```
/*
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*
* Author :
*   Manikandan Pillai <mani.pillai@ti.com>
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/omap4/clocks.c
clocks.c
```

```
/*
*
* Clock initialization for OMAP4
*
*/
```

```
* (C) Copyright 2010
* Texas Instruments, <www.ti.com>
*
* Aneesh V <aneesh@ti.com>
*
* Based on previous work by:
*     Santosh Shilimkar <santosh.shilimkar@ti.com>
*     Rajendra Nayak <rnayak@ti.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/omap4/emif.c
emif.c
```

```
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* EMIF programming
*
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* Aneesh V <aneesh@ti.com>
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/omap4/hwinit.c
```

```
hwinit.c
```

```
/*
*
* Common functions for OMAP4 based boards
*
* (C) Copyright 2010
* Texas Instruments, <www.ti.com>
*
* Author :
*     Aneesh V      <aneesh@ti.com>
*     Steve Sakoman <steve@sakoman.com>
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* project.
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/omap4/sdram_elpida.c
```

```
sdram_elpida.c
```

```
/*
*
* Timing and Organization details of the Elpida parts used in OMAP4
* SDPs and Panda
*
* (C) Copyright 2010
* Texas Instruments, <www.ti.com>
*
* Aneesh V <aneesh@ti.com>
*
*/
```

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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/omap5/clocks.c
clocks.c
```

```
/*
*
* Clock initialization for OMAP5
*
* (C) Copyright 2010
* Texas Instruments, <www.ti.com>
*
* Aneesh V <aneesh@ti.com>
* Sricharan R <r.sricharan@ti.com>
*
* Based on previous work by:
*     Santosh Shilimkar <santosh.shilimkar@ti.com>
*     Rajendra Nayak <rnayak@ti.com>
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```

```
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* MA 02111-1307 USA  
*/
```

```
./arch/arm/cpu/armv7/omap5/emif.c  
emif.c
```

```
/*  
 * EMIF programming  
 *  
 * (C) Copyright 2010  
 * Texas Instruments, <www.ti.com>  
 *  
 * Aneesh V <aneesh@ti.com> for OMAP4  
 *  
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
 * MA 02111-1307 USA  
 */
```

```
./arch/arm/cpu/armv7/omap5/hwinit.c  
hwinit.c
```

```
/*  
 *  
 * Functions for omap5 based boards.  
 *  
 * (C) Copyright 2011  
 * Texas Instruments, <www.ti.com>  
 *  
 * Author :  
 *     Aneesh V           <aneesh@ti.com>  
 *     Steve Sakoman     <steve@sakoman.com>  
 *     Sricharan         <r.sricharan@ti.com>  
 *  
 * See file CREDITS for list of people who contributed to this  
 * project.  
 */
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/omap5/sdram.c
sdram.c
```

```
/*
* Timing and Organization details of the ddr device parts used in OMAP5
* EVM
*
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* Texas Instruments, <www.ti.com>
*
* Aneesh V <aneesh@ti.com>
* Sricharan R <r.sricharan@ti.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/rmobile/board.c
board.c
```

```
/*
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/rmobile/cpu_info-r8a7740.c
cpu_info-r8a7740.c
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/rmobile/cpu_info-sh73a0.c
cpu_info-sh73a0.c
/*
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/rmobile/cpu_info.c
```

```
cpu_info.c
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/rmobile/emacs.c
```

```
emacs.c
```

```
/*
```

```
* RMOBILE EtherMAC initialization.
*
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* Foundation, Inc.
*/
```

```
./arch/arm/cpu/armv7/rmobile/pfc-r8a7740.c
pfc-r8a7740.c
```

```
/*
 * R8A7740 processor support
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 51 Franklin St, Fifth Floor, Boston, MA 02110-1301 USA
 */
```

```
./arch/arm/cpu/armv7/rmobile/pfc-sh73a0.c
pfc-sh73a0.c
```

```
/*
 * sh73a0 processor support - PFC hardware block
```

```
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*/
```

```
./arch/arm/cpu/armv7/rmobile/timer.c
```

```
timer.c
```

```
/*
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/s5p-common/cpu_info.c
```

```
cpu_info.c
```

```
/*
* Copyright (C) 2009 Samsung Electronics
* Minkyu Kang <mk7.kang@samsung.com>
*
*/
```

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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/s5p-common/pwm.c
```

```
pwm.c
```

```
/*
* Copyright (C) 2011 Samsung Electronics
*
* Donghwa Lee <dh09.lee@samsung.com>
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*/
```

```
./arch/arm/cpu/armv7/s5p-common/sromc.c
```

```
sromc.c
```

```
/*
* Copyright (C) 2010 Samsung Electronics
* Naveen Krishna Ch <ch.naveen@samsung.com>
```

```
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*/
```

```
./arch/arm/cpu/armv7/s5p-common/timer.c
timer.c
```

```
/*
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* Inki Dae <inki.dae@samsung.com>
* Minkyu Kang <mk7.kang@samsung.com>
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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/s5pc1xx/clock.c
clock.c
```

```
/*
```

```
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* Minkyu Kang <mk7.kang@samsung.com>
* Heungjun Kim <riverful.kim@samsung.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/armv7/socfpga/altlimits.h
altlimits.h
```

```
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* this program. If not, see <http://www.gnu.org/licenses/>.
*
* This is an abstract header file for best portability. uBoot does not
* have the equivalent of a limits.h file (Linux has kernel.h), and
* some recently added code requires range checks on computation results.
*
* This code first checks for the existence of the macro before defining it
* in the event limits.h or the equivalent is ever added in the future.
* This file can be removed if that ever occurs.
*/
```

```
./arch/arm/cpu/armv7/socfpga/asm-offsets.s
```

asm-offsets.s

No license information found.

./arch/arm/cpu/armv7/socfpga/clock_manager.c

clock_manager.c

```
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 */
```

./arch/arm/cpu/armv7/socfpga/debug_memory.c

debug_memory.c

```
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```

./arch/arm/cpu/armv7/socfpga/ecc_ram.c

ecc_ram.c

```
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./arch/arm/cpu/armv7/socfpga/fpga_manager.c
fpga_manager.c
/*
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*/

./arch/arm/cpu/armv7/socfpga/freeze_controller.c
freeze_controller.c
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```

```
./arch/arm/cpu/armv7/socfpga/gic.c  
gic.c
```

```
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*/
```

```
./arch/arm/cpu/armv7/socfpga/interrupts.c  
interrupts.c
```

```
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*/
```

```
./arch/arm/cpu/armv7/socfpga/misc.c  
misc.c
```

```
/*
```

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```

```
./arch/arm/cpu/armv7/socfpga/nic301.c
nic301.c
```

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*/
```

```
./arch/arm/cpu/armv7/socfpga/reset_manager.c
reset_manager.c
```

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*/
```

```
./arch/arm/cpu/armv7/socfpga/scan_manager.c
```

```
scan_manager.c
```

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*/
```

```
./arch/arm/cpu/armv7/socfpga/sdram.c
```

```
sdram.c
```

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```

```
./arch/arm/cpu/armv7/socfpga/sdram_test.c
```

```
sdram_test.c
```

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*/
```

```
./arch/arm/cpu/armv7/socfpga/spl.c
```

```
spl.c
```

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*/
```

```
./arch/arm/cpu/armv7/socfpga/system_manager.c
```

```
system_manager.c
```

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*/
```

```
./arch/arm/cpu/armv7/socfpga/s_init.c
```

```
s_init.c
```

```
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 */
```

```
./arch/arm/cpu/armv7/socfpga/timer.c
timer.c
```

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 */
```

```
./arch/arm/cpu/armv7/syslib.c
syslib.c
```

```
/*
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 *
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* MA 02111-1307 USA  
*/
```

```
./arch/arm/cpu/armv7/tegra-common/cmd_enterrcm.c  
cmd_enterrcm.c
```

```
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```

```
./arch/arm/cpu/armv7/tegra20/display.c
display.c
```

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*/
```

```
./arch/arm/cpu/armv7/tegra20/pwm.c
pwm.c
```

```
/*
 * Tegra2 pulse width frequency modulator definitions
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*/
```

```
./arch/arm/cpu/armv7/tegra20/usb.c  
usb.c
```

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*/
```

```
./arch/arm/cpu/armv7/u8500/clock.c  
clock.c
```

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```

* MA 02111-1307 USA
*/

./arch/arm/cpu/armv7/u8500/cpu.c

cpu.c

/*

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./arch/arm/cpu/armv7/u8500/prcmu.c

prcmu.c

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*/
```

```
./arch/arm/cpu/armv7/u8500/timer.c
timer.c
```

```
/*
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 *
 * Based on original from Linux kernel source and
 * internal ST-Ericsson U-Boot source.
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 */
```

```
./arch/arm/cpu/armv7/zynq/cpu.c
cpu.c
```

```
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* MA 02111-1307 USA
*/

./arch/arm/cpu/armv7/zynq/timer.c
timer.c

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```

```
./arch/arm/cpu/arm_intcm/cpu.c
```

```
cpu.c
```

```
/*
```

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```
./arch/arm/cpu/ixp/cpu.c
```

```
cpu.c
```

```
/*
```

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* MA 02111-1307 USA
*/
```

```
./arch/arm/cpu/ixp/interrupts.c
interrupts.c
```

```
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*/
```

```
./arch/arm/cpu/ixp/timer.c
```

timer.c

```
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 *
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 * MA 02111-1307 USA
 */
```

./arch/arm/cpu/pxa/cpuinfo.c

cpuinfo.c

```
/*
 * PXA CPU information display
 *
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```

```
./arch/arm/cpu/pxa/pxa2xx.c
```

```
pxa2xx.c
```

```
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*/
```

```
./arch/arm/cpu/pxa/pxafb.c
```

```
pxafb.c
```

```
/*
* PXA LCD Controller
*
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*/
```

```
./arch/arm/cpu/pxa/timer.c
timer.c
```

```
/*
* Marvell PXA2xx/3xx timer driver
*
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```

```
./arch/arm/cpu/pxa/usb.c
usb.c
```

```
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```
./arch/arm/cpu/s3c44b0/cache.c
```

```
cache.c
```

```
/*
```

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```

```
./arch/arm/cpu/s3c44b0/cpu.c
```

```
cpu.c
```

```
/*
```

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```

```
./arch/arm/cpu/s3c44b0/timer.c
```

```
timer.c
```

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```

./arch/arm/cpu/sa1100/cpu.c

cpu.c

/*

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* Marius Groeger <mgroeger@sysgo.de>

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*/

./arch/arm/cpu/sa1100/timer.c

timer.c

/*

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```

```
./arch/arm/cpu/tegra-common/ap.c
```

```
ap.c
```

```
/*
```

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```
*/
```

```
./arch/arm/cpu/tegra-common/board.c
```

```
board.c
```

```
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```

```
./arch/arm/cpu/tegra-common/sys_info.c
```

```
sys_info.c
```

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```

```
./arch/arm/cpu/tegra-common/timer.c
```

```
timer.c
```

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```

```
./arch/arm/cpu/tegra20-common/clock.c
```

```
clock.c
```

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```

```
./arch/arm/cpu/tegra20-common/crypto.c
```

```
crypto.c
```

```
/*
```

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```

```
./arch/arm/cpu/tegra20-common/crypto.h
```

```
crypto.h
```

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```

```
./arch/arm/cpu/tegra20-common/emc.c
```

```
emc.c
```

```
/*
```

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```
./arch/arm/cpu/tegra20-common/funcmux.c
```

```
funcmux.c
```

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```
./arch/arm/cpu/tegra20-common/pinmux.c
```

```
pinmux.c
```

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```
./arch/arm/cpu/tegra20-common/pmu.c
```

```
pmu.c
```

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```

```
./arch/arm/cpu/tegra20-common/warmboot.c
```

```
warmboot.c
```

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```
./arch/arm/cpu/tegra20-common/warmboot_avp.c
warmboot_avp.c
```

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```
./arch/arm/cpu/tegra20-common/warmboot_avp.h
warmboot_avp.h
```

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```
./arch/arm/imx-common/cmd_bmode.c
cmd_bmode.c
```

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```

```
./arch/arm/imx-common/cpu.c
cpu.c
```

```
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```
./arch/arm/imx-common/i2c-mxv7.c
```

```
i2c-mxv7.c
```

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```

```
./arch/arm/imx-common/iomux-v3.c
```

```
iomux-v3.c
```

```
/*  
* Based on the iomux-v3.c from Linux kernel:  
* Copyright (C) 2008 by Sascha Hauer <kernel@pengutronix.de>  
* Copyright (C) 2009 by Jan Weitzel Phytex Messtechnik GmbH,  
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```
./arch/arm/imx-common/speed.c
speed.c
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/arm/imx-common/timer.c
timer.c
```

```
/*
* (C) Copyright 2007
* Sascha Hauer, Pengutronix
```

```
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-a320/a320.h
a320.h
```

```
/*
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* Po-Yu Chuang <ratbert@faraday-tech.com>
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./arch/arm/include/asm/arch-am33xx/clock.h
clock.h
```

```
/*
* clock.h
*
* clock header
*/
```

```
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*/
```

```
./arch/arm/include/asm/arch-am33xx/clocks_am33xx.h
clocks_am33xx.h
```

```
/*
 * clocks_am33xx.h
 *
 * AM33xx clock define
 *
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*/
```

```
./arch/arm/include/asm/arch-am33xx/cpu.h
cpu.h
```

```
/*
 * cpu.h
 *
 * AM33xx specific header file
 *
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```

```
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*/
```

```
./arch/arm/include/asm/arch-am33xx/ddr_defs.h
```

```
ddr_defs.h
```

```
/*  
 * ddr_defs.h  
 *  
 * ddr specific header  
 *  
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 */
```

```
./arch/arm/include/asm/arch-am33xx/elm.h
```

```
elm.h
```

```
/*  
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 * Mansoor Ahamed <mansoor.ahamed@ti.com>  
 *  
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
 * MA 02111-1307 USA  
 */
```

```
./arch/arm/include/asm/arch-am33xx/gpio.h
```

```
gpio.h
```

```
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
 * MA 02111-1307 USA  
 *  
 */
```

```
./arch/arm/include/asm/arch-am33xx/hardware.h
```

```
hardware.h
```

```
/*  
 * hardware.h  
 *  
 * hardware specific header  
 *  
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 */
```

```
./arch/arm/include/asm/arch-am33xx/i2c.h
```

```
i2c.h
```

```
/*  
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 *
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-am33xx/mem.h
```

```
mem.h
```

```
/*
* (C) Copyright 2006-2008
* Texas Instruments, <www.ti.com>
*
* Author
*         Mansoor Ahamed <mansoor.ahamed@ti.com>
*
* Initial Code from:
*         Richard Woodruff <r-woodruff2@ti.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-am33xx/mmc_host_def.h
```

```
mmc_host_def.h
```

```
/*
```

```
* mmc_host_def.h
*
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*/
```

```
./arch/arm/include/asm/arch-am33xx/mux.h
mux.h
```

```
/*
* mux.h
*
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*/
```

```
./arch/arm/include/asm/arch-am33xx/omap.h
omap.h
```

```
/*
* omap.h
*
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*
* Author:
*     Chandan Nath <chandan.nath@ti.com>
*
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*/
```

```
./arch/arm/include/asm/arch-am33xx/omap_gpmc.h
```

```
omap_gpmc.h
```

```
/*
```

```
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```

```
* Rohit Choraria <rohitkc@ti.com>
```

```
*
```

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* project.
```

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```

```
*/
```

```
./arch/arm/include/asm/arch-am33xx/spl.h
```

```
spl.h
```

```
/*
```

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*/
```

```
./arch/arm/include/asm/arch-am33xx/sys_proto.h
sys_proto.h
```

```
/*
 * sys_proto.h
 *
 * System information header
 *
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 */
```

```
./arch/arm/include/asm/arch-arm720t/hardware.h
hardware.h
```

```
/*
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 * Curt Brune <curt@cuce.com>
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/arm/include/asm/arch-armada100/armada100.h

armada100.h

```
/*
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 * Written-by: Prafulla Wadaskar <prafulla@marvell.com>
 * Contributor: Mahavir Jain <mjain@marvell.com>
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 * MA 02110-1301 USA
 */
```

./arch/arm/include/asm/arch-armada100/config.h

config.h

```
/*
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 * Written-by: Lei Wen <leiwen@marvell.com>
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```

```
* MA 02110-1301 USA
*/

./arch/arm/include/asm/arch-armada100/cpu.h
cpu.h
/*
 * (C) Copyright 2010
 * Marvell Semiconductor <www.marvell.com>
 * Written-by: Prafulla Wadaskar <prafulla@marvell.com>, Contributor: Mahavir Jain
 <mjain@marvell.com>
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 * MA 02110-1301 USA
 */

./arch/arm/include/asm/arch-armada100/gpio.h
gpio.h
/*
 * (C) Copyright 2011
 * eInfochips Ltd. <www.einfochips.com>
 * Written-by: Ajay Bhargav <ajay.bhargav@einfochips.com>
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* MA 02110-1301 USA
*/
```

```
./arch/arm/include/asm/arch-armada100/mfp.h
mfp.h
```

```
/*
* Based on linux/arch/arm/mach-mpp/include/mfp-pxa168.h
* (C) Copyright 2007
* Marvell Semiconductor <www.marvell.com>
* 2007-08-21: eric miao <eric.miao@marvell.com>
*
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* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
* Contributor: Mahavir Jain <mjain@marvell.com>
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* MA 02110-1301 USA
*/
```

```
./arch/arm/include/asm/arch-armada100/spi.h
spi.h
```

```
/*
* (C) Copyright 2011
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* Written-by: Ajay Bhargav <ajay.bhargav@einfochips.com>
*
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* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./arch/arm/include/asm/arch-armada100/utmi-armada100.h
utmi-armada100.h
```

```
/*
* (C) Copyright 2012
* eInfochips Ltd. <www.einfochips.com>
* Written-by: Ajay Bhargav <ajay.bhargav@einfochips.com>
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* MA 02110-1301 USA
*/
```

```
./arch/arm/include/asm/arch-armv7/globaltimer.h
```

globaltimer.h

```
/*
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 * (C) Copyright 2012 Renesas Solutions Corp.
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/arm/include/asm/arch-armv7/sysctrl.h

sysctrl.h

```
/*
 * (C) Copyright 2010 Linaro
 * Matt Waddel, <matt.waddel@linaro.org>
 *
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 * project.
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/arm/include/asm/arch-armv7/systimer.h

systemer.h

```
/*
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/arm/include/asm/arch-armv7/wdt.h

wdt.h

```
/*
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 *
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 * project.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/arm/include/asm/arch-at91/at91cap9.h

at91cap9.h

```
/*
 * [origin: Linux kernel include/asm-arm/arch-at91/at91cap9.h]
 *
 * Copyright (C) 2007 Stelian Pop <stelian@popies.net>
 * Copyright (C) 2007 Lead Tech Design <www.leadtechdesign.com>
 * Copyright (C) 2007 Atmel Corporation.
 *
 * Common definitions.
 * Based on AT91CAP9 datasheet revision B (Preliminary).
 *
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 * it under the terms of the GNU General Public License as published by
 * the Free Software Foundation; either version 2 of the License, or
 * (at your option) any later version.
 */
```

./arch/arm/include/asm/arch-at91/at91cap9_matrix.h

at91cap9_matrix.h

```
/*
 * [origin: Linux kernel include/asm-arm/arch-at91/at91cap9_matrix.h]
 *
 * Copyright (C) 2007 Stelian Pop <stelian@popies.net>
 * Copyright (C) 2007 Lead Tech Design <www.leadtechdesign.com>
 * Copyright (C) 2006 Atmel Corporation.
 *
 * Memory Controllers (MATRIX, EBI) - System peripherals registers.
 * Based on AT91CAP9 datasheet revision B (Preliminary).
 *
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 * it under the terms of the GNU General Public License as published by
 * the Free Software Foundation; either version 2 of the License, or
 * (at your option) any later version.
 */
```

./arch/arm/include/asm/arch-at91/at91rm9200.h

at91rm9200.h

```
/*
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 * modify it under the terms of the GNU General Public License as
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 */
```

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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-at91/at91sam9260.h
at91sam9260.h
```

```
/*
* [origin: Linux kernel include/asm-arm/arch-at91/at91sam9260.h]
*
* (C) 2006 Andrew Victor
* (C) Copyright 2010
* Reinhard Meyer, EMK Elektronik, reinhard.meyer@emk-elektronik.de
*
* Definitions for the SoCs:
* AT91SAM9260, AT91SAM9G20, AT91SAM9XE
*
* Note that those SoCs are mostly software and pin compatible,
* therefore this file applies to all of them. Differences between
* those SoCs are concentrated at the end of this file.
*
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* it under the terms of the GNU General Public License as published by
* the Free Software Foundation; either version 2 of the License, or
* (at your option) any later version.
*/
```

```
./arch/arm/include/asm/arch-at91/at91sam9260_matrix.h
at91sam9260_matrix.h
```

```
/*
* [origin: Linux kernel include/asm-arm/arch-at91/at91sam9260_matrix.h]
*
* Copyright (C) 2007 Atmel Corporation.
*
* Memory Controllers (MATRIX, EBI) - System peripherals registers.
* Based on AT91SAM9260 datasheet revision B.
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* it under the terms of the GNU General Public License as published by
* the Free Software Foundation; either version 2 of the License, or
* (at your option) any later version.
*/
```

```
./arch/arm/include/asm/arch-at91/at91sam9261.h
at91sam9261.h
```

```
/*
* [origin: Linux kernel include/asm-arm/arch-at91/at91sam9261.h]
*
```

```
* Copyright (C) SAN People
* (C) Copyright 2010
* Reinhard Meyer, EMK Elektronik, reinhard.meyer@emk-elektronik.de
*
* Definitions for the SoCs:
* AT91SAM9261, AT91SAM9G10
*
* Note that those SoCs are mostly software and pin compatible,
* therefore this file applies to all of them. Differences between
* those SoCs are concentrated at the end of this file.
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*/
```

```
./arch/arm/include/asm/arch-at91/at91sam9261_matrix.h
at91sam9261_matrix.h
```

```
/*
 * [origin: Linux kernel include/asm-arm/arch-at91/at91sam9261_matrix.h]
 *
 * Copyright (C) 2007 Atmel Corporation.
 *
 * Memory Controllers (MATRIX, EBI) - System peripherals registers.
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 * (at your option) any later version.
*/
```

```
./arch/arm/include/asm/arch-at91/at91sam9263.h
at91sam9263.h
```

```
/*
 * [origin: Linux kernel include/asm-arm/arch-at91/at91sam9263.h]
 *
 * (C) 2007 Atmel Corporation.
 * (C) Copyright 2010
 * Reinhard Meyer, EMK Elektronik, reinhard.meyer@emk-elektronik.de
 *
 * Definitions for the SoC:
 * AT91SAM9263
 *
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 * (at your option) any later version.
*/
```

```
./arch/arm/include/asm/arch-at91/at91sam9263_matrix.h
at91sam9263_matrix.h
```

```
/*
 * [origin: Linux kernel include/asm-arm/arch-at91/at91sam9263_matrix.h]
 *
 * Copyright (C) 2006 Atmel Corporation.
 *
 * Memory Controllers (MATRIX, EBI) - System peripherals registers.
 * Based on AT91SAM9263 datasheet revision B (Preliminary).
 *
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 * (at your option) any later version.
 */
```

```
./arch/arm/include/asm/arch-at91/at91sam9g45.h
at91sam9g45.h
```

```
/*
 * Chip-specific header file for the AT91SAM9M1x family
 *
 * (C) 2008 Atmel Corporation.
 *
 * Definitions for the SoC:
 * AT91SAM9G45
 *
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 * it under the terms of the GNU General Public License as published by
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 * (at your option) any later version.
 */
```

```
./arch/arm/include/asm/arch-at91/at91sam9g45_matrix.h
at91sam9g45_matrix.h
```

```
/*
 * Matrix-centric header file for the AT91SAM9M1x family
 *
 * Copyright (C) 2008 Atmel Corporation.
 *
 * Memory Controllers (MATRIX, EBI) - System peripherals registers.
 * Based on AT91SAM9G45 preliminary datasheet.
 *
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 * the Free Software Foundation; either version 2 of the License, or
 * (at your option) any later version.
 */
```

```
./arch/arm/include/asm/arch-at91/at91sam9r1.h
```

at91sam9rl.h

```
/*
 * [origin: Linux kernel include/asm-arm/arch-at91/at91sam9rl.h]
 *
 * Copyright (C) 2007 Atmel Corporation
 *
 * Common definitions.
 * Based on AT91SAM9RL datasheet revision A. (Preliminary)
 *
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 * more details.
 */
```

./arch/arm/include/asm/arch-at91/at91sam9rl_matrix.h

at91sam9rl_matrix.h

```
/*
 * [origin: Linux kernel include/asm-arm/arch-at91/at91sam9rl_matrix.h]
 *
 * Copyright (C) 2007 Atmel Corporation
 *
 * Memory Controllers (MATRIX, EBI) - System peripherals registers.
 * Based on AT91SAM9RL datasheet revision A. (Preliminary)
 *
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 * more details.
 */
```

./arch/arm/include/asm/arch-at91/at91sam9x5.h

at91sam9x5.h

```
/*
 * Chip-specific header file for the AT91SAM9x5 family
 *
 * Copyright (C) 2012 Atmel Corporation.
 *
 * Definitions for the SoC:
 * AT91SAM9x5
 *
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 * it under the terms of the GNU General Public License as published by
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 * (at your option) any later version.
 */
```

./arch/arm/include/asm/arch-at91/at91sam9x5_matrix.h

at91sam9x5_matrix.h

```
/*
 * Matrix-centric header file for the AT91SAM9X5 family
 *
```

```

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*
* Memory Controllers (MATRIX, EBI) - System peripherals registers.
* Based on AT91SAM9X5 preliminary datasheet.
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* (at your option) any later version.
*/

./arch/arm/include/asm/arch-at91/at91sam9_matrix.h
at91sam9_matrix.h
/*
* Copyright (C) 2009 Jean-Christophe PLAGNIOL-VILLARD <plagnioj@jrosoft.com>
*
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* it under the terms of the GNU General Public License as published by
* the Free Software Foundation; either version 2 of the License, or
* (at your option) any later version.
*
*/

./arch/arm/include/asm/arch-at91/at91sam9_sdramc.h
at91sam9_sdramc.h
/*
* [origin: Linux kernel arch/arm/mach-at91/include/mach/at91_wdt.h]
*
* Copyright (C) 2009 Jean-Christophe PLAGNIOL-VILLARD <plagnioj@jrosoft.com>
* Copyright (C) 2007 Andrew Victor
* Copyright (C) 2007 Atmel Corporation.
*
* SDRAM Controllers (SDRAMC) - System peripherals registers.
* Based on AT91SAM9261 datasheet revision D.
*
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* it under the terms of the GNU General Public License as published by
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* (at your option) any later version.
*/

./arch/arm/include/asm/arch-at91/at91sam9_smc.h
at91sam9_smc.h
/*
* [origin: Linux kernel include/asm-arm/arch-at91/at91sam9_smc.h]
*
* Copyright (C) 2007 Andrew Victor
* Copyright (C) 2007 Atmel Corporation.
*
* Static Memory Controllers (SMC) - System peripherals registers.

```

```
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* the Free Software Foundation; either version 2 of the License, or  
* (at your option) any later version.  
*/
```

```
./arch/arm/include/asm/arch-at91/at91_common.h  
at91_common.h
```

```
/*  
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* Stelian Pop <stelian@popies.net>  
* Lead Tech Design <www.leadtechdesign.com>  
*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/arm/include/asm/arch-at91/at91_dbu.h  
at91_dbu.h
```

```
/*  
* Copyright (C) 2010  
* Reinhard Meyer, reinhard.meyer@emk-elektronik.de  
*  
* Debug Unit  
* Based on AT91SAM9XE datasheet  
*  
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* it under the terms of the GNU General Public License as published by  
* the Free Software Foundation; either version 2 of the License, or  
* (at your option) any later version.  
*/
```

```
./arch/arm/include/asm/arch-at91/at91_eefc.h
```

at91_eefc.h

```
/*
 * Copyright (C) 2010
 * Reinhard Meyer, reinhard.meyer@emk-elektronik.de
 *
 * Enhanced Embedded Flash Controller
 * Based on AT91SAM9XE datasheet
 *
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 * it under the terms of the GNU General Public License as published by
 * the Free Software Foundation; either version 2 of the License, or
 * (at your option) any later version.
 */
```

./arch/arm/include/asm/arch-at91/at91_emac.h

at91_emac.h

```
/*
 * Copyright (C) 2009 Jens Scharsig (js_at_ng@scharsoft.de)
 *
 * based on AT91RM9200 datasheet revision I (36. Ethernet MAC (EMAC))
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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 * MA 02111-1307 USA
 */
```

./arch/arm/include/asm/arch-at91/at91_gpbr.h

at91_gpbr.h

```
/*
 * Copyright (C) 2010
 * Reinhard Meyer, reinhard.meyer@emk-elektronik.de
 *
 * General Purpose Backup Registers
 * Based on AT91SAM9XE datasheet
 *
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```

```
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* (at your option) any later version.
*/
```

```
./arch/arm/include/asm/arch-at91/at91_matrix.h
```

```
at91_matrix.h
```

```
/*
 * Copyright (C) 2009 Jens Scharsig (js_at_ng@scharsoft.de)
 *
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 * project.
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-at91/at91_mc.h
```

```
at91_mc.h
```

```
/*
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 *
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 * project.
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
```

```
* MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-at91/at91_pdc.h
```

```
at91_pdc.h
```

```
/*
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 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-at91/at91_pio.h
```

```
at91_pio.h
```

```
/*
 * [origin: Linux kernel include/asm-arm/arch-at91/at91_pio.h]
 *
 * Copyright (C) 2005 Ivan Kokshaysky
 * Copyright (C) SAN People
 * Copyright (C) 2009 Jens Scharsig (js_at_ng@scharsoft.de)
 *
 * Parallel I/O Controller (PIO) - System peripherals registers.
 * Based on AT91RM9200 datasheet revision E.
 *
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 * it under the terms of the GNU General Public License as published by
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*/
```

```
./arch/arm/include/asm/arch-at91/at91_pit.h
```

```
at91_pit.h
```

```
/*
 * [origin: Linux kernel include/asm-arm/arch-at91/at91_pit.h]
 *
```

```
* Copyright (C) 2007 Andrew Victor
* Copyright (C) 2007 Atmel Corporation.
*
* Periodic Interval Timer (PIT) - System peripherals registers.
* Based on AT91SAM9261 datasheet revision D.
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* (at your option) any later version.
*/
```

```
./arch/arm/include/asm/arch-at91/at91_pmc.h
at91_pmc.h
```

```
/*
 * [origin: Linux kernel arch/arm/mach-at91/include/mach/at91_pmc.h]
 *
 * Copyright (C) 2005 Ivan Kokshaysky
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 * Copyright (C) 2009 Jens Scharsig (js_at_ng@scharsoft.de)
 *
 * Power Management Controller (PMC) - System peripherals registers.
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*/
```

```
./arch/arm/include/asm/arch-at91/at91_rstc.h
at91_rstc.h
```

```
/*
 * [origin: Linux kernel include/asm-arm/arch-at91/at91_rstc.h]
 *
 * Copyright (C) 2007 Andrew Victor
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 *
 * Reset Controller (RSTC) - System peripherals registers.
 * Based on AT91SAM9261 datasheet revision D.
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*/
```

```
./arch/arm/include/asm/arch-at91/at91_rtt.h
at91_rtt.h
```

```
/*
```

```
* Copyright (C) 2010
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*
* Real-time Timer
* Based on AT91SAM9XE datasheet
*
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* (at your option) any later version.
*/
```

```
./arch/arm/include/asm/arch-at91/at91_shdwn.h
at91_shdwn.h
```

```
/*
* Copyright (C) 2010
* Reinhard Meyer, reinhard.meyer@emk-elektronik.de
*
* Shutdown Controller
* Based on AT91SAM9XE datasheet
*
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*/
```

```
./arch/arm/include/asm/arch-at91/at91_spi.h
at91_spi.h
```

```
/*
* [origin: Linux kernel include/asm-arm/arch-at91/at91_spi.h]
*
* Copyright (C) 2005 Ivan Kokshaysky
* Copyright (C) SAN People
*
* Serial Peripheral Interface (SPI) registers.
* Based on AT91RM9200 datasheet revision E.
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*/
```

```
./arch/arm/include/asm/arch-at91/at91_st.h
at91_st.h
```

```
/*
* Copyright (C) 2009 Jens Scharsig (js_at_ng@scharsoft.de)
*
* See file CREDITS for list of people who contributed to this
```

```
* project.  
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* MA 02111-1307 USA  
*/
```

```
./arch/arm/include/asm/arch-at91/at91_tc.h  
at91_tc.h
```

```
/*  
* Copyright (C) 2009 Jens Scharsig (js_at_ng@scharsoft.de)  
*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/arm/include/asm/arch-at91/at91_wdt.h  
at91_wdt.h
```

```
/*  
* [origin: Linux kernel arch/arm/mach-at91/include/mach/at91_wdt.h]  
*  
* Copyright (C) 2008 Jean-Christophe PLAGNIOL-VILLARD <plagnioj@jcrosoft.com>  
* Copyright (C) 2007 Andrew Victor  
* Copyright (C) 2007 Atmel Corporation.
```

```

*
* Watchdog Timer (WDT) - System peripherals registers.
* Based on AT91SAM9261 datasheet revision D.
*
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* it under the terms of the GNU General Public License as published by
* the Free Software Foundation; either version 2 of the License, or
* (at your option) any later version.
*/

./arch/arm/include/asm/arch-at91/clk.h
clk.h
/*
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* MA 02111-1307 USA
*/

./arch/arm/include/asm/arch-at91/gpio.h
gpio.h
/*
* [origin: Linux kernel include/asm-arm/arch-at91/gpio.h]
*
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*
*/

```

```
./arch/arm/include/asm/arch-at91/hardware.h
hardware.h
```

```
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 */
```

```
./arch/arm/include/asm/arch-bcm2835/gpio.h
gpio.h
```

```
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 * <vikram186@gmail.com>
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 */
```

```
./arch/arm/include/asm/arch-bcm2835/timer.h
timer.h
```

```
/*
```

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*/
```

```
./arch/arm/include/asm/arch-bcm2835/wdog.h
wdog.h
```

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*/
```

```
./arch/arm/include/asm/arch-davinci/aintc_defs.h
aintc_defs.h
```

```
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* MA 02111-1307 USA  
*/
```

```
./arch/arm/include/asm/arch-davinci/da850_lowlevel.h  
da850_lowlevel.h
```

```
/*  
* SoC-specific lowlevel code for DA850  
*  
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*/
```

```
./arch/arm/include/asm/arch-davinci/da8xx-fb.h  
da8xx-fb.h
```

```
/*  
* Porting to u-boot:  
*  
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* Stefano Babic, DENX Software Engineering, sbabic@denx.de.  
*  
* Copyright (C) 2008-2009 MontaVista Software Inc.  
* Copyright (C) 2008-2009 Texas Instruments Inc  
*  
* Based on the LCD driver for TI Avalanche processors written by  
* Ajay Singh and Shalom Hai.  
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*/
```

```
./arch/arm/include/asm/arch-davinci/da8xx-usb.h
```

```
da8xx-usb.h
```

```
/*
* da8xx-usb.h -- TI's DA8xx platform specific usb wrapper definitions.
*
* Author: Ajay Kumar Gupta <ajay.gupta@ti.com>
*
* Based on drivers/usb/musb/davinci.h
*
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*/
```

```
./arch/arm/include/asm/arch-davinci/davinci_misc.h
```

```
davinci_misc.h
```

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*/
```

```
./arch/arm/include/asm/arch-davinci/ddr2_defs.h
ddr2_defs.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-davinci/dm365_lowlevel.h
dm365_lowlevel.h
```

```
/*
* SoC-specific lowlevel code for tms320dm365 and similar chips
*
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*/
```

```
./arch/arm/include/asm/arch-davinci/emacs_defs.h
```

```
emacs_defs.h
```

```
/*
 * Copyright (C) 2007 Sergey Kubushyn <ksi@koi8.net>
 *
 * Based on:
 *
 * -----
 *
 * dm644x_emac.h
 *
 * TI DaVinci (DM644X) EMAC peripheral driver header for DV-EVM
 *
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 *
 * -----
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 * -----
```

```
./arch/arm/include/asm/arch-davinci/emif_defs.h
```

```
emif_defs.h
```

```
/*
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 *
```

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*/
```

```
./arch/arm/include/asm/arch-davinci/gpio.h
```

```
gpio.h
```

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*/
```

```
./arch/arm/include/asm/arch-davinci/hardware.h
```

```
hardware.h
```

```
/*
* Copyright (C) 2007 Sergey Kubushyn <ksi@koi8.net>
*
* Based on:
*
*/
```

```
* -----
*
* linux/include/asm-arm/arch-davinci/hardware.h
*
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*
*/
```

```
./arch/arm/include/asm/arch-davinci/i2c_defs.h
i2c_defs.h
```

```
/*
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*/
```

```
./arch/arm/include/asm/arch-davinci/nand_defs.h
```

```
nand_defs.h
```

```
/*
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*/
```

```
./arch/arm/include/asm/arch-davinci/pinmux_defs.h
```

```
pinmux_defs.h
```

```
/*
 * Pinmux configurations for the DAxxx SoCs
 *
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```

```
./arch/arm/include/asm/arch-davinci/pll_defs.h
```

```
pll_defs.h
```

```
/*
```

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```

```
./arch/arm/include/asm/arch-davinci/psc_defs.h
```

```
psc_defs.h
```

```
/*
```

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```

```
./arch/arm/include/asm/arch-davinci/sdmmc_defs.h
sdmmc_defs.h
```

```
/*
 * Davinci MMC Controller Defines - Based on Linux davinci_mmc.c
 *
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```

```
./arch/arm/include/asm/arch-davinci/spl.h
spl.h
```

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```

```
*/  
  
./arch/arm/include/asm/arch-davinci/syscfg_defs.h  
syscfg_defs.h  
/*  
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```

```
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timer_defs.h  
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```

```
*/  
  
./arch/arm/include/asm/arch-ep93xx/ep93xx.h  
ep93xx.h  
/*  
* Cirrus Logic EP93xx register definitions.  
*  
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*  
* Copyright (C) 2006  
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* Cory T. Tusar, Videon Central, Inc., <ctusar@videon-central.com>  
*  
* Based in large part on linux/include/asm-arm/arch-ep93xx/regmap.h, which is  
*  
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* 675 Mass Ave, Cambridge, MA 02139, USA.  
*/
```

```
./arch/arm/include/asm/arch-exynos/adc.h  
adc.h  
/*  
* Copyright (C) 2010 Samsung Electronics  
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*/
```

```
./arch/arm/include/asm/arch-exynos/clk.h
clk.h
```

```
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* Minkyu Kang <mk7.kang@samsung.com>
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* MA 02111-1307 USA
*
*/
```

```
./arch/arm/include/asm/arch-exynos/clock.h
clock.h
```

```
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* Minkyu Kang <mk7.kang@samsung.com>
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* MA 02111-1307 USA
*
*/
```

```
./arch/arm/include/asm/arch-exynos/cpu.h
```

```
cpu.h
```

```
/*
```

```
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```

```
*
```

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```

```
*
```

```
*/
```

```
./arch/arm/include/asm/arch-exynos/dmc.h
```

```
dmc.h
```

```
No license information found.
```

```
./arch/arm/include/asm/arch-exynos/dp.h
```

```
dp.h
```

```
/*
```

```
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```

```
*
```

```
* Author: Donghwa Lee <dh09.lee@samsung.com>
```

```
*
```

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```
*
```

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* MA 02111-1307 USA  
*/
```

```
./arch/arm/include/asm/arch-exynos/dp_info.h
```

```
dp_info.h
```

```
/*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/arm/include/asm/arch-exynos/dsim.h
```

```
dsim.h
```

```
/*  
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*/
```

```
./arch/arm/include/asm/arch-exynos/dwmmc.h
dwmmc.h
```

```
/*
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 * Jaehoon Chung <jh80.chung@samsung.com>
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
 */
```

```
./arch/arm/include/asm/arch-exynos/ehci.h
ehci.h
```

```
/*
 * SAMSUNG EXYNOS USB HOST EHCI Controller
 *
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```

```
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* MA 02110-1301 USA  
*/
```

```
./arch/arm/include/asm/arch-exynos/fb.h  
fb.h
```

```
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* MA 02111-1307 USA  
*  
*/
```

```
./arch/arm/include/asm/arch-exynos/gpio.h  
gpio.h
```

```
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* MA 02111-1307 USA  
*  
*/
```

```
./arch/arm/include/asm/arch-exynos/i2s-regs.h
```

i2s-regs.h

```
/*
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 * R. Chandrasekar <rcsekar@samsung.com>
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 * MA 02111-1307 USA
 */
```

./arch/arm/include/asm/arch-exynos/mipi_dsim.h

mipi_dsim.h

```
/*
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 *
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 * Author: Donghwa Lee <dh09.lee@samsung.com>
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
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 */
```

./arch/arm/include/asm/arch-exynos/mmc.h

mmc.h

```
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
 */
```

```
./arch/arm/include/asm/arch-exynos/periph.h
periph.h
```

```
/*
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./arch/arm/include/asm/arch-exynos/pinmux.h
pinmux.h
```

```
/*
 * Copyright (C) 2012 Samsung Electronics
 * Abhilash Kesavan <a.kesavan@samsung.com>
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*/
```

```
./arch/arm/include/asm/arch-exynos/power.h
power.h
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
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*/
```

```
./arch/arm/include/asm/arch-exynos/pwm.h
pwm.h
```

```
/*
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* Kyungmin Park <kyungmin.park@samsung.com>
```

```
* Minkyu Kang <mk7.kang@samsung.com>
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*/
```

```
./arch/arm/include/asm/arch-exynos/pwm_backlight.h
```

```
pwm_backlight.h
```

```
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```

```
./arch/arm/include/asm/arch-exynos/sound.h
```

```
sound.h
```

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* MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-exynos/spi.h
spi.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-exynos/spl.h
spl.h
```

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*/
```

```
./arch/arm/include/asm/arch-exynos/sromc.h
```

```
sromc.h
```

```
/*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*  
* Note: This file contains the register description for SROMC  
*  
*/
```

```
./arch/arm/include/asm/arch-exynos/system.h
```

```
system.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-exynos/sys_proto.h
sys_proto.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-exynos/tzpc.h
tzpc.h
```

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*/
```

```
./arch/arm/include/asm/arch-exynos/uart.h
uart.h
```

```
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* Heungjun Kim <riverful.kim@samsung.com>
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*
*/
```

```
./arch/arm/include/asm/arch-exynos/watchdog.h
watchdog.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-imx/imx-regs.h
imx-regs.h
```

```
/* -----
* Motorola IMX system registers
* -----
*/
```

```
./arch/arm/include/asm/arch-ixp/ixp425.h
ixp425.h
```

```
/*
* include/asm-arm/arch-ixp425/ixp425.h
*
* Register definitions for IXP425
*
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*
*/
```

```
./arch/arm/include/asm/arch-ixp/ixp425pci.h
ixp425pci.h
```

```
/*
* IXP PCI Init
* (C) Copyright 2004 eslab.whut.edu.cn
* Yue Hu(huyue_whut@yahoo.com.cn), Ligong Xue(lgxue@hotmail.com)
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* See file CREDITS for list of people who contributed to this
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```

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*/
```

```
./arch/arm/include/asm/arch-kirkwood/config.h  
config.h
```

```
/*  
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*/
```

```
./arch/arm/include/asm/arch-kirkwood/cpu.h  
cpu.h
```

```
/*  
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* Marvell Semiconductor <www.marvell.com>  
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>  
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*/
```

```
./arch/arm/include/asm/arch-kirkwood/gpio.h
```

```
gpio.h
```

```
/*
 * arch/asm-arm/mach-kirkwood/include/mach/gpio.h
 *
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*/
```

```
./arch/arm/include/asm/arch-kirkwood/kirkwood.h
```

```
kirkwood.h
```

```
/*
 * (C) Copyright 2009
 * Marvell Semiconductor <www.marvell.com>
 * Written-by: Prafulla Wadaskar <prafulla@marvell.com>
 *
 * Header file for the Marvell's Feroceon CPU core.
 *
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 * project.
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* MA 02110-1301 USA
*/
```

```
./arch/arm/include/asm/arch-kirkwood/kw88f6192.h
kw88f6192.h
```

```
/*
 * (C) Copyright 2009
 * Marvell Semiconductor <www.marvell.com>
 * Written-by: Prafulla Wadaskar <prafulla@marvell.com>
 *
 * Header file for Feroceon CPU core 88FR131 Based KW88F6192 SOC.
 *
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 * project.
 *
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 * MA 02110-1301 USA
*/
```

```
./arch/arm/include/asm/arch-kirkwood/kw88f6281.h
kw88f6281.h
```

```
/*
 * (C) Copyright 2009
 * Marvell Semiconductor <www.marvell.com>
 * Written-by: Prafulla Wadaskar <prafulla@marvell.com>
 *
 * Header file for Feroceon CPU core 88FR131 Based KW88F6281 SOC.
 *
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* MA 02110-1301 USA
*/
```

```
./arch/arm/include/asm/arch-kirkwood/mpp.h
```

```
mpp.h
```

```
/*
* linux/arch/arm/mach-kirkwood/mpp.h -- Multi Purpose Pins
*
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* MA 02110-1301 USA
*/
```

```
./arch/arm/include/asm/arch-kirkwood/spi.h
```

```
spi.h
```

```
/*
* (C) Copyright 2009
* Marvell Semiconductor <www.marvell.com>
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
*
* Derived from drivers/spi/mpc8xxx_spi.c
*
* See file CREDITS for list of people who contributed to this
```

```
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*/
```

```
./arch/arm/include/asm/arch-ks8695/platform.h  
platform.h
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA  
*/
```

```
./arch/arm/include/asm/arch-lpc32xx/clk.h  
clk.h
```

```
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* MA 02110-1301, USA.
*/
```

```
./arch/arm/include/asm/arch-lpc32xx/config.h
config.h
```

```
/*
* Common definitions for LPC32XX board configurations
*
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*/
```

```
./arch/arm/include/asm/arch-lpc32xx/cpu.h
cpu.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-lpc32xx/emc.h  
emc.h
```

```
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 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,  
 * MA 02110-1301, USA.  
 */
```

```
./arch/arm/include/asm/arch-lpc32xx/sys_proto.h  
sys_proto.h
```

```
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 */
```

```
./arch/arm/include/asm/arch-lpc32xx/timer.h  
timer.h
```

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*/
```

```
./arch/arm/include/asm/arch-lpc32xx/uart.h
uart.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-lpc32xx/wdt.h
wdt.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-mb86r0x/hardware.h
hardware.h
```

```
/*
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 *
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 *         <cs@mycable.de>
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 * MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-mb86r0x/mb86r0x.h
mb86r0x.h
```

```
/*
 * (C) Copyright 2007
 *
 * mb86r0x definitions
 *
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 *         <cs@mycable.de>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-mx25/clock.h
clock.h
```

```
/*
*
* (c) 2009 Ilya Yanok, Emcraft Systems <yanok@emcraft.com>
*
* Modified for mx25 by John Rigby <jrigby@gmail.com>
*
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```

```
./arch/arm/include/asm/arch-mx25/gpio.h
gpio.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-mx25/imx-regs.h
```

```
imx-regs.h
```

```
/*
* Copyright (C) 2009, DENX Software Engineering
* Author: John Rigby <jcrigby@gmail.com>
*
* Based on arch-mx31/imx-regs.h
* Copyright (C) 2009 Ilya Yanok,
* Emcraft Systems <yanok@emcraft.com>
* and arch-mx27/imx-regs.h
* Copyright (C) 2007 Pengutronix,
* Sascha Hauer <s.hauer@pengutronix.de>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-mx25/imx25-pinmux.h
```

```
imx25-pinmux.h
```

```
/*  
 * iopin settings are controlled by four different sets of registers  
 *   iopad mux control  
 *   individual iopad setup (voltage select, pull/keep, drive strength ...)  
 *   group iopad setup (same as above but for groups of signals)  
 *   input select when multiple inputs are possible  
 */  
/*  
 * software pad mux control  
 */  
/* SW Input On (Loopback) */
```

```
./arch/arm/include/asm/arch-mx25/macro.h
```

```
macro.h
```

```
/*  
 * (C) Copyright 2011  
 * Matthias Weisser <weisserm@arcor.de>  
 *  
 * (C) Copyright 2009 DENX Software Engineering  
 * Author: John Rigby <jrigby@gmail.com>  
 *  
 * Common asm macros for imx25  
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
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 */
```

```
./arch/arm/include/asm/arch-mx25/sys_proto.h
```

```
sys_proto.h
```

```
/*  
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```

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*/
```

```
./arch/arm/include/asm/arch-mx27/clock.h
clock.h
```

```
/*
*
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*/
```

```
./arch/arm/include/asm/arch-mx27/gpio.h
gpio.h
```

```
/*
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* Philippe Reynes <tremyfr@yahoo.fr>
```

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*/
```

```
./arch/arm/include/asm/arch-mx27/imx-regs.h
imx-regs.h
```

```
/*
* (c) 2007 Pengutronix, Sascha Hauer <s.hauer@pengutronix.de>
* (c) 2009 Ilya Yanok, Emcraft Systems <yanok@emcraft.com>
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*/
```

```
./arch/arm/include/asm/arch-mx27/mxcmmc.h
mxcmmc.h
```

```
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*
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*/
```

```
./arch/arm/include/asm/arch-mx27/regs-rtc.h
regs-rtc.h
```

```
/*
 * Freescale i.MX27 RTC Register Definitions
 *
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 */
```

```
./arch/arm/include/asm/arch-mx31/clock.h
clock.h
```

```
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```

```
./arch/arm/include/asm/arch-mx31/gpio.h
```

```
gpio.h
```

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```

```
./arch/arm/include/asm/arch-mx31/imx-regs.h
```

```
imx-regs.h
```

```
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```

```
./arch/arm/include/asm/arch-mx31/sys_proto.h
```

```
sys_proto.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-mx35/clock.h
```

```
clock.h
```

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```

```
./arch/arm/include/asm/arch-mx35/crm_regs.h
```

```
crm_regs.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-mx35/gpio.h
```

```
gpio.h
```

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*/
```

```
./arch/arm/include/asm/arch-mx35/imx-regs.h
```

```
imx-regs.h
```

```
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```
./arch/arm/include/asm/arch-mx35/iomux.h
```

```
iomux.h
```

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```

```
./arch/arm/include/asm/arch-mx35/mmc_host_def.h
mmc_host_def.h
```

```
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```
./arch/arm/include/asm/arch-mx35/mx35_pins.h
mx35_pins.h
```

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*/
```

```
./arch/arm/include/asm/arch-mx35/spl.h
```

```
spl.h
```

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```

```
./arch/arm/include/asm/arch-mx35/sys_proto.h
```

```
sys_proto.h
```

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```

```
./arch/arm/include/asm/arch-mx5/clock.h
```

```
clock.h
```

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```

```
./arch/arm/include/asm/arch-mx5/crm_regs.h
```

```
crm_regs.h
```

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*/
```

```
./arch/arm/include/asm/arch-mx5/gpio.h
```

```
gpio.h
```

```
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```

```
./arch/arm/include/asm/arch-mx5/imx-regs.h
```

```
imx-regs.h
```

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```

```
./arch/arm/include/asm/arch-mx5/iomux-mx51.h
iomux-mx51.h
```

```
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```
./arch/arm/include/asm/arch-mx5/iomux.h
iomux.h
```

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```
./arch/arm/include/asm/arch-mx5/mx5x_pins.h
```

```
mx5x_pins.h
```

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```

```
./arch/arm/include/asm/arch-mx5/sys_proto.h
```

```
sys_proto.h
```

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```

```
./arch/arm/include/asm/arch-mx6/clock.h
```

```
clock.h
```

```
/*
```

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```
./arch/arm/include/asm/arch-mx6/crm_regs.h
```

```
crm_regs.h
```

```
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```

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```

```
./arch/arm/include/asm/arch-mx6/gpio.h
```

```
gpio.h
```

```
/*
```

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```
*/
```

```
./arch/arm/include/asm/arch-mx6/imx-regs.h
```

```
imx-regs.h
```

```
/*
```

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```
./arch/arm/include/asm/arch-mx6/iomux.h
```

```
iomux.h
```

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```

```
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```

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```
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mx6x_pins.h
```

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```

```
./arch/arm/include/asm/arch-mx6/mxc_hdmi.h  
mxc_hdmi.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-mx6/sys_proto.h
```

```
sys_proto.h
```

```
/*
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 * MA 02111-1307 USA
 */
```

```
./arch/arm/include/asm/arch-mxs/clock.h
```

```
clock.h
```

```
/*
 * Freescale i.MX28 Clock
 *
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*
*/
```

```
./arch/arm/include/asm/arch-mxs/dma.h
dma.h
```

```
/*
 * Freescale i.MX28 APBH DMA
 *
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 *
*/
```

```
./arch/arm/include/asm/arch-mxs/gpio.h
gpio.h
```

```
/*
 * Freescale i.MX28 GPIO
 *
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*
*/
```

```
./arch/arm/include/asm/arch-mxs/imx-regs.h
imx-regs.h
```

```
/*
 * Freescale i.MX28 Registers
 *
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 *
*/
```

```
./arch/arm/include/asm/arch-mxs/iomux-mx28.h
iomux-mx28.h
```

```
/*
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 */
```

```
./arch/arm/include/asm/arch-mxs/iomux.h
iomux.h
```

```
/*
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```

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*/

./arch/arm/include/asm/arch-mxs/regs-apbh.h

regs-apbh.h

/*

* Freescale i.MX28 APBH Register Definitions

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*

*/

./arch/arm/include/asm/arch-mxs/regs-base.h

regs-base.h

/*

* Freescale i.MX28 Peripheral Base Addresses

*

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*/
```

```
./arch/arm/include/asm/arch-mxs/regs-bch.h
regs-bch.h
```

```
/*
 * Freescale i.MX28 BCH Register Definitions
 *
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 *
*/
```

```
./arch/arm/include/asm/arch-mxs/regs-clkctrl-mx28.h
regs-clkctrl-mx28.h
/*
 * Freescale i.MX28 CLKCTRL Register Definitions
 *
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 */
```

```
./arch/arm/include/asm/arch-mxs/regs-common.h
regs-common.h
/*
 * Freescale i.MXS Register Accessors
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 */
```

```
./arch/arm/include/asm/arch-mxs/regs-digctl.h
regs-digctl.h
```

```
/*
 * Freescale i.MX28 DIGCTL Register Definitions
 *
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 */
```

```
./arch/arm/include/asm/arch-mxs/regs-gpmi.h
regs-gpmi.h
```

```
/*
 * Freescale i.MX28 GPMI Register Definitions
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 */
```

```
./arch/arm/include/asm/arch-mxs/regs-i2c.h
regs-i2c.h
```

```
/*
 * Freescale i.MX28 I2C Register Definitions
 *
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 */
```

```
./arch/arm/include/asm/arch-mxs/regs-lcdif.h
regs-lcdif.h
```

```
/*
 * Freescale i.MX28 LCDIF Register Definitions
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 */
```

*/

./arch/arm/include/asm/arch-mxs/regs-lradc.h
regs-lradc.h

/*

* Freescale i.MX28 LRADC Register Definitions

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*/

./arch/arm/include/asm/arch-mxs/regs-ocotp.h
regs-ocotp.h

/*

* Freescale i.MX28 OCOTP Register Definitions

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*/
```

```
./arch/arm/include/asm/arch-mxs/regs-pinctrl.h
regs-pinctrl.h
```

```
/*
 * Freescale i.MX28 PINCTRL Register Definitions
 *
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*/
```

```
./arch/arm/include/asm/arch-mxs/regs-power-mx28.h
regs-power-mx28.h
```

```
/*
 * Freescale i.MX28 Power Controller Register Definitions
 *
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*/
```

```
./arch/arm/include/asm/arch-mxs/regs-rtc.h
regs-rtc.h
```

```
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 * Freescale i.MX28 RTC Register Definitions
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 *
*/
```

```
./arch/arm/include/asm/arch-mxs/regs-ssp.h
regs-ssp.h
```

```
/*
 * Freescale i.MX28 SSP Register Definitions
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*
*/
```

```
./arch/arm/include/asm/arch-mxs/regs-timrot.h
regs-timrot.h
```

```
/*
 * Freescale i.MX28 TIMROT Register Definitions
 *
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 *
*/
```

```
./arch/arm/include/asm/arch-mxs/regs-usb.h
regs-usb.h
```

```
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 * Freescale i.MX28 USB OTG Register Definitions
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*/
```

```
./arch/arm/include/asm/arch-mxs/regs-usbphy.h
regs-usbphy.h
```

```
/*
 * Freescale i.MX28 USB PHY Register Definitions
 *
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```

```
./arch/arm/include/asm/arch-mxs/sys_proto.h
sys_proto.h
```

```
/*
 * Freescale i.MX23/i.MX28 specific functions
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*/
```

```
./arch/arm/include/asm/arch-nomadik/gpio.h
gpio.h
```

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 */
```

```
./arch/arm/include/asm/arch-nomadik/mtu.h
mtu.h
```

```
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```

*/

./arch/arm/include/asm/arch-omap24xx/bits.h
bits.h

/* bits.h

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*/

./arch/arm/include/asm/arch-omap24xx/clocks.h
clocks.h

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* Richard Woodruff <r-woodruff2@ti.com>

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./arch/arm/include/asm/arch-omap24xx/i2c.h
i2c.h

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```

```
./arch/arm/include/asm/arch-omap24xx/mem.h
```

```
mem.h
```

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```

```
./arch/arm/include/asm/arch-omap24xx/mux.h
```

```
mux.h
```

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```

```
./arch/arm/include/asm/arch-omap24xx/omap2420.h
omap2420.h
```

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```
./arch/arm/include/asm/arch-omap24xx/sys_info.h
sys_info.h
```

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```
./arch/arm/include/asm/arch-omap24xx/sys_proto.h
sys_proto.h
```

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```

```
./arch/arm/include/asm/arch-omap3/am35x_def.h
am35x_def.h
```

```
/*
* am35x_def.h - TI's AM35x specific definitions.
*
* Based on arch/arm/include/asm/arch-omap3/cpu.h
*
* Author: Ajay Kumar Gupta <ajay.gupta@ti.com>
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```
./arch/arm/include/asm/arch-omap3/clocks.h
clocks.h
```

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```
./arch/arm/include/asm/arch-omap3/clocks_omap3.h
clocks_omap3.h
```

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```

```
./arch/arm/include/asm/arch-omap3/cpu.h  
cpu.h
```

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```

```
./arch/arm/include/asm/arch-omap3/dma.h  
dma.h
```

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*/
```

```
./arch/arm/include/asm/arch-omap3/dss.h
```

```
dss.h
```

```
/*
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 * Syed Mohammed Khasim <khasim@ti.com>
 *
 * Referred to Linux Kernel DSS driver files for OMAP3 by
 * Tomi Valkeinen from drivers/video/omap2/dss/
 *
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```

```
./arch/arm/include/asm/arch-omap3/ehci.h
```

```
ehci.h
```

```
/*
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 * Alexander Holler <holler@ahsoftware.de>
 *
 * Based on "drivers/usb/host/ehci-omap.c" from Linux 2.6.37
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*/
```

```
./arch/arm/include/asm/arch-omap3/emacs_defs.h
emacs_defs.h
```

```
/*
 * Copyright (C) 2007 Sergey Kubushyn <ksi@koi8.net>
 *
 * Based on:
 *
 * -----
 *
 * dm644x_emac.h
 *
 * TI DaVinci (DM644X) EMAC peripheral driver header for DV-EVM
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 *
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 * -----
```

```
./arch/arm/include/asm/arch-omap3/emif4.h
emif4.h
```

```
/*
 * Auther:
 *      Vaibhav Hiremath <hvaibhav@ti.com>
```

```
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*/

./arch/arm/include/asm/arch-omap3/gpio.h
gpio.h
/*
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*
* Below is the original's header including its copyright
*
* linux/arch/arm/plat-omap/gpio.c
*
```

```
* Support functions for OMAP GPIO
*
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```

```
./arch/arm/include/asm/arch-omap3/i2c.h
```

```
i2c.h
```

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```
./arch/arm/include/asm/arch-omap3/mem.h
```

```
mem.h
```

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```
./arch/arm/include/asm/arch-omap3/mmc_host_def.h
```

```
mmc_host_def.h
```

```
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```
./arch/arm/include/asm/arch-omap3/musb.h
```

```
musb.h
```

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```
./arch/arm/include/asm/arch-omap3/mux.h
```

```
mux.h
```

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```

```
./arch/arm/include/asm/arch-omap3/omap3-regs.h
```

```
omap3-regs.h
```

```
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```
./arch/arm/include/asm/arch-omap3/omap3.h
```

```
omap3.h
```

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```

```
./arch/arm/include/asm/arch-omap3/omap_gpmmc.h
```

```
omap_gpmmc.h
```

```
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```
./arch/arm/include/asm/arch-omap3/spl.h
```

```
spl.h
```

```
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```

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*/
```

```
./arch/arm/include/asm/arch-omap3/sys_proto.h
```

```
sys_proto.h
```

```
/*
```

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* Richard Woodruff <r-woodruff2@ti.com>
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```

```
./arch/arm/include/asm/arch-omap4/clocks.h  
clocks.h
```

```
/*  
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* Aneesh V <aneesh@ti.com>  
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*/
```

```
./arch/arm/include/asm/arch-omap4/cpu.h  
cpu.h
```

```
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*
*/
```

```
./arch/arm/include/asm/arch-omap4/ehci.h
ehci.h
```

```
/*
* OMAP EHCI port support
* Based on LINUX KERNEL
* drivers/usb/host/ehci-omap.c and drivers/mfd/omap-usb-host.c
*
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*/
```

```
./arch/arm/include/asm/arch-omap4/gpio.h
gpio.h
```

```
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*
* linux/arch/arm/plat-omap/gpio.c
*
* Support functions for OMAP GPIO
*
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* Written by Juha Yrjölä <juha.yrjola@nokia.com>
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*/
```

```
./arch/arm/include/asm/arch-omap4/i2c.h
i2c.h
```

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*/
```

```
./arch/arm/include/asm/arch-omap4/mmc_host_def.h
mmc_host_def.h
```

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 * MA 02111-1307 USA
 */
```

```
./arch/arm/include/asm/arch-omap4/mux_omap4.h
mux_omap4.h
```

```
/*
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 * Richard Woodruff          <r-woodruff2@ti.com>
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```

```
*/  
  
./arch/arm/include/asm/arch-omap4/omap.h  
omap.h  
/*  
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```
./arch/arm/include/asm/arch-omap4/spl.h  
spl.h  
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```

```
./arch/arm/include/asm/arch-omap4/sys_proto.h
sys_proto.h
```

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```
./arch/arm/include/asm/arch-omap5/clocks.h
clocks.h
```

```
/*
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```
./arch/arm/include/asm/arch-omap5/cpu.h
cpu.h
```

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 */
```

```
./arch/arm/include/asm/arch-omap5/gpio.h
gpio.h
```

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*
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*
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* Written by Juha Yrjölä <juha.yrjola@nokia.com>
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```
./arch/arm/include/asm/arch-omap5/i2c.h
```

```
i2c.h
```

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```

```
*/  
  
./arch/arm/include/asm/arch-omap5/mmc_host_def.h  
mmc_host_def.h  
/*  
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*/
```

```
./arch/arm/include/asm/arch-omap5/mux_omap5.h  
mux_omap5.h  
/*  
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```

```
./arch/arm/include/asm/arch-omap5/omap.h
```

```
omap.h
```

```
/*
```

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*
```

```
* Authors:
```

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*     Sricharan R <r.sricharan@ti.com>
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```
*/
```

```
./arch/arm/include/asm/arch-omap5/spl.h
```

```
spl.h
```

```
/*
```

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*/
```

```
./arch/arm/include/asm/arch-omap5/sys_proto.h
```

```
sys_proto.h
```

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*/
```

```
./arch/arm/include/asm/arch-orion5x/cpu.h
```

```
cpu.h
```

```
/*
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* Based on original Kirorion5x_ood support which is
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*/
```

```
./arch/arm/include/asm/arch-orion5x/mv88f5182.h  
mv88f5182.h
```

```
/*  
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*  
* Based on original Kirkwood 88F6182 support which is  
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* Marvell Semiconductor <www.marvell.com>  
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>  
*  
* Header file for Feroceon CPU core 88F5182 SOC.  
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*/
```

```
./arch/arm/include/asm/arch-orion5x/orion5x.h  
orion5x.h
```

```
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* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
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```

```
./arch/arm/include/asm/arch-pantheon/config.h
config.h
```

```
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```

```
./arch/arm/include/asm/arch-pantheon/cpu.h
```

cpu.h

```
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 * MA 02110-1301 USA
 */
```

./arch/arm/include/asm/arch-pantheon/mfp.h

mfp.h

```
/*
 * Based on arch/arm/include/asm/arch-armada100/mfp.h
 * (C) Copyright 2011
 * Marvell Semiconductor <www.marvell.com>
 * Written-by: Lei Wen <leiwen@marvell.com>
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 * MA 02110-1301 USA
```

*/

./arch/arm/include/asm/arch-pantheon/pantheon.h
pantheon.h

/*

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* Marvell Semiconductor <www.marvell.com>

* Written-by: Lei Wen <leiwen@marvell.com>

*

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* MA 02110-1301 USA

*/

./arch/arm/include/asm/arch-pxa/bitfield.h
bitfield.h

/*

* FILE bitfield.h

*

* Version 1.1

* Author Copyright (c) Marc A. Viredaz, 1998

* DEC Western Research Laboratory, Palo Alto, CA

* Date April 1998 (April 1997)

* System Advanced RISC Machine (ARM)

* Language C or ARM Assembly

* Purpose Definition of macros to operate on bit fields.

*/

./arch/arm/include/asm/arch-pxa/hardware.h
hardware.h

/*

* linux/include/asm-arm/arch-pxa/hardware.h

*

* Author: Nicolas Pitre

* Created: Jun 15, 2001

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```

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* published by the Free Software Foundation.
*
* Note: This file was taken from linux-2.4.19-rmk4-pxa1
*
* - 2003/01/20 implementation specifics activated
*   Robert Schwebel <r.schwebel@pengutronix.de>
*/

./arch/arm/include/asm/arch-pxa/pxa-regs.h
pxa-regs.h
/*
* linux/include/asm-arm/arch-pxa/pxa-regs.h
*
* Author:      Nicolas Pitre
* Created:     Jun 15, 2001
* Copyright:   MontaVista Software Inc.
*
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* it under the terms of the GNU General Public License version 2 as
* published by the Free Software Foundation.
*
* - 2003/01/20: Robert Schwebel <r.schwebel@pengutronix.de
*   Original file taken from linux-2.4.19-rmk4-pxa1. Added some definitions.
*   Added include for hardware.h (for __REG definition)
*/

./arch/arm/include/asm/arch-pxa/pxa.h
pxa.h
/*
* PXA common functions
*
* Copyright (C) 2011 Marek Vasut <marek.vasut@gmail.com>
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* MA 02111-1307 USA

```

*/

./arch/arm/include/asm/arch-pxa/regs-mmc.h
regs-mmc.h

/*

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* MA 02111-1307 USA

*/

./arch/arm/include/asm/arch-pxa/regs-uart.h
regs-uart.h

/*

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* MA 02111-1307 USA

*/

./arch/arm/include/asm/arch-pxa/regs-usb.h
regs-usb.h

/*

* PXA25x UDC definitions

*

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*/
```

```
./arch/arm/include/asm/arch-rmobile/gpio.h
gpio.h
No license information found.
```

```
./arch/arm/include/asm/arch-rmobile/irqs.h
irqs.h
/* GIC */
/* INTCA */
/* INTCS */
```

```
./arch/arm/include/asm/arch-rmobile/r8a7740-gpio.h
r8a7740-gpio.h
/*
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* Copyright (C) 2011 Kuninori Morimoto <kuninori.morimoto.gx@renesas.com>
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* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston, MA 02110-1301 USA
*/
```

```
./arch/arm/include/asm/arch-rmobile/r8a7740.h
r8a7740.h
/*
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```

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* MA 02110-1301, USA.
*/
```

```
./arch/arm/include/asm/arch-rmobile/rmobile.h
rmobile.h
No license information found.
```

```
./arch/arm/include/asm/arch-rmobile/sh73a0-gpio.h
sh73a0-gpio.h
/* Pin Function Controller:
* GPIO_FN_xx - GPIO used to select pin function and MSEL switch
* GPIO_PORTxx - GPIO mapped to real I/O pin on CPU
*/
```

```
./arch/arm/include/asm/arch-rmobile/sh73a0.h
sh73a0.h
/* Global Timer */
/* GIC */
/* Secure control register */
```

```
./arch/arm/include/asm/arch-rmobile/sys_proto.h
sys_proto.h
/*
* (C) Copyright 2010
* Texas Instruments, <www.ti.com>
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```

```
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* MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-s3c24x0/gpio.h
```

```
gpio.h
```

```
/*
 * Copyright (c) 2012.
 *
 * Gabriel Huau <contact@huau-gabriel.fr>
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 * project.
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 * MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-s3c24x0/iomux.h
```

```
iomux.h
```

```
/*
 * Copyright (c) 2012
 *
 * Gabriel Huau <contact@huau-gabriel.fr>
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* MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-s3c24x0/memory.h
memory.h
```

```
/*
* linux/include/asm-arm/arch-s3c2400/memory.h by garyj@denx.de
* based on
* linux/include/asm-arm/arch-sa1100/memory.h
*
* Copyright (c) 1999 Nicolas Pitre <nico@visuaide.com>
*/
/*
* Task size: 3GB
*/
```

```
./arch/arm/include/asm/arch-s3c24x0/s3c2400.h
s3c2400.h
```

```
/*
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```

```
./arch/arm/include/asm/arch-s3c24x0/s3c2410.h
s3c2410.h
```

```
/*
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```

```
./arch/arm/include/asm/arch-s3c24x0/s3c2440.h
s3c2440.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-s3c24x0/s3c24x0.h
s3c24x0.h
```

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*/

./arch/arm/include/asm/arch-s3c24x0/s3c24x0_cpu.h
s3c24x0_cpu.h
/*
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* MA 02111-1307 USA
*/

./arch/arm/include/asm/arch-s3c44b0/hardware.h
hardware.h
/*****/
/* */
/* Samsung S3C44B0 */
/* tpu <tapu@371.net> */
/* */
/*****/

```

```
./arch/arm/include/asm/arch-s3c64xx/hardware.h  
hardware.h
```

```
/*  
 * Originates from Samsung's u-boot 1.1.6 port to S3C6400 / SMDK6400  
 *  
 * (C) Copyright 2008  
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
 * MA 02111-1307 USA  
 */
```

```
./arch/arm/include/asm/arch-s3c64xx/s3c6400.h  
s3c6400.h
```

```
/*  
 * (C) Copyright 2007  
 * Byungjae Lee, Samsung Electronics, bjlee@samsung.com.  
 * - only support for S3C6400  
 *  
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 * Guennadi Liakhovetki, DENX Software Engineering, <lg@denx.de>  
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 */
```

```
./arch/arm/include/asm/arch-s3c64xx/s3c64x0.h  
s3c64x0.h
```

```
/*  
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 * David MÅeller ELSOFT AG Switzerland. d.mueller@elsoft.ch  
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
 * MA 02111-1307 USA  
 */
```

```
./arch/arm/include/asm/arch-s5pc1xx/clk.h  
clk.h
```

```
/*  
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 * Heungjun Kim <riverful.kim@samsung.com>  
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```

```
*
*/

./arch/arm/include/asm/arch-s5pc1xx/clock.h
clock.h
/*
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* Heungjun Kim <riverful.kim@samsung.com>
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*
*/
```

```
./arch/arm/include/asm/arch-s5pc1xx/cpu.h
cpu.h
/*
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*
*/
```

```
./arch/arm/include/asm/arch-s5pc1xx/gpio.h
gpio.h
```

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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
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 */
```

```
./arch/arm/include/asm/arch-s5pc1xx/mmc.h
mmc.h
```

```
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
 */
```

```
./arch/arm/include/asm/arch-s5pc1xx/power.h
power.h
```

```
/*
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 * Kyungmin Park <kyungmin.park@samsung.com>
```

```
* Minkyu Kang <mk7.kang@samsung.com>
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*
*/
```

```
./arch/arm/include/asm/arch-s5pc1xx/pwm.h
```

```
pwm.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-s5pc1xx/sromc.h
```

```
sromc.h
```

```
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* Naveen Krishna Ch <ch.naveen@samsung.com>
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
* Note: This file contains the register description for Memory subsystem
*       (SRAM, NAND Flash, OneNand, DDR, OneDRAM) on S5PC1XX.
*
*       Only SRAMC is defined as of now
*/
```

```
./arch/arm/include/asm/arch-s5pc1xx/sys_proto.h
```

```
sys_proto.h
```

```
/*
* Copyright (C) 2009 Samsung Electronics
* Minkyu Kang <mk7.kang@samsung.com>
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* MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-s5pc1xx/uart.h
```

```
uart.h
```

```
/*
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* Minkyu Kang <mk7.kang@samsung.com>
* Heungjun Kim <riverful.kim@samsung.com>
```

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*
*/
```

```
./arch/arm/include/asm/arch-s5pc1xx/watchdog.h
watchdog.h
```

```
/*
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* MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-sa1100/bitfield.h
bitfield.h
```

```
/*
* FILE bitfield.h
*
* Version 1.1
```

```
* Author          Copyright (c) Marc A. Viredaz, 1998
*                DEC Western Research Laboratory, Palo Alto, CA
* Date           April 1998 (April 1997)
* System         Advanced RISC Machine (ARM)
* Language       C or ARM Assembly
* Purpose        Definition of macros to operate on bit fields.
*/
```

```
./arch/arm/include/asm/arch-socfpga/clock_manager.h
clock_manager.h
```

```
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 */
```

```
./arch/arm/include/asm/arch-socfpga/debug_memory.h
debug_memory.h
```

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 */
```

```
./arch/arm/include/asm/arch-socfpga/dwmmc.h
dwmmc.h
```

```
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```

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*/
```

```
./arch/arm/include/asm/arch-socfpga/ecc_ram.h
ecc_ram.h
```

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*/
```

```
./arch/arm/include/asm/arch-socfpga/fpga_manager.h
fpga_manager.h
```

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*/
```

```
./arch/arm/include/asm/arch-socfpga/freeze_controller.h
freeze_controller.h
```

```
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 */
```

```
./arch/arm/include/asm/arch-socfpga/gic.h
gic.h
```

```
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```

```
*/  
  
./arch/arm/include/asm/arch-socfpga/hardware.h  
hardware.h  
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*/
```

```
./arch/arm/include/asm/arch-socfpga/interrupts.h  
interrupts.h  
/*  
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*/
```

```
./arch/arm/include/asm/arch-socfpga/nic301.h  
nic301.h  
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*  
*/
```

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*/
```

```
./arch/arm/include/asm/arch-socfpga/pl330_csr.h  
pl330_csr.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-socfpga/reset_manager.h  
reset_manager.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-socfpga/scan_manager.h  
scan_manager.h
```

```
/*
```

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```

```
./arch/arm/include/asm/arch-socfpga/sdram.h
sdram.h
```

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```

```
./arch/arm/include/asm/arch-socfpga/spl.h
spl.h
```

```
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```

```
./arch/arm/include/asm/arch-socfpga/system_manager.h
system_manager.h
```

```
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```

```
./arch/arm/include/asm/arch-socfpga/timer.h
timer.h
```

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```

```
./arch/arm/include/asm/arch-spear/clk.h
clk.h
```

```
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```

```
./arch/arm/include/asm/arch-spear/gpio.h
```

```
gpio.h
```

```
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```

```
./arch/arm/include/asm/arch-spear/hardware.h
```

```
hardware.h
```

```
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```

```
./arch/arm/include/asm/arch-spear/spr_defs.h
spr_defs.h
```

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```
./arch/arm/include/asm/arch-spear/spr_emi.h
spr_emi.h
```

```
/*
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```
./arch/arm/include/asm/arch-spear/spr_gpt.h
```

```
spr_gpt.h
```

```
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```

```
./arch/arm/include/asm/arch-spear/spr_misc.h
```

```
spr_misc.h
```

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*/
```

```
./arch/arm/include/asm/arch-spear/spr_ssp.h
```

```
spr_ssp.h
```

```
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*/
```

```
./arch/arm/include/asm/arch-spear/spr_syscntl.h
```

```
spr_syscntl.h
```

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```

```
./arch/arm/include/asm/arch-tegra/ap.h
ap.h
```

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```

```
./arch/arm/include/asm/arch-tegra/board.h
board.h
```

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```

```
./arch/arm/include/asm/arch-tegra/clk_rst.h
clk_rst.h
```

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```

```
./arch/arm/include/asm/arch-tegra/clock.h
clock.h
```

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```
./arch/arm/include/asm/arch-tegra/fuse.h
```

```
fuse.h
```

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*/
```

```
./arch/arm/include/asm/arch-tegra/gpio.h
```

```
gpio.h
```

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```

```
./arch/arm/include/asm/arch-tegra/mmc.h
```

```
mmc.h
```

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```

```
./arch/arm/include/asm/arch-tegra/pmc.h
```

```
pmc.h
```

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./arch/arm/include/asm/arch-tegra/scu.h
scu.h
```

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```
./arch/arm/include/asm/arch-tegra/sys_proto.h
sys_proto.h
```

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```
./arch/arm/include/asm/arch-tegra/tegra.h
tegra.h
```

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```
./arch/arm/include/asm/arch-tegra/tegra_i2c.h
tegra_i2c.h
```

```
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```
./arch/arm/include/asm/arch-tegra/tegra_mmc.h
tegra_mmc.h
```

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```
./arch/arm/include/asm/arch-tegra/tegra_spi.h
tegra_spi.h
```

```
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* NVIDIA Tegra20 SPI-FLASH controller
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```
./arch/arm/include/asm/arch-tegra/timer.h
timer.h
```

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```
./arch/arm/include/asm/arch-tegra/uart.h
uart.h
```

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```
./arch/arm/include/asm/arch-tegra/warmboot.h
warmboot.h
```

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```
./arch/arm/include/asm/arch-tegra20/apb_misc.h
apb_misc.h
```

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```
./arch/arm/include/asm/arch-tegra20/clock-tables.h
clock-tables.h
```

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```
./arch/arm/include/asm/arch-tegra20/clock.h
clock.h
```

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```

```
./arch/arm/include/asm/arch-tegra20/dc.h
dc.h
```

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```
./arch/arm/include/asm/arch-tegra20/display.h
display.h
```

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```

```
./arch/arm/include/asm/arch-tegra20/emc.h
emc.h
```

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```
./arch/arm/include/asm/arch-tegra20/flow.h
flow.h
```

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```
./arch/arm/include/asm/arch-tegra20/funcmux.h
funcmux.h
```

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```
./arch/arm/include/asm/arch-tegra20/gpio.h
gpio.h
```

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```
./arch/arm/include/asm/arch-tegra20/gp_padctrl.h
gp_padctrl.h
```

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```
./arch/arm/include/asm/arch-tegra20/hardware.h
hardware.h
```

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```

./arch/arm/include/asm/arch-tegra20/pinmux.h

pinmux.h

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```

./arch/arm/include/asm/arch-tegra20/pmu.h

pmu.h

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 */
```

```
./arch/arm/include/asm/arch-tegra20/pwm.h
```

```
pwm.h
```

```
/*
```

```
* Tegra pulse width frequency modulator definitions
```

```
*
```

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```
*
```

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```
*
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```

```
*/
```

```
./arch/arm/include/asm/arch-tegra20/sdram_param.h
```

```
sdram_param.h
```

```
/*
```

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```

```
*
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```
*/
```

./arch/arm/include/asm/arch-tegra20/spl.h

spl.h

/*

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*/

./arch/arm/include/asm/arch-tegra20/tegra.h

tegra.h

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*/

./arch/arm/include/asm/arch-tegra20/uart-spi-switch.h

uart-spi-switch.h

/*

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*/

./arch/arm/include/asm/arch-tegra20/usb.h

usb.h

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* MA 02111-1307 USA

*/

./arch/arm/include/asm/arch-tnetv107x/clock.h

clock.h

/*

* TNETV107X: Clock APIs

```
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./arch/arm/include/asm/arch-tnetv107x/emif_defs.h
emif_defs.h
No license information found.
```

```
./arch/arm/include/asm/arch-tnetv107x/hardware.h
hardware.h
/*
* TNETV107X: Hardware information
*
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* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./arch/arm/include/asm/arch-tnetv107x/mux.h
mux.h
/*
* TNETV107X: Pinmux APIs
```

```
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./arch/arm/include/asm/arch-tnetv107x/nand_defs.h
```

```
nand_defs.h
```

```
/*
* TNETV107X: NAND definitions
*
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* project.
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./arch/arm/include/asm/arch-u8500/clock.h
```

```
clock.h
```

```
/*
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*
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* project.
*
*/
```

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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-u8500/db8500_gpio.h
db8500_gpio.h
```

```
/*
 * Structures and registers for GPIO access in the Nomadik SoC
 *
 * Code ported from Nomadik GPIO driver in ST-Ericsson Linux kernel code.
 * The purpose is that GPIO config found in kernel should work by simply
 * copy-paste it to U-boot.
 *
 * Ported to U-boot by:
 * Copyright (C) 2010 Joakim Axelsson <joakim.axelsson AT stericsson.com>
 * Copyright (C) 2008 STMicroelectronics
 * Author: Prafulla WADASKAR <prafulla.wadaskar@st.com>
 * Copyright (C) 2009 Alessandro Rubini <rubini@unipv.it>
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 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 */
```

```
./arch/arm/include/asm/arch-u8500/db8500_pincfg.h
db8500_pincfg.h
```

```
/*
 * Copyright (C) ST-Ericsson SA 2010
 *
 * Code ported from Nomadik GPIO driver in ST-Ericsson Linux kernel code.
 * The purpose is that GPIO config found in kernel should work by simply
 * copy-paste it to U-boot. Ported 2010 to U-boot by:
 * Author: Joakim Axelsson <joakim.axelsson AT stericsson.com>
 *
 * License terms: GNU General Public License, version 2
 * Author: Rabin Vincent <rabin.vincent@stericsson.com> for ST-Ericsson
 *
 */
```

```
* Based on arch/arm/mach-pxa/include/mach/mfp.h:
* Copyright (C) 2007 Marvell International Ltd.
* eric miao <eric.miao@marvell.com>
*/
```

```
./arch/arm/include/asm/arch-u8500/gpio.h
```

```
gpio.h
```

```
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./arch/arm/include/asm/arch-u8500/hardware.h
```

```
hardware.h
```

```
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 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
```

```
* MA 02110-1301 USA
*/
```

```
./arch/arm/include/asm/arch-u8500/prcmu.h
prcmu.h
```

```
/*
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 *
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 * Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./arch/arm/include/asm/arch-u8500/sys_proto.h
sys_proto.h
```

```
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/arch-u8500/u8500.h
u8500.h
```

```
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 * MA 02111-1307 USA
 */
```

```
./arch/arm/include/asm/armv7.h
armv7.h
```

```
/*
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 * Aneesh V <aneesh@ti.com>
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 * MA 02111-1307 USA
 */
```

```
./arch/arm/include/asm/asm assembler.h
```

```
assembler.h
```

```
/*  
 * arch/arm/include/asm/asm assembler.h  
 *  
 * Copyright (C) 1996-2000 Russell King  
 *  
 * This program is free software; you can redistribute it and/or modify  
 * it under the terms of the GNU General Public License version 2 as  
 * published by the Free Software Foundation.  
 *  
 * This file contains arm architecture specific defines  
 * for the different processors.  
 *  
 * Do not include any C declarations in this file - it is included by  
 * assembler source.  
 */
```

```
./arch/arm/include/asm/atomic.h
```

```
atomic.h
```

```
/*  
 * linux/include/asm-arm/atomic.h  
 *  
 * Copyright (c) 1996 Russell King.  
 *  
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 * it under the terms of the GNU General Public License version 2 as  
 * published by the Free Software Foundation.  
 *  
 * Changelog:  
 * 27-06-1996 RMK Created  
 * 13-04-1997 RMK Made functions atomic!  
 * 07-12-1997 RMK Upgraded for v2.1.  
 * 26-08-1998 PJB Added #ifdef __KERNEL__  
 */
```

```
./arch/arm/include/asm/bitops.h
```

```
bitops.h
```

```
/*  
 * Copyright 1995, Russell King.  
 * Various bits and pieces copyrights include:  
 * Linus Torvalds (test_bit).  
 *  
 * bit 0 is the LSB of addr; bit 32 is the LSB of (addr+1).  
 *  
 * Please note that the code in this file should never be included  
 * from user space. Many of these are not implemented in assembler  
 * since they would be too costly. Also, they require privileged  
 * instructions (which are not available from user mode) to ensure  
 * that they are atomic.
```

```
*/
```

```
./arch/arm/include/asm/bootm.h
```

```
bootm.h
```

```
/* Copyright (C) 2011
```

```
* Corscience GmbH & Co. KG - Simon Schwarz <schwarz@corscience.de>
```

```
*
```

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```

```
*
```

```
*/
```

```
./arch/arm/include/asm/byteorder.h
```

```
byteorder.h
```

```
/*
```

```
* linux/include/asm-arm/byteorder.h
```

```
*
```

```
* ARM Endian-ness. In little endian mode, the data bus is connected such  
* that byte accesses appear as:
```

```
* 0 = d0...d7, 1 = d8...d15, 2 = d16...d23, 3 = d24...d31
```

```
* and word accesses (data or instruction) appear as:
```

```
* d0...d31
```

```
*
```

```
* When in big endian mode, byte accesses appear as:
```

```
* 0 = d24...d31, 1 = d16...d23, 2 = d8...d15, 3 = d0...d7
```

```
* and word accesses (data or instruction) appear as:
```

```
* d0...d31
```

```
*/
```

```
./arch/arm/include/asm/cache.h
```

```
cache.h
```

```
/*
```

```
* (C) Copyright 2009
```

```
* Marvell Semiconductor <www.marvell.com>
```

```
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
```

```
*
```

```
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* project.
```

```
*
```

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* MA 02110-1301 USA
*/
```

```
./arch/arm/include/asm/config.h
config.h
```

```
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* MA 02111-1307 USA
*
*/
```

```
./arch/arm/include/asm/dma-mapping.h
dma-mapping.h
```

```
/*
* (C) Copyright 2007
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* Lead Tech Design <www.leadtechdesign.com>
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* MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/ehci-omap.h
ehci-omap.h
```

```
/*
 * OMAP EHCI port support
 * Based on LINUX KERNEL
 * drivers/usb/host/ehci-omap.c and drivers/mfd/omap-usb-host.c
 *
 * Copyright (C) 2011 Texas Instruments Incorporated - http://www.ti.com*
 * Author: Govindraj R <govindraj.raja@ti.com>
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 */
```

```
./arch/arm/include/asm/emif.h
emif.h
```

```
/*
 * OMAP44xx EMIF header
 *
 * Copyright (C) 2009-2010 Texas Instruments, Inc.
 *
 * Aneesh V <aneesh@ti.com>
 *
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 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 */
```

```
./arch/arm/include/asm/errno.h
errno.h
No license information found.
```

```
./arch/arm/include/asm/global_data.h
global_data.h
```

```
/*
 * (C) Copyright 2002-2010
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
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 * project.
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./arch/arm/include/asm/gpio.h
gpio.h
No license information found.
```

```
./arch/arm/include/asm/hardware.h
hardware.h
```

```
/*
 * linux/include/asm-arm/hardware.h
 *
 * Copyright (C) 1996 Russell King
 *
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 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 *
 * Common hardware definitions
 */
```

```
./arch/arm/include/asm/imx-common/boot_mode.h
boot_mode.h
```

```
/*
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 *
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 * project.
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./arch/arm/include/asm/imx-common/gpio.h
```

```
gpio.h
```

```
/*
 * Copyright (C) 2011
 * Stefano Babic, DENX Software Engineering, <sbabic@denx.de>
 *
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 * project.
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./arch/arm/include/asm/imx-common/iomux-v3.h
```

```
iomux-v3.h
```

```
/*
```

```
* Based on Linux i.MX iomux-v3.h file:
* Copyright (C) 2009 by Jan Weitzel Phytex Messtechnik GmbH,
* <armlinux@phytec.de>
*
* Copyright (C) 2011 Freescale Semiconductor, Inc.
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
* MA 02110-1301, USA.
*/
```

```
./arch/arm/include/asm/imx-common/mx5_video.h
```

```
mx5_video.h
```

```
/*
* Copyright (C) 2012
* Anatolij Gustschin, DENX Software Engineering, <agust@denx.de>
*
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* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2 of
* the License, or (at your option) any later version.
*/
```

```
./arch/arm/include/asm/imx-common/mxc_i2c.h
```

```
mxc_i2c.h
```

```
/*
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*
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* but WITHOUT ANY WARRANTY; without even the implied warranty of
* MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
* GNU General Public License for more details.
```

```
./arch/arm/include/asm/io.h
```

```
io.h
```

```
/*
```

```
* linux/include/asm-arm/io.h
*
* Copyright (C) 1996-2000 Russell King
*
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* it under the terms of the GNU General Public License version 2 as
* published by the Free Software Foundation.
*
* Modifications:
* 16-Sep-1996 RMK      Inlined the inx/outx functions & optimised for both
*                    constant addresses and variable addresses.
* 04-Dec-1997 RMK      Moved a lot of this stuff to the new architecture
*                    specific IO header files.
* 27-Mar-1999 PJB      Second parameter of memcpy_toio is const..
* 04-Apr-1999 PJB      Added check_signature.
* 12-Dec-1999 RMK      More cleanups
* 18-Jun-2000 RMK      Removed virt_to_* and friends definitions
*/
```

```
./arch/arm/include/asm/linkage.h
linkage.h
No license information found.
```

```
./arch/arm/include/asm/mach-types.h
mach-types.h
/*
 * This was automagically generated from arch/arm/tools/mach-types!
 * Do NOT edit
 */
/* The type of machine we're running on */
```

```
./arch/arm/include/asm/macro.h
macro.h
/*
 * include/asm-arm/macro.h
 *
 * Copyright (C) 2009 Jean-Christophe PLAGNIOL-VILLARD <plagnioj@jcrosoft.com>
 *
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 * project.
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```

```
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/memory.h
```

```
memory.h
```

```
/*
 * linux/include/asm-arm/memory.h
 *
 * Copyright (C) 2000-2002 Russell King
 *
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 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 *
 * Note: this file should not be included by non-asm/.h files
*/
```

```
./arch/arm/include/asm/omap_common.h
```

```
omap_common.h
```

```
/*
 * (C) Copyright 2010
 * Texas Instruments, <www.ti.com>
 *
 * Aneesh V <aneesh@ti.com>
 *
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 * project.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./arch/arm/include/asm/omap_gpio.h
```

```
omap_gpio.h
```

```
/*
 * Copyright (c) 2009 Wind River Systems, Inc.
 * Tom Rix <Tom.Rix@windriver.com>
 *
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 * MA 02111-1307 USA
 *
 * This work is derived from the linux 2.6.27 kernel source
 * To fetch, use the kernel repository
 * git://git.kernel.org/pub/scm/linux/kernel/git/torvalds/linux-2.6.git
 * Use the v2.6.27 tag.
 *
 * Below is the original's header including its copyright
 *
 * linux/arch/arm/plat-omap/gpio.c
 *
 * Support functions for OMAP GPIO
 *
 * Copyright (C) 2003-2005 Nokia Corporation
 * Written by Juha Yrjölä <juha.yrjola@nokia.com>
 *
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 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 */
```

```
./arch/arm/include/asm/omap_musb.h
omap_musb.h
```

```
/*
 * Board data structure for musb gadget on OMAPs
 *
 * Copyright (C) 2012, Ilya Yanok <ilya.yanok@gmail.com>
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 *
```

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* MERCHANTABILITY or FITNESS FOR A PARTICULAR /PURPOSE. See the  
* GNU General Public License for more details.  
*/
```

```
./arch/arm/include/asm/pl310.h
```

```
pl310.h
```

```
/*  
* (C) Copyright 2010  
* Texas Instruments, <www.ti.com>  
* Aneesh V <aneesh@ti.com>  
*  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/arm/include/asm/posix_types.h
```

```
posix_types.h
```

```
/*  
* linux/include/asm-arm/posix_types.h  
*  
* Copyright (C) 1996-1998 Russell King.  
*  
* This program is free software; you can redistribute it and/or modify  
* it under the terms of the GNU General Public License version 2 as  
* published by the Free Software Foundation.  
*  
* Changelog:  
* 27-06-1996 RMK Created  
*/
```

```
./arch/arm/include/asm/proc-armv/domain.h
```

```
domain.h
```

```
/*
```

```

* linux/include/asm-arm/proc-armv/domain.h
*
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*
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* it under the terms of the GNU General Public License version 2 as
* published by the Free Software Foundation.
*/
/*
* Domain numbers
*
* DOMAIN_IO      - domain 2 includes all IO only
* DOMAIN_KERNEL - domain 1 includes all kernel memory only
* DOMAIN_USER    - domain 0 includes all user memory only
*/

./arch/arm/include/asm/proc-armv/processor.h
processor.h
/*
* linux/include/asm-arm/proc-armv/processor.h
*
* Copyright (C) 1996-1999 Russell King.
*
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* it under the terms of the GNU General Public License version 2 as
* published by the Free Software Foundation.
*
* Changelog:
* 20-09-1996 RMK      Created
* 26-09-1996 RMK      Added 'EXTRA_THREAD_STRUCT*'
* 28-09-1996 RMK      Moved start_thread into the processor dependencies
* 09-09-1998 PJB      Delete redundant `wp_works_ok'
* 30-05-1999 PJB      Save sl across context switches
* 31-07-1999 RMK      Added 'domain' stuff
*/

./arch/arm/include/asm/proc-armv/ptrace.h
ptrace.h
/*
* linux/include/asm-arm/proc-armv/ptrace.h
*
* Copyright (C) 1996-1999 Russell King
*
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* it under the terms of the GNU General Public License version 2 as
* published by the Free Software Foundation.
*/

./arch/arm/include/asm/proc-armv/system.h
system.h

```

```
/*
 * linux/include/asm-arm/proc-armv/system.h
 *
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 */
```

```
./arch/arm/include/asm/processor.h
processor.h
```

```
/*
 * linux/include/asm-arm/processor.h
 *
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 * published by the Free Software Foundation.
 */
```

```
./arch/arm/include/asm/ptrace.h
ptrace.h
/* options set using PTRACE_SETOPTIONS */
```

```
./arch/arm/include/asm/setup.h
setup.h
/*
 * linux/include/asm/setup.h
 *
 * Copyright (C) 1997-1999 Russell King
 *
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 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 *
 * Structure passed to kernel to tell it about the
 * hardware it's running on. See linux/Documentation/arm/Setup
 * for more info.
 *
 * NOTE:
 * This file contains two ways to pass information from the boot
 * loader to the kernel. The old struct param_struct is deprecated,
 * but it will be kept in the kernel for 5 years from now
 * (2001). This will allow boot loaders to convert to the new struct
 * tag way.
 */
```

```
./arch/arm/include/asm/sizes.h
```

sizes.h

```
/*
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 * (at your option) any later version.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
 */
/* Size defintions
 * Copyright (C) ARM Limited 1998. All rights reserved.
 */
```

./arch/arm/include/asm/spl.h

spl.h

```
/*
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 * Texas Instruments, <www.ti.com>
 *
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 * project.
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/arm/include/asm/string.h

string.h

```
/*
 * We don't do inline string functions, since the
 * optimised inline asm versions are not small.
```

```
*/

./arch/arm/include/asm/system.h
system.h
No license information found.

./arch/arm/include/asm/types.h
types.h
/*
 * __xx is ok: it doesn't pollute the POSIX namespace. Use these in the
 * header files exported to user space
 */

./arch/arm/include/asm/u-boot-arm.h
u-boot-arm.h
/*
 * (C) Copyright 2002
 * Sysgo Real-Time Solutions, GmbH <www.elinos.com>
 * Marius Groeger <mgroeger@sysgo.de>
 *
 * (C) Copyright 2002
 * Sysgo Real-Time Solutions, GmbH <www.elinos.com>
 * Alex Zuepke <azu@sysgo.de>
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */

./arch/arm/include/asm/u-boot.h
u-boot.h
/*
 * (C) Copyright 2002
 * Sysgo Real-Time Solutions, GmbH <www.elinos.com>
 * Marius Groeger <mgroeger@sysgo.de>
 *
```

```
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* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Alex Zuepke <azu@sysgo.de>
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* See file CREDITS for list of people who contributed to this
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*****
* NOTE: This header file defines an interface to U-Boot. Including
* this (unmodified) header file in another file is considered normal
* use of U-Boot, and does *not* fall under the heading of "derived
* work".
*****
*/
```

```
./arch/arm/include/asm/unaligned.h
unaligned.h
/*
* Select endianness
*/
```

```
./arch/arm/include/asm/utils.h
utils.h
/*
* (C) Copyright 2010
* Texas Instruments, <www.ti.com>
* Aneesh V <aneesh@ti.com>
*
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* project.
*
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```

```
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/lib/board.c
board.c
```

```
/*
* (C) Copyright 2002-2006
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* (C) Copyright 2002
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/lib/bootm.c
bootm.c
```

```
/* Copyright (C) 2011
* Corscience GmbH & Co. KG - Simon Schwarz <schwarz@corscience.de>
* - Added prep subcommand support
* - Reorganized source - modeled after powerpc version
*
* (C) Copyright 2002
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
```

```
* Marius Groeger <mgroeger@sysgo.de>
*
* Copyright (C) 2001 Erik Mouw (J.A.K.Mouw@its.tudelft.nl)
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*
*/
```

```
./arch/arm/lib/cache-cp15.c
cache-cp15.c
```

```
/*
* (C) Copyright 2002
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
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*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/arm/lib/cache-pl310.c
cache-pl310.c
```

```
/*
* (C) Copyright 2010
* Texas Instruments, <www.ti.com>
```

```
* Aneesh V <aneesh@ti.com>
*
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* MA 02111-1307 USA
*/
```

```
./arch/arm/lib/cache.c
```

```
cache.c
```

```
/*
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*
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* MA 02111-1307 USA
*/
```

```
./arch/arm/lib/div0.c
```

```
div0.c
```

```
/*
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```

```
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* MA 02111-1307 USA  
*/
```

```
./arch/arm/lib/eabi_compat.c  
eabi_compat.c
```

```
/*  
* Utility functions needed for (some) EABI conformant tool chains.  
*  
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*/
```

```
./arch/arm/lib/interrupts.c  
interrupts.c
```

```
/*  
* (C) Copyright 2003  
* Texas Instruments <www.ti.com>  
*  
* (C) Copyright 2002  
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>  
* Marius Groeger <mgroeger@sysgo.de>  
*  
* (C) Copyright 2002  
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>  
* Alex Zuepke <azu@sysgo.de>  
*  
* (C) Copyright 2002-2004  
* Gary Jennejohn, DENX Software Engineering, <garyj@denx.de>
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/arm/lib/reset.c
```

```
reset.c
```

```
/*
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* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*
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*/
```

```
./arch/arm/lib/spl.c
```

```
spl.c
```

```
/*
* (C) Copyright 2010-2012
* Texas Instruments, <www.ti.com>
*
* Aneesh V <aneesh@ti.com>
* Tom Rini <trini@ti.com>
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*/
```

```
./arch/avr32/cpu/at32ap700x/clk.c
```

```
clk.c
```

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*
*/
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*/
```

```
./arch/avr32/cpu/at32ap700x/mmu.c
mmu.c
No license information found.
```

```
./arch/avr32/cpu/at32ap700x/portmux.c
portmux.c
/*
```

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*/
```

```
./arch/avr32/cpu/at32ap700x/sm.h
sm.h
/*
```

```
* Register definitions for System Manager
*/
/* SM register offsets */
```

```
./arch/avr32/cpu/cache.c  
cache.c
```

```
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 */
```

```
./arch/avr32/cpu/cpu.c  
cpu.c
```

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 */
```

```
./arch/avr32/cpu/exception.c
```

exception.c

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 */
```

./arch/avr32/cpu/hsdramc.c

hsdramc.c

```
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 * MA 02111-1307 USA
 */
```

./arch/avr32/cpu/hsdramc1.h

hsdramc1.h

```
/*
```

```

* Register definitions for SDRAM Controller
*/
/* HSDRAMC1 register offsets */

./arch/avr32/cpu/hsmc3.h
hsmc3.h
/*
* Register definitions for Static Memory Controller
*/
/* HSMC3 register offsets */

./arch/avr32/cpu/interrupts.c
interrupts.c
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*/

./arch/avr32/cpu/pio2.h
pio2.h
/*
* Register definitions for Parallel Input/Output Controller
*/
/* PIO2 register offsets */

./arch/avr32/cpu/portmux-gpio.c
portmux-gpio.c
/*
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*

```

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```

```
./arch/avr32/cpu/portmux-pio.c
portmux-pio.c
```

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*/
```

```
./arch/avr32/include/asm/arch-at32ap700x/addrspace.h
addrspace.h
```

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* MA 02111-1307 USA
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```

```
./arch/avr32/include/asm/arch-at32ap700x/cacheflush.h
cacheflush.h
```

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*/
```

```
./arch/avr32/include/asm/arch-at32ap700x/chip-features.h
chip-features.h
```

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```

```
./arch/avr32/include/asm/arch-at32ap700x/clk.h
clk.h
```

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*/
```

```
./arch/avr32/include/asm/arch-at32ap700x/gpio-impl.h
gpio-impl.h
/* Register offsets */
```

```
./arch/avr32/include/asm/arch-at32ap700x/gpio.h
gpio.h
```

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```

```
./arch/avr32/include/asm/arch-at32ap700x/hardware.h
hardware.h
```

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```

```
./arch/avr32/include/asm/arch-at32ap700x/hmatrix.h
hmatrix.h
```

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*/
```

```
./arch/avr32/include/asm/arch-at32ap700x/mmu.h
mmu.h
```

```
/*
* In order to deal with the hardcoded u-boot requirement that virtual
* addresses are always mapped 1:1 with physical addresses, we implement
* a small virtual memory manager so that we can use the MMU hardware in
* order to get the caching properties right.
*
* A few pages (or possibly just one) are locked in the TLB permanently
* in order to avoid recursive TLB misses, but most pages are faulted in
* on demand.
*/
```

```
./arch/avr32/include/asm/arch-at32ap700x/portmux.h
portmux.h
```

```
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```
./arch/avr32/include/asm/arch-common/portmux-gpio.h
```

portmux-gpio.h

```
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 */
```

./arch/avr32/include/asm/arch-common/portmux-pio.h

portmux-pio.h

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./arch/avr32/include/asm/bitops.h

bitops.h

```
/*
```

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```

```
./arch/avr32/include/asm/byteorder.h
```

```
byteorder.h
```

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```

```
./arch/avr32/include/asm/cache.h
```

```
cache.h
```

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*/
```

```
./arch/avr32/include/asm/config.h  
config.h
```

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*/
```

```
./arch/avr32/include/asm/dma-mapping.h  
dma-mapping.h
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/avr32/include/asm/errno.h
errno.h
No license information found.
```

```
./arch/avr32/include/asm/global_data.h
global_data.h
/*
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/avr32/include/asm/hmatrix-common.h
hmatrix-common.h
/*
```

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*
```

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*/
```

```
./arch/avr32/include/asm/io.h
```

```
io.h
```

```
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*/
```

```
./arch/avr32/include/asm/posix_types.h
```

```
posix_types.h
```

```
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*/
```

```
./arch/avr32/include/asm/processor.h
processor.h
```

```
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*/
```

```
./arch/avr32/include/asm/ptrace.h
ptrace.h
```

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* MA 02111-1307 USA
*/
```

```
./arch/avr32/include/asm/sdram.h
sdram.h
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/avr32/include/asm/sections.h
sections.h
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/avr32/include/asm/setup.h
setup.h
```

```
/*
* Copyright (C) 2004-2006 Atmel Corporation
*
* Based on linux/include/asm-arm/setup.h
* Copyright (C) 1997-1999 Russel King
*
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* project.
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* MA 02111-1307 USA
*/
```

```
./arch/avr32/include/asm/string.h
string.h
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/avr32/include/asm/sysreg.h  
sysreg.h  
/*  
 * System registers for AVR32  
 */  
/* system register offsets */
```

```
./arch/avr32/include/asm/types.h  
types.h  
/*  
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
 * MA 02111-1307 USA  
*/
```

```
./arch/avr32/include/asm/u-boot.h  
u-boot.h  
/*  
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 *
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/avr32/include/asm/unaligned.h
unaligned.h
No license information found.
```

```
./arch/avr32/lib/board.c
board.c
```

```
/*
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./arch/avr32/lib/bootm.c
bootm.c
```

```
/*
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```

```
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* MA 02111-1307 USA  
*/
```

```
./arch/avr32/lib/interrupts.c
```

```
interrupts.c
```

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* MA 02111-1307 USA  
*/
```

```
./arch/blackfin/cpu/cpu.c
```

```
cpu.c
```

```
/*  
* U-boot - cpu.c CPU specific functions  
*  
* Copyright (c) 2005-2008 Analog Devices Inc.  
*  
* (C) Copyright 2000-2004
```

```
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
* Licensed under the GPL-2 or later.  
*/
```

```
./arch/blackfin/cpu/cpu.h
```

```
cpu.h
```

```
/*  
 * U-boot - cpu.h  
 *  
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 * Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,  
 * MA 02110-1301 USA  
 */
```

```
./arch/blackfin/cpu/gpio.c
```

```
gpio.c
```

```
/*  
 * GPIO Abstraction Layer  
 *  
 * Copyright 2006-2010 Analog Devices Inc.  
 *  
 * Licensed under the GPL-2 or later  
 */
```

```
./arch/blackfin/cpu/initcode.c
```

```
initcode.c
```

```
/*  
 * initcode.c - Initialize the processor. This is usually entails things  
 * like external memory, voltage regulators, etc... Note that this file  
 * cannot make any function calls as it may be executed all by itself by  
 * the Blackfin's bootrom in LDR format.  
 *  
 * Copyright (c) 2004-2011 Analog Devices Inc.
```

```
*
* Licensed under the GPL-2 or later.
*/

./arch/blackfin/cpu/initcode.h
initcode.h
/*
* Code for early processor initialization
*
* Copyright (c) 2004-2011 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/

./arch/blackfin/cpu/interrupts.c
interrupts.c
/*
* U-boot - interrupts.c Interrupt related routines
*
* Copyright (c) 2005-2008 Analog Devices Inc.
*
* This file is based on interrupts.c
* Copyright 1996 Roman Zippel
* Copyright 1999 D. Jeff Dionne <jeff@uclinux.org>
* Copyright 2000-2001 Lineo, Inc. D. Jeff Dionne <jeff@lineo.ca>
* Copyright 2002 Arcturus Networks Inc. MaTed <mated@sympatico.ca>
* Copyright 2003 Metrowerks/Motorola
* Copyright 2003 Bas Vermeulen <bas@buyways.nl>,
*           BuyWays B.V. (www.buyways.nl)
*
* (C) Copyright 2000-2004
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* Licensed under the GPL-2 or later.
*/

./arch/blackfin/cpu/jtag-console.c
jtag-console.c
/*
* jtag-console.c - console driver over Blackfin JTAG
*
* Copyright (c) 2008-2010 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/

./arch/blackfin/cpu/os_log.c
os_log.c
/*
* functions for handling OS log buffer
```

```

*
* Copyright (c) 2009 Analog Devices Inc.
*
* Licensed under the 2-clause BSD.
*/

./arch/blackfin/cpu/reset.c
reset.c
/*
* reset.c - logic for resetting the cpu
*
* Copyright (c) 2005-2008 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/

./arch/blackfin/cpu/serial.c
serial.c
/*
* U-boot - serial.c Blackfin Serial Driver
*
* Copyright (c) 2005-2008 Analog Devices Inc.
*
* Copyright (c) 2003 Bas Vermeulen <bas@buyways.nl>,
* BuyWays B.V. (www.buyways.nl)
*
* Based heavily on:
* blkfinserial.c: Serial driver for BlackFin DSP internal USRTs.
* Copyright(c) 2003 Metrowerks <mwaddel@metrowerks.com>
* Copyright(c) 2001 Tony Z. Kou <tonyko@arcturusnetworks.com>
* Copyright(c) 2001-2002 Arcturus Networks Inc. <www.arcturusnetworks.com>
*
* Based on code from 68328 version serial driver implementation which was:
* Copyright (C) 1995 David S. Miller <davem@caip.rutgers.edu>
* Copyright (C) 1998 Kenneth Albanowski <kjahds@kjahds.com>
* Copyright (C) 1998, 1999 D. Jeff Dionne <jeff@uclinux.org>
* Copyright (C) 1999 Vladimir Gurevich <vgurevic@cisco.com>
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*
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*/

./arch/blackfin/cpu/serial.h
serial.h
/*
* serial.h - common serial defines for early debug and serial driver.
*
* any functions defined here must be always_inline since
*
* initcode cannot have function calls.

```

```

*
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*/

./arch/blackfin/cpu/traps.c
traps.c
/*
* U-boot - traps.c Routines related to interrupts and exceptions
*
* Copyright (c) 2005-2008 Analog Devices Inc.
*
* This file is based on
* No original Copyright holder listed,
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*/

./arch/blackfin/cpu/watchdog.c
watchdog.c
/*
* watchdog.c - driver for Blackfin on-chip watchdog
*
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*
* Licensed under the GPL-2 or later.
*/

./arch/blackfin/include/asm/bfin_logo_230x230_gzip.h
bfin_logo_230x230_gzip.h
/*
* Generated by EasyLogo, (C) 2000 by Paolo Scaffardi
*
* To use this, include it and call: easylogo_plot(screen,&bfin_logo, width,x,y)
*
* Where:      'screen'      is the pointer to the frame buffer
*             'width' is the screen width
*             'x'           is the horizontal position
*             'y'           is the vertical position
*/

```

```
./arch/blackfin/include/asm/bfin_logo_230x230_lzma.h
bfin_logo_230x230_lzma.h
/*
 * Generated by EasyLogo, (C) 2000 by Paolo Scaffardi
 *
 * To use this, include it and call: easylogo_plot(screen,&bfin_logo, width,x,y)
 *
 * Where:      'screen'      is the pointer to the frame buffer
 *             'width' is the screen width
 *             'x'          is the horizontal position
 *             'y'          is the vertical position
 */
```

```
./arch/blackfin/include/asm/bfin_logo_rgb565_230x230_gzip.h
bfin_logo_rgb565_230x230_gzip.h
/*
 * Generated by EasyLogo, (C) 2000 by Paolo Scaffardi
 *
 * To use this, include it and call: easylogo_plot(screen,&bfin_logo, width,x,y)
 *
 * Where:      'screen'      is the pointer to the frame buffer
 *             'width' is the screen width
 *             'x'          is the horizontal position
 *             'y'          is the vertical position
 */
```

```
./arch/blackfin/include/asm/bfin_logo_rgb565_230x230_lzma.h
bfin_logo_rgb565_230x230_lzma.h
/*
 * Generated by EasyLogo, (C) 2000 by Paolo Scaffardi
 *
 * To use this, include it and call: easylogo_plot(screen,&bfin_logo, width,x,y)
 *
 * Where:      'screen'      is the pointer to the frame buffer
 *             'width' is the screen width
 *             'x'          is the horizontal position
 *             'y'          is the vertical position
 */
```

```
./arch/blackfin/include/asm/bitops.h
bitops.h
/*
 * U-boot - bitops.h Routines for bit operations
 *
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* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./arch/blackfin/include/asm/blackfin.h
blackfin.h
```

```
/* DO NOT EDIT THIS FILE
* Automatically generated by toolchain/trunk/proc-defs/sh/create-arch-headers.sh
* DO NOT EDIT THIS FILE
*/
```

```
./arch/blackfin/include/asm/blackfin_cdef.h
blackfin_cdef.h
```

```
/* DO NOT EDIT THIS FILE
* Automatically generated by toolchain/trunk/proc-defs/sh/create-arch-headers.sh
* DO NOT EDIT THIS FILE
*/
```

```
./arch/blackfin/include/asm/blackfin_def.h
blackfin_def.h
```

```
/* DO NOT EDIT THIS FILE
* Automatically generated by toolchain/trunk/proc-defs/sh/create-arch-headers.sh
* DO NOT EDIT THIS FILE
*/
```

```
./arch/blackfin/include/asm/blackfin_local.h
blackfin_local.h
```

```
/*
* U-boot - blackfin_local.h
*
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* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./arch/blackfin/include/asm/byteorder.h
byteorder.h
```

```
/*
* U-boot - byteorder.h
*
* Copyright (c) 2005-2007 Analog Devices Inc.
*
* (C) Copyright 2000-2004
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./arch/blackfin/include/asm/cache.h
cache.h
```

```
/*
* Copyright 2004-2009 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/
/*
* Bytes per L1 cache line
```

```
* Blackfin loads 32 bytes for cache
*/

./arch/blackfin/include/asm/config-pre.h
config-pre.h
/*
 * config-pre.h - common defines for Blackfin boards in config.h
 *
 * Copyright (c) 2007-2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */
/* Misc helper functions */

./arch/blackfin/include/asm/config.h
config.h
/*
 * config.h - setup common defines for Blackfin boards based on config.h
 *
 * Copyright (c) 2007-2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */
/* Some of our defines use this (like CONFIG_SYS_GBL_DATA_ADDR) */

./arch/blackfin/include/asm/cplb.h
cplb.h
/*
 * cplb.h - defines for managing CPLB tables
 *
 * Copyright (c) 2002-2007 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */

./arch/blackfin/include/asm/deferred.h
deferred.h
/*
 * U-boot - deferred register layout
 *
 * Copyright 2004-2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */

./arch/blackfin/include/asm/delay.h
delay.h
/*
 * U-boot - delay.h Routines for introducing delays
 *
```

```
* Copyright (c) 2005-2007 Analog Devices Inc.
*
* See file CREDITS for list of people who contributed to this
* project.
*
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* modify it under the terms of the GNU General Public License as
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*
* You should have received a copy of the GNU General Public License
* along with this program; if not, write to the Free Software
* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./arch/blackfin/include/asm/dma.h
```

```
dma.h
```

```
/*
 * dma.h - Blackfin DMA defines/structures/etc...
 *
 * Copyright 2004-2008 Analog Devices Inc.
 * Licensed under the GPL-2 or later.
 */
```

```
./arch/blackfin/include/asm/entry.h
```

```
entry.h
```

```
/*
 * entry.h - routines for context saving and restoring (for interrupts/exceptions)
 *
 * Copyright (c) 2005-2007 Analog Devices Inc.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
 * This program is free software; you can redistribute it and/or
 * modify it under the terms of the GNU General Public License as
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 * the License, or (at your option) any later version.
 *
 * This program is distributed in the hope that it will be useful,
 * but WITHOUT ANY WARRANTY; without even the implied warranty of
 * MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
 * GNU General Public License for more details.
 */
```

```
* You should have received a copy of the GNU General Public License
* along with this program; if not, write to the Free Software
* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./arch/blackfin/include/asm/errno.h
errno.h
No license information found.
```

```
./arch/blackfin/include/asm/global_data.h
global_data.h
/*
 * U-boot - global_data.h Declarations for global data of u-boot
 *
 * Copyright (c) 2005-2007 Analog Devices Inc.
 *
 * (C) Copyright 2000-2010
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
 * This program is free software; you can redistribute it and/or
 * modify it under the terms of the GNU General Public License as
 * published by the Free Software Foundation; either version 2 of
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 * GNU General Public License for more details.
 *
 * You should have received a copy of the GNU General Public License
 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
 * MA 02110-1301 USA
*/
```

```
./arch/blackfin/include/asm/gpio.h
gpio.h
/*
 * Copyright 2006-2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
*/
```

```
./arch/blackfin/include/asm/io.h
io.h
/*
```

```

* U-boot - io.h IO routines
*
* Copyright 2004-2009 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/

./arch/blackfin/include/asm/linkage.h
linkage.h
/*
* U-boot - linkage.h
*
* Copyright (c) 2005-2007 Analog Devices Inc.
*
* See file CREDITS for list of people who contributed to this
* project.
*
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* modify it under the terms of the GNU General Public License as
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*
* You should have received a copy of the GNU General Public License
* along with this program; if not, write to the Free Software
* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
* MA 02110-1301 USA
*/

./arch/blackfin/include/asm/mach-bf506/anomaly.h
anomaly.h
/*
* DO NOT EDIT THIS FILE
* This file is under version control at
*   svn://sources.blackfin.uclinux.org/toolchain/trunk/proc-defs/header-frags/
* and can be replaced with that version at any time
* DO NOT EDIT THIS FILE
*
* Copyright 2004-2011 Analog Devices Inc.
* Licensed under the ADI BSD license.
*   https://docs.blackfin.uclinux.org/doku.php?id=adi_bsd
*/
/* This file should be up to date with:
* - Revision A, 02/18/2011; ADSP-BF504/BF504F/BF506F Blackfin Processor Anomaly
List
*/

```

```
./arch/blackfin/include/asm/mach-bf506/BF504_cdef.h
BF504_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf506/BF504_def.h
BF504_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf506/BF506_cdef.h
BF506_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf506/BF506_def.h
BF506_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf506/def_local.h
def_local.h
No license information found.
```

```
./arch/blackfin/include/asm/mach-bf506/gpio.h
gpio.h
/*
 * Copyright (C) 2008 Analog Devices Inc.
 * Licensed under the GPL-2 or later.
 */
```

```
./arch/blackfin/include/asm/mach-bf506/portmux.h
portmux.h
/*
 * Copyright 2008-2010 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later
 */
/* PPI Port Mux */
```

```
./arch/blackfin/include/asm/mach-bf506/ports.h
```

```

ports.h
/*
 * Port Masks
 */
/* PORTx_MUX Masks */

./arch/blackfin/include/asm/mach-bf518/anomaly.h
anomaly.h
/*
 * DO NOT EDIT THIS FILE
 * This file is under version control at
 *   svn://sources.blackfin.uclinux.org/toolchain/trunk/proc-defs/header-frags/
 * and can be replaced with that version at any time
 * DO NOT EDIT THIS FILE
 *
 * Copyright 2004-2011 Analog Devices Inc.
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 *   https://docs.blackfin.uclinux.org/doku.php?id=adi_bsd
 */
/* This file should be up to date with:
 * - Revision F, 05/23/2011; ADSP-BF512/BF514/BF516/BF518 Blackfin Processor
Anomaly List
 */

./arch/blackfin/include/asm/mach-bf518/BF512_cdef.h
BF512_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf518/BF512_def.h
BF512_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf518/BF514_cdef.h
BF514_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf518/BF514_def.h
BF514_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */

```

```
*/

./arch/blackfin/include/asm/mach-bf518/BF516_cdef.h
BF516_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf518/BF516_def.h
BF516_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf518/BF518_cdef.h
BF518_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf518/BF518_def.h
BF518_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf518/def_local.h
def_local.h
No license information found.

./arch/blackfin/include/asm/mach-bf518/gpio.h
gpio.h
/*
 * Copyright (C) 2008 Analog Devices Inc.
 * Licensed under the GPL-2 or later.
 */

./arch/blackfin/include/asm/mach-bf518/portmux.h
portmux.h
/*
 * Copyright 2008-2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later
 */
/* EMAC MII/RMII Port Mux */
```

./arch/blackfin/include/asm/mach-bf518/ports.h

ports.h

/*

* Port Masks

*/

/* PORTx_MUX Masks */

./arch/blackfin/include/asm/mach-bf527/anomaly.h

anomaly.h

/*

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* [svn://sources.blackfin.uclinux.org/toolchain/trunk/proc-defs/header-frags/](https://sources.blackfin.uclinux.org/toolchain/trunk/proc-defs/header-frags/)

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*/

/* This file should be up to date with:

* - Revision F, 05/23/2011; ADSP-BF526 Blackfin Processor Anomaly List

* - Revision I, 05/23/2011; ADSP-BF527 Blackfin Processor Anomaly List

*/

./arch/blackfin/include/asm/mach-bf527/BF522_cdef.h

BF522_cdef.h

/* DO NOT EDIT THIS FILE

* Automatically generated by generate-cdef-headers.xsl

* DO NOT EDIT THIS FILE

*/

./arch/blackfin/include/asm/mach-bf527/BF522_def.h

BF522_def.h

/* DO NOT EDIT THIS FILE

* Automatically generated by generate-def-headers.xsl

* DO NOT EDIT THIS FILE

*/

./arch/blackfin/include/asm/mach-bf527/BF523_cdef.h

BF523_cdef.h

No license information found.

./arch/blackfin/include/asm/mach-bf527/BF523_def.h

BF523_def.h

No license information found.

./arch/blackfin/include/asm/mach-bf527/BF524_cdef.h

BF524_cdef.h

/* DO NOT EDIT THIS FILE

```
* Automatically generated by generate-cdef-headers.xml
* DO NOT EDIT THIS FILE
*/
```

```
./arch/blackfin/include/asm/mach-bf527/BF524_def.h
BF524_def.h
/* DO NOT EDIT THIS FILE
* Automatically generated by generate-def-headers.xml
* DO NOT EDIT THIS FILE
*/
```

```
./arch/blackfin/include/asm/mach-bf527/BF525_cdef.h
BF525_cdef.h
No license information found.
```

```
./arch/blackfin/include/asm/mach-bf527/BF525_def.h
BF525_def.h
No license information found.
```

```
./arch/blackfin/include/asm/mach-bf527/BF526_cdef.h
BF526_cdef.h
/* DO NOT EDIT THIS FILE
* Automatically generated by generate-cdef-headers.xml
* DO NOT EDIT THIS FILE
*/
```

```
./arch/blackfin/include/asm/mach-bf527/BF526_def.h
BF526_def.h
/* DO NOT EDIT THIS FILE
* Automatically generated by generate-def-headers.xml
* DO NOT EDIT THIS FILE
*/
```

```
./arch/blackfin/include/asm/mach-bf527/BF527_cdef.h
BF527_cdef.h
No license information found.
```

```
./arch/blackfin/include/asm/mach-bf527/BF527_def.h
BF527_def.h
No license information found.
```

```
./arch/blackfin/include/asm/mach-bf527/def_local.h
def_local.h
No license information found.
```

```
./arch/blackfin/include/asm/mach-bf527/gpio.h
gpio.h
/*
* Copyright (C) 2008 Analog Devices Inc.
* Licensed under the GPL-2 or later.
```

```

*/

./arch/blackfin/include/asm/mach-bf527/mem_map.h
mem_map.h
/*
 * Common Blackfin memory map
 *
 * Copyright 2004-2009 Analog Devices Inc.
 * Licensed under the GPL-2 or later.
 */

./arch/blackfin/include/asm/mach-bf527/portmux.h
portmux.h
/*
 * Copyright 2007-2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later
 */

./arch/blackfin/include/asm/mach-bf527/ports.h
ports.h
/*
 * Port Masks
 */
/* PORTx_MUX Masks */

./arch/blackfin/include/asm/mach-bf533/anomaly.h
anomaly.h
/*
 * DO NOT EDIT THIS FILE
 * This file is under version control at
 *   svn://sources.blackfin.uclinux.org/toolchain/trunk/proc-defs/header-fragments/
 * and can be replaced with that version at any time
 * DO NOT EDIT THIS FILE
 *
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 *   https://docs.blackfin.uclinux.org/doku.php?id=adi_bsd
 */
/* This file should be up to date with:
 * - Revision G, 05/23/2011; ADSP-BF531/BF532/BF533 Blackfin Processor Anomaly
List
 */

./arch/blackfin/include/asm/mach-bf533/BF531_cdef.h
BF531_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xsl
 * DO NOT EDIT THIS FILE
 */

```

```
./arch/blackfin/include/asm/mach-bf533/BF531_def.h
BF531_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf533/BF532_cdef.h
BF532_cdef.h
No license information found.
```

```
./arch/blackfin/include/asm/mach-bf533/BF532_def.h
BF532_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf533/BF533_cdef.h
BF533_cdef.h
No license information found.
```

```
./arch/blackfin/include/asm/mach-bf533/BF533_def.h
BF533_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf533/def_local.h
def_local.h
No license information found.
```

```
./arch/blackfin/include/asm/mach-bf533/gpio.h
gpio.h
/*
 * Copyright (C) 2008 Analog Devices Inc.
 * Licensed under the GPL-2 or later.
 */
```

```
./arch/blackfin/include/asm/mach-bf533/portmux.h
portmux.h
/*
 * Copyright 2007-2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later
 */
```

```
./arch/blackfin/include/asm/mach-bf533/ports.h
```

```
ports.h
/*
 * Port Masks
 */

./arch/blackfin/include/asm/mach-bf537/anomaly.h
anomaly.h
/*
 * DO NOT EDIT THIS FILE
 * This file is under version control at
 *   svn://sources.blackfin.uclinux.org/toolchain/trunk/proc-defs/header-frags/
 * and can be replaced with that version at any time
 * DO NOT EDIT THIS FILE
 *
 * Copyright 2004-2011 Analog Devices Inc.
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 *   https://docs.blackfin.uclinux.org/doku.php?id=adi_bsd
 */
/* This file should be up to date with:
 * - Revision F, 05/23/2011; ADSP-BF534/ADSP-BF536/ADSP-BF537 Blackfin Processor
Anomaly List
 */

./arch/blackfin/include/asm/mach-bf537/BF534_cdef.h
BF534_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf537/BF534_def.h
BF534_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf537/BF536_cdef.h
BF536_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf537/BF536_def.h
BF536_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf537/BF537_cdef.h
BF537_cdef.h
No license information found.
```

```
./arch/blackfin/include/asm/mach-bf537/BF537_def.h
BF537_def.h
No license information found.
```

```
./arch/blackfin/include/asm/mach-bf537/def_local.h
def_local.h
No license information found.
```

```
./arch/blackfin/include/asm/mach-bf537/gpio.h
gpio.h
/*
 * Copyright (C) 2008 Analog Devices Inc.
 * Licensed under the GPL-2 or later.
 */
```

```
./arch/blackfin/include/asm/mach-bf537/portmux.h
portmux.h
/*
 * Copyright 2007-2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later
 */
```

```
./arch/blackfin/include/asm/mach-bf537/ports.h
ports.h
/*
 * Port Masks
 */
/* PORT_MUX Masks */
```

```
./arch/blackfin/include/asm/mach-bf538/anomaly.h
anomaly.h
/*
 * DO NOT EDIT THIS FILE
 * This file is under version control at
 *   svn://sources.blackfin.uclinux.org/toolchain/trunk/proc-defs/header-frags/
 * and can be replaced with that version at any time
 * DO NOT EDIT THIS FILE
 *
 * Copyright 2004-2011 Analog Devices Inc.
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 *   https://docs.blackfin.uclinux.org/doku.php?id=adi_bsd
 */
/* This file should be up to date with:
 * - Revision J, 05/23/2011; ADSP-BF538/BF538F Blackfin Processor Anomaly List
```

```
* - Revision 0, 05/23/2011; ADSP-BF539/BF539F Blackfin Processor Anomaly List
*/
```

```
./arch/blackfin/include/asm/mach-bf538/BF538_cdef.h
BF538_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf538/BF538_def.h
BF538_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf538/BF539_cdef.h
BF539_cdef.h
No license information found.
```

```
./arch/blackfin/include/asm/mach-bf538/BF539_def.h
BF539_def.h
No license information found.
```

```
./arch/blackfin/include/asm/mach-bf538/def_local.h
def_local.h
No license information found.
```

```
./arch/blackfin/include/asm/mach-bf538/gpio.h
gpio.h
/*
 * Copyright (C) 2008-2009 Analog Devices Inc.
 * Licensed under the GPL-2 or later.
 */
```

```
./arch/blackfin/include/asm/mach-bf538/portmux.h
portmux.h
/*
 * Copyright 2008-2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */
```

```
./arch/blackfin/include/asm/mach-bf538/ports.h
ports.h
/*
 * Port Masks
 */
```

```
./arch/blackfin/include/asm/mach-bf548/ADSP-EDN-BF542-extended_cdef.h
ADSP-EDN-BF542-extended_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf548/ADSP-EDN-BF542-extended_def.h
ADSP-EDN-BF542-extended_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf548/ADSP-EDN-BF544-extended_cdef.h
ADSP-EDN-BF544-extended_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf548/ADSP-EDN-BF544-extended_def.h
ADSP-EDN-BF544-extended_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf548/ADSP-EDN-BF547-extended_cdef.h
ADSP-EDN-BF547-extended_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf548/ADSP-EDN-BF547-extended_def.h
ADSP-EDN-BF547-extended_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf548/ADSP-EDN-BF548-extended_cdef.h
ADSP-EDN-BF548-extended_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf548/ADSP-EDN-BF548-extended_def.h
```

ADSP-EDN-BF548-extended_def.h

/* DO NOT EDIT THIS FILE

* Automatically generated by generate-def-headers.xml

* DO NOT EDIT THIS FILE

*/

./arch/blackfin/include/asm/mach-bf548/ADSP-EDN-BF549-extended_cdef.h

ADSP-EDN-BF549-extended_cdef.h

/* DO NOT EDIT THIS FILE

* Automatically generated by generate-cdef-headers.xml

* DO NOT EDIT THIS FILE

*/

./arch/blackfin/include/asm/mach-bf548/ADSP-EDN-BF549-extended_def.h

ADSP-EDN-BF549-extended_def.h

/* DO NOT EDIT THIS FILE

* Automatically generated by generate-def-headers.xml

* DO NOT EDIT THIS FILE

*/

./arch/blackfin/include/asm/mach-bf548/anomaly.h

anomaly.h

/*

* DO NOT EDIT THIS FILE

* This file is under version control at

* [svn://sources.blackfin.uclinux.org/toolchain/trunk/proc-defs/header-frags/](https://sources.blackfin.uclinux.org/toolchain/trunk/proc-defs/header-frags/)

* and can be replaced with that version at any time

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*/

/* This file should be up to date with:

* - Revision K, 05/23/2011; ADSP-BF542/BF544/BF547/BF548/BF549 Blackfin Processor

Anomaly List

*/

./arch/blackfin/include/asm/mach-bf548/BF542_cdef.h

BF542_cdef.h

/* DO NOT EDIT THIS FILE

* Automatically generated by generate-cdef-headers.xml

* DO NOT EDIT THIS FILE

*/

./arch/blackfin/include/asm/mach-bf548/BF542_def.h

BF542_def.h

/* DO NOT EDIT THIS FILE

* Automatically generated by generate-def-headers.xml

* DO NOT EDIT THIS FILE

```
*/

./arch/blackfin/include/asm/mach-bf548/BF544_cdef.h
BF544_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf548/BF544_def.h
BF544_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf548/BF547_cdef.h
BF547_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf548/BF547_def.h
BF547_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf548/BF548_cdef.h
BF548_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf548/BF548_def.h
BF548_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf548/BF549_cdef.h
BF549_cdef.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-cdef-headers.xml
 * DO NOT EDIT THIS FILE
 */
```

```
./arch/blackfin/include/asm/mach-bf548/BF549_def.h
BF549_def.h
/* DO NOT EDIT THIS FILE
 * Automatically generated by generate-def-headers.xml
 * DO NOT EDIT THIS FILE
 */

./arch/blackfin/include/asm/mach-bf548/def_local.h
def_local.h
No license information found.

./arch/blackfin/include/asm/mach-bf548/gpio.h
gpio.h
/*
 * Copyright 2007-2009 Analog Devices Inc.
 * Licensed under the GPL-2 or later.
 */

./arch/blackfin/include/asm/mach-bf548/mem_map.h
mem_map.h
/*
 * Common Blackfin memory map
 *
 * Copyright 2004-2009 Analog Devices Inc.
 * Licensed under the GPL-2 or later.
 */

./arch/blackfin/include/asm/mach-bf548/portmux.h
portmux.h
/*
 * Copyright 2007-2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */

./arch/blackfin/include/asm/mach-bf548/ports.h
ports.h
/*
 * Port Masks
 */
/* PORTx_MUX Masks */

./arch/blackfin/include/asm/mach-bf561/anomaly.h
anomaly.h
/*
 * DO NOT EDIT THIS FILE
 * This file is under version control at
 * svn://sources.blackfin.uclinux.org/toolchain/trunk/proc-defs/header-frags/
 * and can be replaced with that version at any time
```

```
* DO NOT EDIT THIS FILE
*
* Copyright 2004-2011 Analog Devices Inc.
* Licensed under the ADI BSD license.
* https://docs.blackfin.uclinux.org/doku.php?id=adi\_bsd
*/
/* This file should be up to date with:
* - Revision S, 05/23/2011; ADSP-BF561 Blackfin Processor Anomaly List
*/
```

```
./arch/blackfin/include/asm/mach-bf561/BF561_cdef.h
BF561_cdef.h
/* DO NOT EDIT THIS FILE
* Automatically generated by generate-cdef-headers.xsl
* DO NOT EDIT THIS FILE
*/
```

```
./arch/blackfin/include/asm/mach-bf561/BF561_def.h
BF561_def.h
/* DO NOT EDIT THIS FILE
* Automatically generated by generate-def-headers.xsl
* DO NOT EDIT THIS FILE
*/
```

```
./arch/blackfin/include/asm/mach-bf561/def_local.h
def_local.h
No license information found.
```

```
./arch/blackfin/include/asm/mach-bf561/gpio.h
gpio.h
/*
* Copyright (C) 2008 Analog Devices Inc.
* Licensed under the GPL-2 or later.
*/
```

```
./arch/blackfin/include/asm/mach-bf561/portmux.h
portmux.h
/*
* Copyright 2007-2009 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/
```

```
./arch/blackfin/include/asm/mach-bf561/ports.h
ports.h
/*
* Port Masks
*/
/* The non-standard PF16+ */
```

```
./arch/blackfin/include/asm/mach-common/ADSP-EDN-core_cdef.h
```

```
ADSP-EDN-core_cdef.h
```

```
/* DO NOT EDIT THIS FILE
```

```
 * Automatically generated by generate-cdef-headers.xml
```

```
 * DO NOT EDIT THIS FILE
```

```
*/
```

```
./arch/blackfin/include/asm/mach-common/ADSP-EDN-core_def.h
```

```
ADSP-EDN-core_def.h
```

```
/* DO NOT EDIT THIS FILE
```

```
 * Automatically generated by generate-def-headers.xml
```

```
 * DO NOT EDIT THIS FILE
```

```
*/
```

```
./arch/blackfin/include/asm/mach-common/bits/bootrom.h
```

```
bootrom.h
```

```
/*
```

```
 * Boot ROM Entry Points and such
```

```
*/
```

```
/* These Blackfins all have a Boot ROM that is not reusable (at all):
```

```
 * BF531 / BF532 / BF533
```

```
 * BF538 / BF539
```

```
 * BF561
```

```
 * So there is nothing for us to export ;(
```

```
 *
```

```
 * These Blackfins started to roll with the idea that the Boot ROM can
```

```
 * provide useful functions, but still only a few (and not really useful):
```

```
 * BF534 / BF536 / BF537
```

```
 *
```

```
 * Looking forward, Boot ROM's on newer Blackfins have quite a few
```

```
 * nice entry points that are usable at runtime and beyond. We'll
```

```
 * only define known legacy parts (listed above) and otherwise just
```

```
 * assume it's a newer part.
```

```
 *
```

```
 * These entry points are accomplished by placing a small jump table at
```

```
 * the start of the Boot ROM. This way the addresses are fixed forever.
```

```
*/
```

```
./arch/blackfin/include/asm/mach-common/bits/core.h
```

```
core.h
```

```
/*
```

```
 * Misc Core Masks
```

```
*/
```

```
/*
```

```
 * EVT registers (ILAT, IMASK, and IPEND).
```

```
*/
```

```
./arch/blackfin/include/asm/mach-common/bits/dma.h
```

```
dma.h
```

```
/*
```

```
* DMA Masks
*/
/* DMAx_CONFIG, MDMA_yy_CONFIG Masks */

./arch/blackfin/include/asm/mach-common/bits/ebiu.h
ebiu.h
/*
 * EBIU Masks
 */
/* EBIU_AMGCTL Masks */

./arch/blackfin/include/asm/mach-common/bits/emacs.h
emacs.h
/*
 * Ethernet MAC Masks
 */
/* EMAC_OPMODE Masks */

./arch/blackfin/include/asm/mach-common/bits/eppi.h
eppi.h
/*
 * Enhanced PPI (EPPI)
 */
/* Bit masks for EPPIx_STATUS */

./arch/blackfin/include/asm/mach-common/bits/lockbox.h
lockbox.h
/*
 * Lockbox/Security Masks
 */
/* SESR argument structure. Expected to reside at 0xFF900018. */

./arch/blackfin/include/asm/mach-common/bits/mpu.h
mpu.h
/*
 * MPU Masks
 */
/*
 * DMEM_CONTROL Register
 */
/* ** Bit Positions */

./arch/blackfin/include/asm/mach-common/bits/otp.h
otp.h
/*
 * OTP Masks
 */

./arch/blackfin/include/asm/mach-common/bits/pata.h
pata.h
```

```
/*
 * ATAPI Masks
 */
/* Bit masks for ATAPI_CONTROL */

./arch/blackfin/include/asm/mach-common/bits/pll.h
pll.h
/*
 * PLL Masks
 */
/* PLL_CTL Masks */

./arch/blackfin/include/asm/mach-common/bits/ports-a.h
ports-a.h
/*
 * Port A Masks
 */

./arch/blackfin/include/asm/mach-common/bits/ports-b.h
ports-b.h
/*
 * Port B Masks
 */

./arch/blackfin/include/asm/mach-common/bits/ports-c.h
ports-c.h
/*
 * Port C Masks
 */

./arch/blackfin/include/asm/mach-common/bits/ports-d.h
ports-d.h
/*
 * Port D Masks
 */

./arch/blackfin/include/asm/mach-common/bits/ports-e.h
ports-e.h
/*
 * Port E Masks
 */

./arch/blackfin/include/asm/mach-common/bits/ports-f.h
ports-f.h
/*
 * Port F Masks
 */

./arch/blackfin/include/asm/mach-common/bits/ports-g.h
ports-g.h
```

```
/*
 * Port G Masks
 */

./arch/blackfin/include/asm/mach-common/bits/ports-h.h
ports-h.h
/*
 * Port H Masks
 */

./arch/blackfin/include/asm/mach-common/bits/ports-i.h
ports-i.h
/*
 * Port I Masks
 */

./arch/blackfin/include/asm/mach-common/bits/ports-j.h
ports-j.h
/*
 * Port J Masks
 */

./arch/blackfin/include/asm/mach-common/bits/ppi.h
ppi.h
/*
 * PPI Masks
 */
/* PPI_CONTROL Masks */

./arch/blackfin/include/asm/mach-common/bits/rtc.h
rtc.h
/*
 * RTC Masks
 */
/* RTC_STAT and RTC_ALARM Masks */

./arch/blackfin/include/asm/mach-common/bits/sdh.h
sdh.h
/*
 * SDH Masks
 */
/* Bit masks for SDH_COMMAND */

./arch/blackfin/include/asm/mach-common/bits/spi.h
spi.h
/*
 * SPI Masks
 */
/* SPI_CTL Masks */
```

```

./arch/blackfin/include/asm/mach-common/bits/sport.h
sport.h
/*
 * SPORT Masks
 */
/* SPORTx_TCR1 Masks */

./arch/blackfin/include/asm/mach-common/bits/timer.h
timer.h
/*
 * General Purpose Timer Masks
 */
/* TIMER_ENABLE Masks */

./arch/blackfin/include/asm/mach-common/bits/trace.h
trace.h
/*
 * Trace Unit Masks
 */
/* Trace Buffer Control (TBUFCTL) Register Masks */

./arch/blackfin/include/asm/mach-common/bits/twi.h
twi.h
/*
 * TWI Masks
 */
/* TWI_CLKDIV Macros (Use: *pTWI_CLKDIV = CLKLOW(x)|CLKHI(y); ) */
/* TWI_PRESCALE Masks */

./arch/blackfin/include/asm/mach-common/bits/uart.h
uart.h
/*
 * UART Masks
 */
/* UARTx_LCR Masks */

./arch/blackfin/include/asm/mach-common/bits/usb.h
usb.h
/*
 * USB Masks
 */
/* Bit masks for USB_FADDR */
/* Bit masks for USB_POWER */

./arch/blackfin/include/asm/mach-common/bits/watchdog.h
watchdog.h
/*
 * Watchdog Masks
 */
/* Watchdog Timer WDOG_CTL Register Masks */

```

```

./arch/blackfin/include/asm/mem_map.h
mem_map.h
/*
 * Common Blackfin memory map
 *
 * Copyright 2004-2009 Analog Devices Inc.
 * Licensed under the GPL-2 or later.
 */
/* Every Blackfin so far has MMRs like this */

./arch/blackfin/include/asm/net.h
net.h
/*
 * net.h - misc Blackfin network helpers
 *
 * Copyright (c) 2008-2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */
/* If the board does not have a real MAC assigned to it, then generate a
 * locally administrated pseudo-random one based on CYCLES and compile date.
 */

./arch/blackfin/include/asm/portmux.h
portmux.h
/*
 * Common header file for Blackfin family of processors
 *
 * Copyright 2007-2008 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */

./arch/blackfin/include/asm/posix_types.h
posix_types.h
/*
 * U-boot - posix_types.h
 *
 * Copyright (c) 2005-2007 Analog Devices Inc.
 *
 * (C) Copyright 2000-2004
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./arch/blackfin/include/asm/processor.h
```

```
processor.h
```

```
/*
* U-boot - processor.h
*
* Copyright (c) 2005-2007 Analog Devices Inc.
*
* This file is based on
* include/asm-m68k/processor.h
* Changes made by Akbar Hussain Lineo, Inc, May 2001 for BLACKFIN
* Copyright (C) 1995 Hamish Macdonald
*
* See file CREDITS for list of people who contributed to this
* project.
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* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./arch/blackfin/include/asm/ptrace.h
```

```
ptrace.h
```

```
/*
* Copyright 2004-2008 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
```

```
*/
/*
* GCC defines register number like this:
* -----
*     0 - 7 are data registers R0-R7
*     8 - 15 are address registers P0-P7
*    16 - 31 dsp registers I/B/L0 -- I/B/L3 & M0--M3
*    32 - 33 A registers A0 & A1
*    34 -   status register
* -----
*
* We follows above, except:
*    32-33 --- Low 32-bit of A0&1
*    34-35 --- High 8-bit of A0&1
*/
```

```
./arch/blackfin/include/asm/sdh.h
sdh.h
```

```
/*
* sdh.h, export bfin_mmc_init
*
* Copyright (c) 2009 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/
```

```
./arch/blackfin/include/asm/shared_resources.h
shared_resources.h
```

```
/*
* U-boot - setup.h
*
* Copyright (c) 2005-2007 Analog Devices Inc.
*
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* project.
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* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
* MA 02110-1301 USA
```

```
*/
```

```
./arch/blackfin/include/asm/signal.h
```

```
signal.h
```

```
No license information found.
```

```
./arch/blackfin/include/asm/string.h
```

```
string.h
```

```
/*
```

```
* U-boot - string.h String functions
```

```
*
```

```
* Copyright (c) 2005-2007 Analog Devices Inc.
```

```
*
```

```
* See file CREDITS for list of people who contributed to this  
* project.
```

```
*
```

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```

```
*
```

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```

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*
```

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* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,  
* MA 02110-1301 USA
```

```
*/
```

```
./arch/blackfin/include/asm/system.h
```

```
system.h
```

```
/*
```

```
* U-boot - system.h
```

```
*
```

```
* Copyright (c) 2005-2007 Analog Devices Inc.
```

```
*
```

```
* See file CREDITS for list of people who contributed to this  
* project.
```

```
*
```

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```

```
*
```

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* along with this program; if not, write to the Free Software
* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./arch/blackfin/include/asm/traps.h
```

```
traps.h
```

```
/*
 * Copyright 2004-2009 Analog Devices Inc.
 *           2001 Lineo, Inc
 *           Tony Kou
 *           1993 Hamish Macdonald
 *
 * Licensed under the GPL-2
 */
```

```
./arch/blackfin/include/asm/types.h
```

```
types.h
```

```
/*
 * U-boot - types.h
 *
 * Copyright (c) 2005-2007 Analog Devices Inc.
 *
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 * project.
 *
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 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
 * MA 02110-1301 USA
 */
```

```
./arch/blackfin/include/asm/u-boot.h
```

```
u-boot.h
```

```
/*
 * U-boot - u-boot.h Structure declarations for board specific data
 */
```

```
* Copyright (c) 2005-2007 Analog Devices Inc.
*
* (C) Copyright 2000-2004
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* See file CREDITS for list of people who contributed to this
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* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./arch/blackfin/include/asm/unaligned.h
unaligned.h
No license information found.
```

```
./arch/blackfin/lib/board.c
board.c
/*
* U-boot - board.c First C file to be called contains init routines
*
* Copyright (c) 2005-2008 Analog Devices Inc.
*
* (C) Copyright 2000-2004
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* Licensed under the GPL-2 or later.
*/
```

```
./arch/blackfin/lib/boot.c
boot.c
/*
* U-boot - boot.c - misc boot helper functions
*
* Copyright (c) 2005-2008 Analog Devices Inc.
*
* (C) Copyright 2000-2004
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
```

```
*
* Licensed under the GPL-2 or later.
*/

./arch/blackfin/lib/cache.c
cache.c
/*
* U-boot - cache.c
*
* Copyright (c) 2005-2008 Analog Devices Inc.
*
* (C) Copyright 2000-2004
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* Licensed under the GPL-2 or later.
*/
```

```
./arch/blackfin/lib/clocks.c
clocks.c
/*
* clocks.c - figure out sclk/cclk/vco and such
*
* Copyright (c) 2005-2008 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/
/* Get the voltage input multiplier */
```

```
./arch/blackfin/lib/cmd_cache_dump.c
cmd_cache_dump.c
/*
* U-boot - cmd_cache_dump.c
*
* Copyright (c) 2007-2008 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/
```

```
./arch/blackfin/lib/kgdb.c
kgdb.c
/*
* U-boot - architecture specific kgdb code
*
* Copyright 2009 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/
```

```
./arch/blackfin/lib/kgdb.h
kgdb.h
```

```

/* Blackfin KGDB header
*
* Copyright 2005-2009 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/
/* gdb locks */

./arch/blackfin/lib/muldi3.c
muldi3.c
/*
* U-boot - muldi3.c contains routines for mult and div
*
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*
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* project.
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* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
* MA 02110-1301 USA
*/

./arch/blackfin/lib/post.c
post.c
/*
* Blackfin POST code
*
* Copyright (c) 2005-2011 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/

./arch/blackfin/lib/string.c
string.c
/*
* U-boot - string.c Contains library routines.
*
* Copyright (c) 2005-2008 Analog Devices Inc.

```

```
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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* MA 02110-1301 USA
*/
```

```
./arch/m68k/cpu/mcf5227x/cpu.c
```

```
cpu.c
```

```
/*
*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* Copyright (C) 2004-2007, 2012 Freescale Semiconductor, Inc.
* TsiChung Liew (Tsi-Chung.Liew@freescale.com)
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```

```
*/  
  
./arch/m68k/cpu/mcf5227x/cpu_init.c  
cpu_init.c  
/*  
*  
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*/
```

```
./arch/m68k/cpu/mcf5227x/interrupts.c  
interrupts.c  
/*  
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*/
```

```
./arch/m68k/cpu/mcf5227x/speed.c
```

```
speed.c
```

```
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*/
```

```
./arch/m68k/cpu/mcf523x/cpu.c
```

```
cpu.c
```

```
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*/
```

```
./arch/m68k/cpu/mcf523x/cpu_init.c
cpu_init.c
```

```
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```

```
./arch/m68k/cpu/mcf523x/interrupts.c
interrupts.c
```

```
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*/
```

```
./arch/m68k/cpu/mcf523x/speed.c
speed.c
```

```
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```

```
./arch/m68k/cpu/mcf52x2/cpu.c
```

cpu.c

```
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 */
```

./arch/m68k/cpu/mcf52x2/cpu.h

cpu.h

```
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 * cpu.h
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```

```
./arch/m68k/cpu/mcf52x2/cpu_init.c
cpu_init.c
```

```
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*/
```

```
./arch/m68k/cpu/mcf52x2/interrupts.c
interrupts.c
```

```
/*
```

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*/
```

```
./arch/m68k/cpu/mcf52x2/speed.c
speed.c
```

```
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```

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*/

./arch/m68k/cpu/mcf532x/cpu.c

cpu.c

/*

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*/

./arch/m68k/cpu/mcf532x/cpu_init.c

cpu_init.c

/*

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*/
```

```
./arch/m68k/cpu/mcf532x/interrupts.c
```

```
interrupts.c
```

```
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*/
```

```
./arch/m68k/cpu/mcf532x/speed.c
```

```
speed.c
```

```
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*/
```

```
./arch/m68k/cpu/mcf5445x/cpu.c
```

```
cpu.c
```

```
/*
```

```
*
```

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```
*/
```

```
./arch/m68k/cpu/mcf5445x/cpu_init.c
```

```
cpu_init.c
```

```
/*
```

```
*
```

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*/
```

```
./arch/m68k/cpu/mcf5445x/interrupts.c
```

```
interrupts.c
```

```
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./arch/m68k/cpu/mcf5445x/pci.c

pci.c

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./arch/m68k/cpu/mcf5445x/speed.c

speed.c

/*

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```

```
./arch/m68k/cpu/mcf547x_8x/cpu.c  
cpu.c
```

```
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*/
```

```
./arch/m68k/cpu/mcf547x_8x/cpu_init.c  
cpu_init.c
```

```
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```

```
./arch/m68k/cpu/mcf547x_8x/interrupts.c
interrupts.c
```

```
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*/
```

```
./arch/m68k/cpu/mcf547x_8x/pci.c
pci.c
```

```
/*
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*/
```

```
./arch/m68k/cpu/mcf547x_8x/slicetimer.c
```

```
slicetimer.c
```

```
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*/
```

```
./arch/m68k/cpu/mcf547x_8x/speed.c
```

```
speed.c
```

```
/*
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*/
```

```
./arch/m68k/include/asm/bitops.h
```

```
bitops.h
```

```
/*
* bitops.h: Bit string operations on the m68k
*/
```

```
./arch/m68k/include/asm/byteorder.h
```

```
byteorder.h
```

```
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*/
```

```
./arch/m68k/include/asm/cache.h
```

```
cache.h
```

```
/*
 * ColdFire cache
 *
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 */
```

```
./arch/m68k/include/asm/coldfire/ata.h
ata.h
```

```
/*
 * ATA Internal Memory Map
 *
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```

*/

./arch/m68k/include/asm/coldfire/crossbar.h
crossbar.h

/*

* Cross Bar Switch Internal Memory Map

*

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*/

./arch/m68k/include/asm/coldfire/dspi.h
dspi.h

/*

* MCF5227x Internal Memory Map

*

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*/
```

```
./arch/m68k/include/asm/coldfire/edma.h
edma.h
```

```
/*
 * EDMA Internal Memory Map
 *
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*/
```

```
./arch/m68k/include/asm/coldfire/eport.h
eport.h
```

```
/*
 * Edge Port Memory Map
 *
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*/
```

```
./arch/m68k/include/asm/coldfire/flexbus.h
flexbus.h
```

```
/*
 * FlexBus Internal Memory Map
 *
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*/
```

```
./arch/m68k/include/asm/coldfire/flexcan.h
flexcan.h
```

```
/*
 * Flex CAN Memory Map
 *
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* MA 02111-1307 USA
*/
```

```
./arch/m68k/include/asm/coldfire/intctrl.h
intctrl.h
```

```
/*
 * Interrupt Controller Memory Map
 *
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*/
```

```
./arch/m68k/include/asm/coldfire/lcd.h
lcd.h
```

```
/*
 * LCD controller Memory Map
 *
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*/
```

```
./arch/m68k/include/asm/coldfire/mdha.h
```

```
mdha.h
```

```
/*  
* Message Digest Hardware Accelerator Memory Map  
*  
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*/
```

```
./arch/m68k/include/asm/coldfire/pwm.h
```

```
pwm.h
```

```
/*  
* Pulse Width Modulation Memory Map  
*  
*/
```

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*/
```

```
./arch/m68k/include/asm/coldfire/qspi.h
```

```
qspi.h
```

```
/*
* Queue Serial Peripheral Interface Memory Map
*
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```
./arch/m68k/include/asm/coldfire/rng.h
```

rng.h

```
/*
 * RNG Memory Map
 *
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 */
```

./arch/m68k/include/asm/coldfire/skha.h

skha.h

```
/*
 * Symmetric Key Hardware Accelerator Memory Map
 *
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```
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*/
```

```
./arch/m68k/include/asm/coldfire/ssi.h
ssi.h
```

```
/*
 * SSI Internal Memory Map
 *
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```

```
./arch/m68k/include/asm/config.h
config.h
```

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 */
```

```
*/

./arch/m68k/include/asm/errno.h
errno.h
No license information found.

./arch/m68k/include/asm/fec.h
fec.h
/*
 * fec.h -- Fast Ethernet Controller definitions
 *
 * Some definitions copied from commproc.h for MPC8xx:
 * MPC8xx Communication Processor Module.
 * Copyright (c) 1997 Dan Malek (dmalek@jlc.net)
 *
 * Add FEC Structure and definitions
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 */

./arch/m68k/include/asm/fsl_i2c.h
fsl_i2c.h
/*
 * Freescale I2C Controller
 *
 * Copyright 2006 Freescale Semiconductor, Inc.
 *
 * Based on earlier versions by Gleb Natapov <gnatapov@mr.v.com>,
 * Xianghua Xiao <x.xiao@motorola.com>, Eran Liberty (liberty@freescale.com),
 * and Jeff Brown.
 * Some bits are taken from linux driver written by adrian@humboldt.co.uk.
 *
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*/

./arch/m68k/include/asm/fsl_mcdmafec.h
fsl_mcdmafec.h
/*
* fsl_mcdmafec.h -- Multi-channel DMA Fast Ethernet Controller definitions
*
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*/

./arch/m68k/include/asm/global_data.h
global_data.h
/*
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```

```
./arch/m68k/include/asm/imap.h
```

```
imap.h
```

```
/*  
* ColdFire Internal Memory Map and Defines  
*  
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*/
```

```
./arch/m68k/include/asm/imap_520x.h
```

```
imap_520x.h
```

```
/*
 * MCF520x Internal Memory Map
 *
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 *
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 */
```

```
./arch/m68k/include/asm/immap_5227x.h
immap_5227x.h
```

```
/*
 * MCF5227x Internal Memory Map
 *
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```

*/

./arch/m68k/include/asm/immap_5235.h

immap_5235.h

/*

* MCF5329 Internal Memory Map

*

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*/

./arch/m68k/include/asm/immap_5249.h

immap_5249.h

/*

* MCF5249 Internal Memory Map

*

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*/
```

```
./arch/m68k/include/asm/immap_5253.h
```

```
immap_5253.h
```

```
/*
```

```
* MCF5253 Internal Memory Map
```

```
*
```

```
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```

```
* TsiChung Liew (Tsi-Chung.Liew@freescale.com)
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```

```
*/
```

```
./arch/m68k/include/asm/immap_5271.h
```

```
immap_5271.h
```

```
/*
```

```
* MCF5272 Internal Memory Map
```

```
*
```

```
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```

```
* 2006 Zachary P. Landau <zachary.landau@labxtechnologies.com>
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*/
```

```
./arch/m68k/include/asm/immap_5272.h
immap_5272.h
```

```
/*
 * MCF5272 Internal Memory Map
 *
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*/
```

```
./arch/m68k/include/asm/immap_5275.h
immap_5275.h
```

```
/*
 * MCF5274/5 Internal Memory Map
 *
 * Copyright (c) 2005 Arthur Shipkowski <art@videon-central.com>
 * Based on work Copyright (c) 2003 Josef Baumgartner
 *                               <josef.baumgartner@telex.de>
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* MA 02111-1307 USA
*/
```

```
./arch/m68k/include/asm/immap_5282.h
```

```
immap_5282.h
```

```
/*
 * MCF5282 Internal Memory Map
 *
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*/
```

```
./arch/m68k/include/asm/immap_5301x.h
```

```
immap_5301x.h
```

```
/*
 * MCF5301x Internal Memory Map
 *
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 */
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*/
```

```
./arch/m68k/include/asm/immap_5329.h
```

```
immap_5329.h
```

```
/*
* MCF5329 Internal Memory Map
*
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*/
```

```
./arch/m68k/include/asm/immap_5441x.h
```

```
immap_5441x.h
```

```
/*
* MCF5441x Internal Memory Map
*
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*/
```

```
./arch/m68k/include/asm/immap_5445x.h
immap_5445x.h
```

```
/*
* MCF5445x Internal Memory Map
*
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*/
```

```
./arch/m68k/include/asm/immap_547x_8x.h
immap_547x_8x.h
```

```
/*
```

```
* MCF547x_8x Internal Memory Map
*
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*/
```

```
./arch/m68k/include/asm/io.h
```

```
io.h
```

```
/*
* IO header file
*
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*/
```

./arch/m68k/include/asm/m520x.h

m520x.h

/*

* m520x.h -- Definitions for Freescale Coldfire 520x

*

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*/

./arch/m68k/include/asm/m5227x.h

m5227x.h

/*

* MCF5227x Internal Memory Map

*

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```

```
./arch/m68k/include/asm/m5235.h
```

```
m5235.h
```

```
/*
 * mcf5329.h -- Definitions for Freescale Coldfire 5329
 *
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*/
```

```
./arch/m68k/include/asm/m5249.h
```

```
m5249.h
```

```
/*
 * mcf5249.h -- Definitions for Motorola Coldfire 5249
 *
 * Based on mcf5272sim.h of uCLinux distribution:
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 * (C) Copyright 2000, Lineo Inc. (www.lineo.com)
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*/
```

```
./arch/m68k/include/asm/m5253.h
```

```
m5253.h
```

```
/*
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*/
```

```
./arch/m68k/include/asm/m5271.h
```

```
m5271.h
```

```
/*
* mcf5271.h -- Definitions for Motorola Coldfire 5271
*
* (C) Copyright 2006, Lab X Technologies <zachary.landau@labxtechnologies.com>
* Based on mcf5272sim.h of uCLinux distribution:
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*/
```

```
./arch/m68k/include/asm/m5272.h
```

```
m5272.h
```

```
/*
 * mcf5272.h -- Definitions for Motorola Coldfire 5272
 *
 * Based on mcf5272sim.h of uCLinux distribution:
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*/
```

```
./arch/m68k/include/asm/m5275.h
```

```
m5275.h
```

```
/*
 * MCF5275 Internal Memory Map
 *
 * Copyright (C) 2003-2004, Greg Ungerer (gerg@snapgear.com)
 * Copyright (C) 2004-2008 Arthur Shipkowski (art@videon-central.com)
 *
 */
```

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*/
```

```
./arch/m68k/include/asm/m5282.h
```

```
m5282.h
```

```
/*
 * mcf5282.h -- Definitions for Motorola Coldfire 5282
 *
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 * project.
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 * MA 02111-1307 USA
*/
```

```
./arch/m68k/include/asm/m5301x.h
```

```
m5301x.h
```

```
/*
 * m5301x.h -- Definitions for Freescale Coldfire 5301x
 *
 * Copyright (C) 2004-2008 Freescale Semiconductor, Inc.
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```

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*/
```

```
./arch/m68k/include/asm/m5329.h
```

```
m5329.h
```

```
/*
* mcf5329.h -- Definitions for Freescale Coldfire 5329
*
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* TsiChung Liew (Tsi-Chung.Liew@freescale.com)
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*/
```

```
./arch/m68k/include/asm/m5441x.h
```

```
m5441x.h
```

```
/*
```

```
* MCF5441X Internal Memory Map
*
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*/
```

```
./arch/m68k/include/asm/m5445x.h
m5445x.h
```

```
/*
* MCF5445x Internal Memory Map
*
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*/
```

```
./arch/m68k/include/asm/m547x_8x.h  
m547x_8x.h
```

```
/*  
 * mcf547x_8x.h -- Definitions for Freescale Coldfire 547x_8x  
 *  
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 * MA 02111-1307 USA  
 */
```

```
./arch/m68k/include/asm/posix_types.h  
posix_types.h
```

```
/*  
 * This file is generally used by user-level software, so you need to  
 * be a little careful about namespace pollution etc. Also, we cannot  
 * assume GCC is being used.  
 */
```

```
./arch/m68k/include/asm/processor.h  
processor.h
```

```
/* Macros for setting and retrieving special purpose registers */
```

```
./arch/m68k/include/asm/ptrace.h  
ptrace.h
```

```
/*  
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 * project.  
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*/
```

```
./arch/m68k/include/asm/rtc.h
```

```
rtc.h
```

```
/*
* RealTime Clock
*
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*/
```

```
./arch/m68k/include/asm/string.h
```

```
string.h
```

```
No license information found.
```

```
./arch/m68k/include/asm/timer.h
```

```
timer.h
```

```
/*
* timer.h -- ColdFire internal TIMER support defines.
*
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* TsiChung Liew (Tsi-Chung.Liew@freescale.com)
```

```
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*/
```

```
./arch/m68k/include/asm/types.h
types.h
No license information found.
```

```
./arch/m68k/include/asm/u-boot.h
u-boot.h
/*
```

```
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*
*****
* NOTE: This header file defines an interface to U-Boot. Including
* this (unmodified) header file in another file is considered normal
* use of U-Boot, and does *not* fall under the heading of "derived
* work".
```

*/

./arch/m68k/include/asm/uart.h

uart.h

/*

* uart.h -- ColdFire internal UART support defines.

*

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* TsiChung Liew (Tsi-Chung.Liew@freescale.com)

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*/

./arch/m68k/include/asm/unaligned.h

unaligned.h

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./arch/m68k/lib/board.c

board.c

/*

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* Josef Baumgartner <josef.baumgartner@telex.de>

*

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*/
```

```
./arch/m68k/lib/bootm.c
```

```
bootm.c
```

```
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*/
```

```
./arch/m68k/lib/cache.c
```

```
cache.c
```

```
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*/
```

```
./arch/m68k/lib/interrupts.c
interrupts.c
```

```
/*
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*
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*/
```

```
./arch/m68k/lib/time.c
time.c
```

```
/*
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*/
```

```
./arch/m68k/lib/traps.c
```

```
traps.c
```

```
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*/
```

```
./arch/microblaze/cpu/cache.c
```

```
cache.c
```

```
/*
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*
```

```
* Michal SIMEK <monstr@monstr.eu>
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*/
```

```
./arch/microblaze/cpu/cpu.c
```

```
cpu.c
```

```
/*
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```

```
./arch/microblaze/cpu/exception.c
```

```
exception.c
```

```
/*
```

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```
./arch/microblaze/cpu/interrupts.c
interrupts.c
```

```
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*/
```

```
./arch/microblaze/cpu/timer.c
```

```
timer.c
```

```
/*
```

```
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```

```
*
```

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```

```
*
```

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```

```
*/
```

```
./arch/microblaze/include/asm/asm.h
```

```
asm.h
```

```
/*
```

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*/
```

```
./arch/microblaze/include/asm/bitops.h
bitops.h
```

```
/*
 * Copyright 1992, Linus Torvalds.
 */
/*
 * Function prototypes to keep gcc -Wall happy
 */
```

```
./arch/microblaze/include/asm/byteorder.h
byteorder.h
```

```
/*
 * include/asm-microblaze/byteorder.h -- Endian id and conversion ops
 *
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 *
 * Written by Miles Bader <miles@gnu.org>
 * Microblaze port by John Williams
 */
```

```
./arch/microblaze/include/asm/cache.h
cache.h
```

```
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```

*/

./arch/microblaze/include/asm/config.h
config.h

/*

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*

*/

./arch/microblaze/include/asm/errno.h
errno.h

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./arch/microblaze/include/asm/global_data.h
global_data.h

/*

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* Yasushi SHOJI <yashi@atmark-techno.com>

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```

```
./arch/microblaze/include/asm/gpio.h  
gpio.h  
No license information found.
```

```
./arch/microblaze/include/asm/io.h  
io.h  
/*  
* include/asm-microblaze/io.h -- Misc I/O operations  
*  
* Copyright (C) 2003      John Williams <jwilliams@itee.uq.edu.au>  
* Copyright (C) 2001,02  NEC Corporation  
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*  
* Written by Miles Bader <miles@gnu.org>  
* Microblaze port by John Williams  
*/
```

```
./arch/microblaze/include/asm/microblaze_intc.h  
microblaze_intc.h  
/*  
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* Michal SIMEK <monstr@monstr.cz>  
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* MA 02111-1307 USA  
*/
```

```
./arch/microblaze/include/asm/microblaze_timer.h
microblaze_timer.h
```

```
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 *
 * Michal SIMEK <monstr@monstr.cz>
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 */
```

```
./arch/microblaze/include/asm/posix_types.h
posix_types.h
```

```
/*
 * include/asm-microblaze/posix_types.h -- Kernel versions of standard types
 *
 * Copyright (C) 2003      John Williams <jwilliams@itee.uq.edu.au>
 * Copyright (C) 2001,2002 NEC Corporation
 * Copyright (C) 2001,2002 Miles Bader <miles@gnu.org>
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 *
 * Written by Miles Bader <miles@gnu.org>
 * Microblaze port by John Williams
 */
```

```
./arch/microblaze/include/asm/processor.h
processor.h
```

```
/*
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```
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* MA 02111-1307 USA  
*/
```

```
./arch/microblaze/include/asm/ptrace.h
```

```
ptrace.h
```

```
/*  
* include/asm-microblaze/ptrace.h -- Access to CPU registers  
*  
* Copyright (C) 2003      John Williams <jwilliams@itee.uq.edu.au>  
* Copyright (C) 2001,2002 NEC Corporation  
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* Written by Miles Bader <miles@gnu.org>  
* Microblaze port by John Williams  
*/
```

```
./arch/microblaze/include/asm/string.h
```

```
string.h
```

```
/*  
* include/asm-microblaze/string.h -- Architecture specific string routines  
*  
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*  
* Written by Miles Bader <miles@gnu.org>  
* Microblaze port by John Williams
```

```
*/  
  
./arch/microblaze/include/asm/system.h  
system.h  
/*  
* include/asm-microblaze/system.h -- Low-level interrupt/thread ops  
*  
* Copyright (C) 2003 John Williams (jwilliams@itee.uq.edu.au)  
* based upon microblaze version  
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* Written by Miles Bader <miles@gnu.org>  
* Microblaze port by John Williams  
* Microblaze port by John Williams  
*/
```

```
./arch/microblaze/include/asm/types.h  
types.h  
/*  
* This file is never included by application software unless  
* explicitly requested (e.g., via linux/types.h) in which case the  
* application is Linux specific so (user-) name space pollution is  
* not a major issue. However, for interoperability, libraries still  
* need to be careful to avoid a name clashes.  
*/
```

```
./arch/microblaze/include/asm/u-boot.h  
u-boot.h  
/*  
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*  
* Yasushi SHOJI <yashi@atmark-techno.com>  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*****
* NOTE: This header file defines an interface to U-Boot. Including
* this (unmodified) header file in another file is considered normal
* use of U-Boot, and does *not* fall under the heading of "derived
* work".
*****
*/
```

```
./arch/microblaze/include/asm/unaligned.h
unaligned.h
No license information found.
```

```
./arch/microblaze/lib/board.c
board.c
/*
```

```
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* (C) Copyright 2004 Atmark Techno, Inc.
*
* Michal SIMEK <monstr@monstr.eu>
* Yasushi SHOJI <yashi@atmark-techno.com>
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* MA 02111-1307 USA
*/
```

```
./arch/microblaze/lib/bootm.c
bootm.c
/*
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```

```
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* Yasushi SHOJI <yashi@atmark-techno.com>
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*/
```

```
./arch/microblaze/lib/time.c
time.c
No license information found.
```

```
./arch/mips/cpu/mips32/au1x00/au1x00_eth.c
au1x00_eth.c
/* Only eth0 supported for now
*
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* Thomas.Lange@corelatus.se
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```

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*/
```

```
./arch/mips/cpu/mips32/au1x00/au1x00_ide.c  
au1x00_ide.c
```

```
/*  
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* MA 02111-1307 USA  
*  
*/
```

```
./arch/mips/cpu/mips32/au1x00/au1x00_serial.c  
au1x00_serial.c
```

```
/*  
* AU1X00 UART support  
*  
* Hardcoded to UART 0 for now  
* Speed and options also hardcoded to 115200 8N1  
*  
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*/

./arch/mips/cpu/mips32/au1x00/au1x00_usb_ohci.c
au1x00_usb_ohci.c
/*
* URB OHCI HCD (Host Controller Driver) for USB on the AU1x00.
*
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* Gary Jennejohn, DENX Software Engineering <garyj@denx.de>
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
* Note: Part of this code has been derived from linux
*
*/
/*
* IMPORTANT NOTES
* 1 - this driver is intended for use with USB Mass Storage Devices
*     (BBB) ONLY. There is NO support for Interrupt or Isochronous pipes!
*/

./arch/mips/cpu/mips32/au1x00/au1x00_usb_ohci.h
au1x00_usb_ohci.h
/*
* URB OHCI HCD (Host Controller Driver) for USB.
*
* (C) Copyright 1999 Roman Weissgaerber <weissg@vienna.at>

```

```
* (C) Copyright 2000-2001 David Brownell <dbrownell@users.sourceforge.net>
*
* usb-ohci.h
*/
/* mapping of the OHCI CC status to error codes */
```

```
./arch/mips/cpu/mips32/cpu.c
cpu.c
/*
```

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*/
```

```
./arch/mips/cpu/mips32/incaip/asc_serial.c
asc_serial.c
/*
* (INCA) ASC UART support
*/
```

```
./arch/mips/cpu/mips32/incaip/asc_serial.h
asc_serial.h
/* incaAscSio.h - (INCA) ASC UART tty driver header */
/* channel operating modes */
```

```
./arch/mips/cpu/mips32/incaip/incaip_clock.c
incaip_clock.c
/*
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```

```
./arch/mips/cpu/mips32/interrupts.c
interrupts.c
```

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```

```
./arch/mips/cpu/mips32/time.c
time.c
```

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```

```
./arch/mips/cpu/mips64/cpu.c
```

```
cpu.c
```

```
/*
```

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```
./arch/mips/cpu/mips64/interrupts.c
```

```
interrupts.c
```

```
/*
```

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```
./arch/mips/cpu/mips64/time.c
time.c
```

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```
./arch/mips/cpu/xburst/cpu.c
cpu.c
```

```
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```

```
./arch/mips/cpu/xburst/jz4740.c
```

```
jz4740.c
```

```
/*
 * Jz4740 common routines
 * Copyright (c) 2006 Ingenic Semiconductor, <jlwei@ingenic.cn>
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*/
```

```
./arch/mips/cpu/xburst/jz_serial.c
```

```
jz_serial.c
```

```
/*
 * Jz4740 UART support
 * Copyright (c) 2011
 * Qi Hardware, Xiangfu Liu <xiangfu@sharism.cc>
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```

```
./arch/mips/cpu/xburst/timer.c
timer.c
```

```
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```

```
./arch/mips/include/asm/addrspace.h
addrspace.h
```

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* Copyright (C) 2000, 2002 Maciej W. Rozycki
* Copyright (C) 1990, 1999 by Silicon Graphics, Inc.
*/
/*
* Configure language
*/
```

```
./arch/mips/include/asm/asm.h
asm.h
```

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 * Copyright (C) 2002 Maciej W. Rozycki
 *
 * Some useful macros for MIPS assembler code
 *
 * Some of the routines below contain useless nops that will be optimized
 * away by gas in -O mode. These nops are however required to fill delay
 * slots in noreorder mode.
 */
```

```
./arch/mips/include/asm/au1x00.h
au1x00.h
```

```
/*
 *
 * BRIEF MODULE DESCRIPTION
 *   Include file for Alchemy Semiconductor's Au1k CPU.
 *
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 * with this program; if not, write to the Free Software Foundation, Inc.,
 * 675 Mass Ave, Cambridge, MA 02139, USA.
 */
```

*/

./arch/mips/include/asm/bitops.h

bitops.h

/*

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* for more details.

*

* Copyright (c) 1994 - 1997, 1999, 2000 Ralf Baechle (ralf@gnu.org)

* Copyright (c) 2000 Silicon Graphics, Inc.

*/

./arch/mips/include/asm/byteorder.h

byteorder.h

/*

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* for more details.

*

* Copyright (C) 1996, 99, 2003 by Ralf Baechle

*/

./arch/mips/include/asm/cache.h

cache.h

/*

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* See file CREDITS for list of people who contributed to this
* project.

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,

* MA 02111-1307 USA

*/

./arch/mips/include/asm/cachectl.h

cachectl.h

/*

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```

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* for more details.
*
* Copyright (C) 1994, 1995, 1996 by Ralf Baechle
*/
/*
* Options for cacheflush system call
*/

./arch/mips/include/asm/cacheops.h
cacheops.h
/*
* Cache operations for the cache instruction.
*
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* License. See the file "COPYING" in the main directory of this archive
* for more details.
*
* (C) Copyright 1996, 97, 99, 2002, 03 Ralf Baechle
* (C) Copyright 1999 Silicon Graphics, Inc.
*/

./arch/mips/include/asm/config.h
config.h
/*
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*
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* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2 of
* the License, or (at your option) any later version.
*
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./arch/mips/include/asm/errno.h
errno.h
/*
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* License. See the file "COPYING" in the main directory of this archive
* for more details.

```

```
*
* Copyright (C) 1995, 1999, 2001, 2002 by Ralf Baechle
*/
/*
* These first 34 error codes are from Linux 2.6, <asm-generic/errno-base.h>
*/
```

```
./arch/mips/include/asm/global_data.h
global_data.h
```

```
/*
* (C) Copyright 2002-2010
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* See file CREDITS for list of people who contributed to this
* project.
*
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/mips/include/asm/inca-ip.h
inca-ip.h
```

```
/******
```

```
./arch/mips/include/asm/io.h
io.h
```

```
/*
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* License. See the file "COPYING" in the main directory of this archive
* for more details.
*
* Copyright (C) 1994, 1995 Waldorf GmbH
* Copyright (C) 1994 - 2000 Ralf Baechle
* Copyright (C) 1999, 2000 Silicon Graphics, Inc.
* Copyright (C) 2000 FSMLabs, Inc.
*/
```

```
./arch/mips/include/asm/isadep.h
```

isadep.h

```
/*
 * Various ISA level dependent constants.
 * Most of the following constants reflect the different layout
 * of Coprocessor 0 registers.
 *
 * Copyright (c) 1998 Harald Koerfgen
 */
/*
 * R2000 or R3000
 */
```

./arch/mips/include/asm/jz4740.h

jz4740.h

```
/*
 * head file for Ingenic Semiconductor's JZ4740 CPU.
 */
/* Boot ROM Specification */
/* NOR Boot config */
```

./arch/mips/include/asm/mipsregs.h

mipsregs.h

```
/*
 * This file is subject to the terms and conditions of the GNU General Public
 * License. See the file "COPYING" in the main directory of this archive
 * for more details.
 *
 * Copyright (C) 1994, 1995, 1996, 1997, 2000, 2001 by Ralf Baechle
 * Copyright (C) 2000 Silicon Graphics, Inc.
 * Modified for further R[236]000 support by Paul M. Antoine, 1996.
 * Kevin D. Kissell, kevink@mips.com and Carsten Langgaard, carstenl@mips.com
 * Copyright (C) 2000, 07 MIPS Technologies, Inc.
 * Copyright (C) 2003, 2004 Maciej W. Rozycki
 */
```

./arch/mips/include/asm/posix_types.h

posix_types.h

```
/* $Id: posix_types.h,v 1.6 2000/02/04 23:32:54 ralf Exp $
 *
 * This file is subject to the terms and conditions of the GNU General Public
 * License. See the file "COPYING" in the main directory of this archive
 * for more details.
 *
 * Copyright (C) 1996, 1997, 1998, 2000 by Ralf Baechle
 */
/*
 * This file is generally used by user-level software, so you need to
 * be a little careful about namespace pollution etc. Also, we cannot
 * assume GCC is being used.
 */
```

```
./arch/mips/include/asm/processor.h
processor.h
```

```
/*
 * This file is subject to the terms and conditions of the GNU General Public
 * License. See the file "COPYING" in the main directory of this archive
 * for more details.
 *
 * Copyright (C) 1994 Waldorf GMBH
 * Copyright (C) 1995, 1996, 1997, 1998, 1999, 2001, 2002, 2003 Ralf Baechle
 * Copyright (C) 1996 Paul M. Antoine
 * Copyright (C) 1999, 2000 Silicon Graphics, Inc.
 */
```

```
./arch/mips/include/asm/ptrace.h
ptrace.h
```

```
/*
 * This file is subject to the terms and conditions of the GNU General Public
 * License. See the file "COPYING" in the main directory of this archive
 * for more details.
 *
 * Copyright (C) 1994, 95, 96, 97, 98, 99, 2000 by Ralf Baechle
 * Copyright (C) 1999, 2000 Silicon Graphics, Inc.
 */
/* 0 - 31 are integer registers, 32 - 63 are fp registers. */
```

```
./arch/mips/include/asm/reboot.h
reboot.h
```

```
/*
 * This file is subject to the terms and conditions of the GNU General Public
 * License. See the file "COPYING" in the main directory of this archive
 * for more details.
 *
 * Copyright (C) 1997, 1999, 2001, 06 by Ralf Baechle
 * Copyright (C) 2001 MIPS Technologies, Inc.
 */
```

```
./arch/mips/include/asm/reg.h
reg.h
```

```
/*
 * Various register offset definitions for debuggers, core file
 * examiners and whatnot.
 *
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 * License. See the file "COPYING" in the main directory of this archive
 * for more details.
 *
 * Copyright (C) 1995, 1999 by Ralf Baechle
 * Copyright (C) 1995, 1999 Silicon Graphics
 */
```

```
./arch/mips/include/asm/regdef.h
regdef.h
```

```
/*
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 * License. See the file "COPYING" in the main directory of this archive
 * for more details.
 *
 * Copyright (C) 1985 MIPS Computer Systems, Inc.
 * Copyright (C) 1994, 95, 99, 2003 by Ralf Baechle
 * Copyright (C) 1990 - 1992, 1999 Silicon Graphics, Inc.
 */
```

```
./arch/mips/include/asm/sgidefs.h
sgidefs.h
```

```
/*
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 * License. See the file "COPYING" in the main directory of this archive
 * for more details.
 *
 * Copyright (C) 1996, 1999, 2001 Ralf Baechle
 * Copyright (C) 1999 Silicon Graphics, Inc.
 * Copyright (C) 2001 MIPS Technologies, Inc.
 */
/*
 * Using a Linux compiler for building Linux seems logic but not to
 * everybody.
 */
```

```
./arch/mips/include/asm/string.h
string.h
```

```
/*
 * This file is subject to the terms and conditions of the GNU General Public
 * License. See the file "COPYING" in the main directory of this archive
 * for more details.
 *
 * Copyright (c) 1994, 95, 96, 97, 98, 2000, 01 Ralf Baechle
 * Copyright (c) 2000 by Silicon Graphics, Inc.
 * Copyright (c) 2001 MIPS Technologies, Inc.
 */
/*
 * We don't do inline string functions, since the
 * optimised inline asm versions are not small.
 */
```

```
./arch/mips/include/asm/system.h
system.h
```

```
/*
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 * License. See the file "COPYING" in the main directory of this archive
```

```
* for more details.
*
* Copyright (C) 1994 - 1999 by Ralf Baechle
* Copyright (C) 1996 by Paul M. Antoine
* Copyright (C) 1994 - 1999 by Ralf Baechle
*
* Changed set_except_vector declaration to allow return of previous
* vector address value - necessary for "borrowing" vectors.
*
* Kevin D. Kissell, kevink@mips.org and Carsten Langgaard, carstenl@mips.com
* Copyright (C) 2000 MIPS Technologies, Inc.
*/
```

```
./arch/mips/include/asm/types.h
```

```
types.h
```

```
/*
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* License. See the file "COPYING" in the main directory of this archive
* for more details.
*
* Copyright (C) 1994, 1995, 1996, 1999 by Ralf Baechle
* Copyright (C) 1999 Silicon Graphics, Inc.
*/
```

```
./arch/mips/include/asm/u-boot-mips.h
```

```
u-boot-mips.h
```

```
/*
* This file is released under the terms of GPL v2 and any later version.
* See the file COPYING in the root directory of the source tree for details.
*
* Copyright (C) 2003 Wolfgang Denk, DENX Software Engineering, wd@denx.de
*/
```

```
./arch/mips/include/asm/u-boot.h
```

```
u-boot.h
```

```
/*
* (C) Copyright 2003
* Wolfgang Denk, DENX Software Engineering, <wd@denx.de>
*
* See file CREDITS for list of people who contributed to this
* project.
*
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* the License, or (at your option) any later version.
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```
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
```

```
*****
* NOTE: This header file defines an interface to U-Boot. Including
* this (unmodified) header file in another file is considered normal
* use of U-Boot, and does *not* fall under the heading of "derived
* work".
*****
*/
```

```
./arch/mips/include/asm/unaligned.h
unaligned.h
```

```
/*
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* License. See the file "COPYING" in the main directory of this archive
* for more details.
*
* Copyright (C) 2007 Ralf Baechle (ralf@linux-mips.org)
*/
```

```
./arch/mips/lib/ashldi3.c
ashldi3.c
No license information found.
```

```
./arch/mips/lib/ashrdi3.c
ashrdi3.c
No license information found.
```

```
./arch/mips/lib/board.c
board.c
/*
* (C) Copyright 2003
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* See file CREDITS for list of people who contributed to this
* project.
*
* This program is free software; you can redistribute it and/or
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```

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*  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/mips/lib/bootm.c
```

```
bootm.c
```

```
/*  
* (C) Copyright 2003  
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
*  
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* it under the terms of the GNU General Public License as published by  
* the Free Software Foundation; either version 2 of the License, or  
* (at your option) any later version.  
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* MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the  
* GNU General Public License for more details.  
*  
* You should have received a copy of the GNU General Public License  
* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA  
*  
*/
```

```
./arch/mips/lib/bootm_qemu_mips.c
```

```
bootm_qemu_mips.c
```

```
/*  
* (C) Copyright 2008  
* Jean-Christophe PLAGNIOL-VILLARD <jcplagniol@jcrosoft.com>  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
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```

```
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*
*/
```

```
./arch/mips/lib/libgcc.h
libgcc.h
No license information found.
```

```
./arch/mips/lib/lshrdi3.c
lshrdi3.c
No license information found.
```

```
./arch/nds32/cpu/n1213/ag101/asm-offsets.c
asm-offsets.c
/*
* Adapted from Linux v2.6.36 kernel: arch/powerpc/kernel/asm-offsets.c
*
* Generate definitions needed by assembly language modules.
* This code generates raw asm output which is post-processed to extract
* and format the required data.
*
* This program is free software; you can redistribute it and/or modify
* it under the terms of the GNU General Public License version 2 as
* published by the Free Software Foundation.
*/
```

```
./arch/nds32/cpu/n1213/ag101/cpu.c
cpu.c
/*
* (C) Copyright 2002
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*
* (C) Copyright 2002
* Gary Jennejohn, DENX Software Engineering, <gj@denx.de>
*
* Copyright (C) 2011 Andes Technology Corporation
* Shawn Lin, Andes Technology Corporation <nobuhiro@andestech.com>
* Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
*
* See file CREDITS for list of people who contributed to this
* project.
*
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* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2 of
```

```
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/nds32/cpu/n1213/ag101/timer.c
```

```
timer.c
```

```
/*
* (C) Copyright 2009 Faraday Technology
* Po-Yu Chuang <ratbert@faraday-tech.com>
*
* Copyright (C) 2011 Andes Technology Corporation
* Shawn Lin, Andes Technology Corporation <nobuhiro@andestech.com>
* Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
*
* See file CREDITS for list of people who contributed to this
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./arch/nds32/cpu/n1213/ag102/asm-offsets.c
```

```
asm-offsets.c
```

```
/*
* Adapted from Linux v2.6.36 kernel: arch/powerpc/kernel/asm-offsets.c
*
* Generate definitions needed by assembly language modules.
* This code generates raw asm output which is post-processed to extract
* and format the required data.
*
*/
```

```
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* it under the terms of the GNU General Public License version 2 as
* published by the Free Software Foundation.
*/
```

```
./arch/nds32/cpu/n1213/ag102/cpu.c
```

```
cpu.c
```

```
/*
 * (C) Copyright 2002
 * Sysgo Real-Time Solutions, GmbH <www.elinos.com>
 * Marius Groeger <mgroeger@sysgo.de>
 *
 * (C) Copyright 2002
 * Gary Jennejohn, DENX Software Engineering, <gj@denx.de>
 *
 * Copyright (C) 2011 Andes Technology Corporation
 * Shawn Lin, Andes Technology Corporation <nobuhiro@andestech.com>
 * Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./arch/nds32/cpu/n1213/ag102/timer.c
```

```
timer.c
```

```
/*
 * (C) Copyright 2009 Faraday Technology
 * Po-Yu Chuang <ratbert@faraday-tech.com>
 *
 * Copyright (C) 2011 Andes Technology Corporation
 * Shawn Lin, Andes Technology Corporation <nobuhiro@andestech.com>
 * Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
```

```
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./arch/nds32/include/asm/arch-ag101/ag101.h
```

```
ag101.h
```

```
/*
* Copyright (C) 2011 Andes Technology Corporation
* Nobuhiro Lin, Andes Technology Corporation <nobuhiro@andestech.com>
* Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
*
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* it under the terms of the GNU General Public License as published by
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* (at your option) any later version.
*
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* MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
* GNU General Public License for more details.
*
* You should have received a copy of the GNU General Public License
* along with this program; if not, write to the Free Software
* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./arch/nds32/include/asm/arch-ag102/ag102.h
```

```
ag102.h
```

```
/*
* Copyright (C) 2011 Andes Technology Corporation
* Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
*
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* it under the terms of the GNU General Public License as published by
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* (at your option) any later version.
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```

```
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./arch/nds32/include/asm/bitops.h
```

```
bitops.h
```

```
/*
 * Copyright 1995, Russell King.
 * Various bits and pieces copyrights include:
 *   Linus Torvalds (test_bit).
 *
 * Copyright (C) 2011 Andes Technology Corporation
 * Shawn Lin, Andes Technology Corporation <nobuhiro@andestech.com>
 *
 * bit 0 is the LSB of addr; bit 32 is the LSB of (addr+1).
 *
 * Please note that the code in this file should never be included
 * from user space. Many of these are not implemented in assembler
 * since they would be too costly. Also, they require privileged
 * instructions (which are not available from user mode) to ensure
 * that they are atomic.
*/
```

```
./arch/nds32/include/asm/byteorder.h
```

```
byteorder.h
```

```
/*
 * linux/include/asm-arm/byteorder.h
 *
 * Copyright (C) 2011 Andes Technology Corporation
 * Shawn Lin, Andes Technology Corporation <nobuhiro@andestech.com>
 * Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
 *
 * ARM Endian-ness. In little endian mode, the data bus is connected such
 * that byte accesses appear as:
 *   0 = d0...d7, 1 = d8...d15, 2 = d16...d23, 3 = d24...d31
 * and word accesses (data or instruction) appear as:
 *   d0...d31
 *
 * When in big endian mode, byte accesses appear as:
 *   0 = d24...d31, 1 = d16...d23, 2 = d8...d15, 3 = d0...d7
 * and word accesses (data or instruction) appear as:
 *   d0...d31
*/
```

```
./arch/nds32/include/asm/cache.h
```

cache.h

```
/*
 * Copyright (C) 2011 Andes Technology Corporation
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 * Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/nds32/include/asm/config.h

config.h

```
/*
 * Copyright (C) 2011 Andes Technology Corporation
 * Copyright (C) 2010 Shawn Lin (nobuhiro@andestech.com)
 * Copyright (C) 2011 Macpaul Lin (macpaul@andestech.com)
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/nds32/include/asm/global_data.h

global_data.h

```
/*
 * (C) Copyright 2002
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
 * Copyright (C) 2011 Andes Technology Corporation
 * Shawn Lin, Andes Technology Corporation <nobuhiro@andestech.com>
 * Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/nds32/include/asm/io.h

io.h

```
/*
 * linux/include/asm-nds/io.h
 *
 * Copyright (C) 1996-2000 Russell King
 *
 * Copyright (C) 2011 Andes Technology Corporation
 * Shawn Lin, Andes Technology Corporation <nobuhiro@andestech.com>
 * Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
 *
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 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 *
 * Modifications:
 * 16-Sep-1996 RMK      Inlined the inx/outx functions & optimised for both
 *                    constant addresses and variable addresses.
 * 04-Dec-1997 RMK      Moved a lot of this stuff to the new architecture
 *                    specific IO header files.
 * 27-Mar-1999 PJB      Second parameter of memcopy_toio is const..
 * 04-Apr-1999 PJB      Added check_signature.
```

```
* 12-Dec-1999 RMK      More cleanups
* 18-Jun-2000 RMK      Removed virt_to_* and friends definitions
*/
```

```
./arch/nds32/include/asm/linkage.h
linkage.h
```

```
/*
 * U-boot - linkage.h
 *
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 *
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 * project.
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
 * MA 02110-1301 USA
 */
```

```
./arch/nds32/include/asm/mach-types.h
mach-types.h
```

```
/*
 * This was automagically generated from arch/nds/tools/mach-types!
 * Do NOT edit
 */
/* The type of machine we're running on */
```

```
./arch/nds32/include/asm/macro.h
macro.h
```

```
/*
 * include/asm-nds32/macro.h
 *
 * Copyright (C) 2009 Jean-Christophe PLAGNIOL-VILLARD <plagnioj@jcrosoft.com>
 * Copyright (C) 2011 Andes Technology Corporation
 * Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
 *
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 * project.
 */
```

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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/nds32/include/asm/posix_types.h
posix_types.h
```

```
/*
* linux/include/asm-arm/posix_types.h
*
* Copyright (C) 1996-1998 Russell King.
*
* Copyright (C) 2011 Andes Technology Corporation
* Copyright (C) 2010 Shawn Lin (nobuhiro@andestech.com)
* Copyright (C) 2011 Macpaul Lin (macpaul@andestech.com)
*
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* published by the Free Software Foundation.
*
* Changelog:
* 27-06-1996 RMK Created
* 05-03-2010 Modified for arch NDS32
*/
```

```
./arch/nds32/include/asm/processor.h
processor.h
```

```
/*
* linux/include/asm-arm/processor.h
*
* Copyright (C) 1995-2002 Russell King
*
* Copyright (C) 2011 Andes Technology Corporation
* Copyright (C) 2010 Shawn Lin (nobuhiro@andestech.com)
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```

*/

./arch/nds32/include/asm/ptrace.h
ptrace.h

/*

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*

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*/

./arch/nds32/include/asm/string.h
string.h

/*

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*/

./arch/nds32/include/asm/system.h
system.h

/*

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* MA 02110-1301 USA

*/

./arch/nds32/include/asm/types.h

types.h

/*

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*/

./arch/nds32/include/asm/u-boot-nds32.h

u-boot-nds32.h

/*

* (C) Copyright 2002
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*

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* Shawn Lin, Andes Technology Corporation <nobuhiro@andestech.com>
* Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
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* MA 02111-1307 USA

*/

./arch/nds32/include/asm/u-boot.h

u-boot.h

/*

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* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*

*

```
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*****
* NOTE: This header file defines an interface to U-Boot. Including
* this (unmodified) header file in another file is considered normal
* use of U-Boot, and does *not* fall under the heading of "derived
* work".
*****
*/
```

```
./arch/nds32/include/asm/unaligned.h
unaligned.h
No license information found.
```

```
./arch/nds32/lib/board.c
board.c
```

```
/*
* (C) Copyright 2002-2006
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* Copyright (C) 2011 Andes Technology Corporation
* Shawn Lin, Andes Technology Corporation <nobuhiro@andestech.com>
* Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
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```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/nds32/lib/bootm.c
```

```
bootm.c
```

```
/*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*/
```

```
./arch/nds32/lib/cache.c
```

```
cache.c
```

```
/*
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* Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
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*
*/
```

```
./arch/nds32/lib/interrupts.c
interrupts.c
```

```
/*
* (C) Copyright 2002
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Alex Zuepke <azu@sysgo.de>
*
* Copyright (C) 2011 Andes Technology Corporation
* Shawn Lin, Andes Technology Corporation <nobuhiro@andestech.com>
* Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/nios2/cpu/cpu.c
cpu.c
```

```
/*
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* Scott McNutt <smcnutt@psyent.com>
*
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* MA 02111-1307 USA
*/
```

```
./arch/nios2/cpu/epcs.c
```

```
epcs.c
```

```
/*
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* Scott McNutt <smcnutt@psyent.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/nios2/cpu/fdt.c
```

```
fdt.c
```

```
/*
* (C) Copyright 2011, Missing Link Electronics
* Joachim Foerster <j Joachim@missinglinkelectronics.com>
*
* Taken from arch/powerpc/cpu/ppc4xx/fdt.c:
*
* (C) Copyright 2007-2008
* Stefan Roes, DENX Software Engineering, sr@denx.de.
*
*/
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/nios2/cpu/interrupts.c
```

```
interrupts.c
```

```
/*
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* Scott McNutt <smcnutt@psyent.com>
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* MA 02111-1307 USA
*/
```

```
./arch/nios2/cpu/sysid.c
```

```
sysid.c
```

```
/*
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/nios2/cpu/traps.c
```

```
traps.c
```

```
/*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/nios2/include/asm/bitops/atomic.h
```

```
atomic.h
```

```
/* Use an array of spinlocks for our atomic_ts.
```

```
* Hash function to index into a different SPINLOCK.  
* Since "a" is usually an address, use one spinlock per cacheline.  
*/
```

```
./arch/nios2/include/asm/bitops/ffs.h  
ffs.h
```

```
/**  
 * ffs - find first bit set  
 * @x: the word to search  
 *  
 * This is defined the same way as  
 * the libc and compiler builtin ffs routines, therefore  
 * differs in spirit from the above ffz (man ffs).  
*/
```

```
./arch/nios2/include/asm/bitops/non-atomic.h  
non-atomic.h
```

```
/**  
 * __set_bit - Set a bit in memory  
 * @nr: the bit to set  
 * @addr: the address to start counting from  
 *  
 * Unlike set_bit(), this function is non-atomic and may be reordered.  
 * If it's called on the same region of memory simultaneously, the effect  
 * may be that only one operation succeeds.  
*/
```

```
./arch/nios2/include/asm/bitops.h  
bitops.h
```

```
/*  
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 * Scott McNutt <smcnutt@psyent.com>  
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
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```

*/

./arch/nios2/include/asm/byteorder.h

byteorder.h

/*

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*/

./arch/nios2/include/asm/cache.h

cache.h

/*

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```

*/

./arch/nios2/include/asm/config.h
config.h
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 * MA 02111-1307 USA
 */

./arch/nios2/include/asm/dma-mapping.h
dma-mapping.h
/* dma_alloc_coherent() return cache-line aligned allocation which is mapped
 * to uncached io region.
 *
 * IO_REGION_BASE should be defined in board config header file
 * 0x80000000 for nommu, 0xe0000000 for mmu
 */

./arch/nios2/include/asm/errno.h
errno.h
No license information found.

./arch/nios2/include/asm/global_data.h
global_data.h
/*
 * (C) Copyright 2004, Psyent Corporation <www.psyent.com>
 * Scott McNutt <smcnutt@psyent.com>
 *
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 * project.
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```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/nios2/include/asm/gpio.h
```

```
gpio.h
```

```
/*
 * nios2 gpio driver
 *
 * This gpio core is described in http://nioswiki.com/GPIO
 * bit[0] data
 * bit[1] output enable
 *
 * When CONFIG_SYS_GPIO_BASE is not defined, the board may either
 * provide its own driver or the altera_pio driver may be used.
 *
 * Copyright (C) 2010 Thomas Chou <thomas@wytron.com.tw>
 *
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 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 */
```

```
./arch/nios2/include/asm/io.h
```

```
io.h
```

```
/*
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 * Scott McNutt <smcnutt@psyent.com>
 *
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 */
```

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* MA 02111-1307 USA
*/
```

```
./arch/nios2/include/asm/opcodes.h
```

```
opcodes.h
```

```
/*
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./arch/nios2/include/asm/posix_types.h
```

```
posix_types.h
```

```
/*
 * This file is generally used by user-level software, so you need to
 * be a little careful about namespace pollution etc. Also, we cannot
 * assume GCC is being used.
*/
```

```
./arch/nios2/include/asm/processor.h
```

```
processor.h
```

```
/*
 * (C) Copyright 2004, Psyent Corporation <www.psyent.com>
 * Scott McNutt <smcnutt@psyent.com>
 *
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/nios2/include/asm/psr.h
```

```
psr.h
```

```
/*
* (C) Copyright 2004, Psyent Corporation <www.psyent.com>
* Scott McNutt <smcnutt@psyent.com>
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* MA 02111-1307 USA
*/
```

```
./arch/nios2/include/asm/ptrace.h
```

```
ptrace.h
```

```
/*
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* Scott McNutt <smcnutt@psyent.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/nios2/include/asm/string.h
string.h
```

```
/*
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* Scott McNutt <smcnutt@psyent.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/nios2/include/asm/system.h
system.h
```

```
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* Scott McNutt <smcnutt@psyent.com>
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/nios2/include/asm/types.h
```

```
types.h
```

```
/*
* This file is never included by application software unless
* explicitly requested (e.g., via linux/types.h) in which case the
* application is Linux specific so (user-) name space pollution is
* not a major issue. However, for interoperability, libraries still
* need to be careful to avoid a name clashes.
*/
```

```
./arch/nios2/include/asm/u-boot.h
```

```
u-boot.h
```

```
/*
* (C) Copyright 2004, Psyent Corporation <www.psyent.com>
* Scott McNutt <smcnutt@psyent.com>
*
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* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*****
* NOTE: This header file defines an interface to U-Boot. Including
```

```
* this (unmodified) header file in another file is considered normal
* use of U-Boot, and does *not* fall under the heading of "derived
* work".
*****
*/
```

```
./arch/nios2/include/asm/unaligned.h
unaligned.h
No license information found.
```

```
./arch/nios2/lib/board.c
board.c
```

```
/*
 * (C) Copyright 2003, Psyent Corporation <www.psyent.com>
 * Scott McNutt <smcnutt@psyent.com>
 *
 * (C) Copyright 2000-2002
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./arch/nios2/lib/bootm.c
bootm.c
```

```
/*
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 * Scott McNutt <smcnutt@psyent.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/nios2/lib/libgcc.c
```

```
libgcc.c
```

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/*
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* License along with GNU CC; see the file COPYING. If not, write
* to the Free Software Foundation, 59 Temple Place - Suite 330,
* Boston, MA 02111-1307, USA.
*/
```

```
./arch/nios2/lib/longlong.h
```

```
longlong.h
```

```
/* longlong.h -- definitions for mixed size 32/64 bit arithmetic.
```

```
./arch/nios2/lib/time.c
```

```
time.c
```

```
/*
* (C) Copyright 2003, Psyent Corporation <www.psyent.com>
* Scott McNutt <smcnutt@psyent.com>
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* MA 02111-1307 USA
*/
```

```
./arch/openrisc/cpu/cache.c
```

```
cache.c
```

```
/*
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* MA 02111-1307 USA
*/
```

```
./arch/openrisc/cpu/cpu.c
```

```
cpu.c
```

```
/*
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* (C) Copyright 2011, Julius Baxter <julius@opencores.org>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/openrisc/cpu/exceptions.c
```

```
exceptions.c
```

```
/*
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* (C) Copyright 2011, Julius Baxter <julius@opencores.org>
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* MA 02111-1307 USA
*/
```

```
./arch/openrisc/cpu/interrupts.c
```

```
interrupts.c
```

```
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```

```
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/openrisc/include/asm/bitops/ffs.h  
ffs.h
```

```
/*  
* OpenRISC Linux  
*  
* Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se>  
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* it under the terms of the GNU General Public License as published by  
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*/
```

```
./arch/openrisc/include/asm/bitops/fls.h  
fls.h
```

```
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* OpenRISC Linux  
*  
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*/
```

```
./arch/openrisc/include/asm/bitops.h  
bitops.h
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/openrisc/include/asm/byteorder.h
byteorder.h
No license information found.
```

```
./arch/openrisc/include/asm/cache.h
cache.h
```

```
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 * MA 02111-1307 USA
 */
```

```
./arch/openrisc/include/asm/config.h
config.h
```

```
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 * MA 02111-1307 USA
 */
```

```
./arch/openrisc/include/asm/errno.h
errno.h
```

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```
./arch/openrisc/include/asm/global_data.h
global_data.h
```

```
/*
 * (C) Copyright 2004 Atmark Techno, Inc.
 *
 * Yasushi SHOJI <yashi@atmark-techno.com>
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 * MA 02111-1307 USA
 */
```

```
./arch/openrisc/include/asm/gpio.h
gpio.h
```

```
/*
 * OpenRISC gpio driver
 *
 * Copyright (C) 2011 Stefan Kristiansson <stefan.kristiansson@saunalahti.fi>
 *
 * based on nios2 gpio driver
 * Copyright (C) 2010 Thomas Chou <thomas@wytron.com.tw>
 *
 * when CONFIG_SYS_GPIO_BASE is not defined, board may provide
 * its own driver.
 *
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/openrisc/include/asm/io.h
```

```
io.h
```

```
/*
```

```
* (C) Copyright 2011, Julius Baxter <julius@opencores.org>
```

```
*
```

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```

```
*/
```

```
./arch/openrisc/include/asm/openrisc_exc.h
```

```
openrisc_exc.h
```

```
/*
```

```
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```

```
*
```

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```

```
*/
```

```
./arch/openrisc/include/asm/posix_types.h
```

posix_types.h

```
/*
 * Based on microblaze implementation:
 * Copyright (C) 2003      John Williams <jwilliams@itee.uq.edu.au>
 * Copyright (C) 2001,2002 NEC Corporation
 * Copyright (C) 2001,2002 Miles Bader <miles@gnu.org>
 *
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 * Public License. See the file COPYING in the main directory of this
 * archive for more details.
 *
 * Written by Miles Bader <miles@gnu.org>
 * Microblaze port by John Williams
 */
```

./arch/openrisc/include/asm/processor.h

processor.h

No license information found.

./arch/openrisc/include/asm/ptrace.h

ptrace.h

```
/*
 * OpenRISC Linux
 *
 * Linux architectural port borrowing liberally from similar works of
 * others. All original copyrights apply as per the original source
 * declaration.
 *
 * OpenRISC implementation:
 * Copyright (C) 2003 Matjaz Breskvar <phoenix@bsemi.com>
 * Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se>
 * et al.
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 * (at your option) any later version.
 */
```

./arch/openrisc/include/asm/spr-defs.h

spr-defs.h

```
/*
 * SPR Definitions
 *
 * Copyright (C) 2000 Damjan Lampret
 * Copyright (C) 2003 Matjaz Breskvar <phoenix@bsemi.com>
 * Copyright (C) 2008, 2010 Embecosm Limited
 * Copyright (C) 2010-2011 Jonas Bonn <jonas@southpole.se>
 * et al.
 */
```

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```

```
*/
* This file is part of OpenRISC 1000 Architectural Simulator.
```

```
./arch/openrisc/include/asm/string.h
string.h
No license information found.
```

```
./arch/openrisc/include/asm/system.h
system.h
```

```
/*
* (C) Copyright 2011, Julius Baxter <julius@opencores.org>
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/openrisc/include/asm/types.h
types.h
```

```
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```

```
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* MA 02111-1307 USA  
*/
```

```
./arch/openrisc/include/asm/u-boot.h  
u-boot.h
```

```
/*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*  
*****  
* NOTE: This header file defines an interface to U-Boot. Including  
* this (unmodified) header file in another file is considered normal  
* use of U-Boot, and does *not* fall under the heading of "derived  
* work".  
*****  
*/
```

```
./arch/openrisc/include/asm/unaligned.h  
unaligned.h  
No license information found.
```

```
./arch/openrisc/lib/board.c  
board.c
```

```
/*  
* (C) Copyright 2011  
* Julius Baxter, julius@opencores.org  
*  
* (C) Copyright 2003, Psyent Corporation <www.psyent.com>  
* Scott McNutt <smcnutt@psyent.com>  
*  
* (C) Copyright 2000-2002
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* MA 02111-1307 USA  
*/
```

```
./arch/openrisc/lib/bootm.c
```

```
bootm.c
```

```
/*  
* (C) Copyright 2011 Stefan Kristiansson <stefan.kristiansson@saunalahti.fi>  
*  
* Based on microblaze implementation by:  
* (C) Copyright 2007 Michal Simek  
* (C) Copyright 2004 Atmark Techno, Inc.  
*  
* Michal SIMEK <monstr@monstr.eu>  
* Yasushi SHOJI <yashi@atmark-techno.com>  
*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
```

```
* MA 02111-1307 USA
*/
```

```
./arch/openrisc/lib/timer.c
timer.c
```

```
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*/
```

```
./arch/powerpc/cpu/74xx_7xx/cpu.c
cpu.c
```

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```

```
./arch/powerpc/cpu/74xx_7xx/cpu_init.c
cpu_init.c
```

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```
./arch/powerpc/cpu/74xx_7xx/interrupts.c
interrupts.c
```

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*/
```

```
./arch/powerpc/cpu/74xx_7xx/speed.c
```

```
speed.c
```

```
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*/
```

```
./arch/powerpc/cpu/74xx_7xx/traps.c
```

```
traps.c
```

```
/*
* linux/arch/powerpc/kernel/traps.c
*
* Copyright (C) 1995-1996 Gary Thomas (gdt@linuxppc.org)
*
* Modified by Cort Dougan (cort@cs.nmt.edu)
* and Paul Mackerras (paulus@cs.anu.edu.au)
*
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*/
```

```
./arch/powerpc/cpu/mpc512x/asm-offsets.h
asm-offsets.h
/*
* needed for arch/powerpc/cpu/mpc512x/start.S
*
* These should be auto-generated
*/
```

```
./arch/powerpc/cpu/mpc512x/cache.c
cache.c
/*
* Copyright (C) 2012 Marek Vasut <marex@denx.de>
*
* This file contains stub implementation of
*   invalidate_dcache_range()
*   flush_dcache_range()
*
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*/
```

```
./arch/powerpc/cpu/mpc512x/cpu.c
cpu.c
```

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 */
```

```
./arch/powerpc/cpu/mpc512x/cpu_init.c
cpu_init.c
```

```
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 * MA 02111-1307 USA
 *
 * Derived from the MPC83xx code.
 */
```

```
./arch/powerpc/cpu/mpc512x/diu.c
```

```
diu.c
```

```
/*
```

```
* Copyright 2008 Freescale Semiconductor, Inc.
```

```
* York Sun <yorksun@freescale.com>
```

```
*
```

```
* FSL DIU Framebuffer driver
```

```
*
```

```
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```

```
*
```

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```

```
*/
```

```
./arch/powerpc/cpu/mpc512x/fixed_sdram.c
```

```
fixed_sdram.c
```

```
/*
```

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```

```
*
*/

./arch/powerpc/cpu/mpc512x/i2c.c
i2c.c
/*
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 *
 * Based on the MPC5xxx code.
*/
```

```
./arch/powerpc/cpu/mpc512x/ide.c
ide.c
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```

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*/
```

```
./arch/powerpc/cpu/mpc512x/iim.c
```

```
iim.c
```

```
/*  
* Copyright 2008 Silicon Turnkey Express, Inc.  
* Martha Marx <mmarx@silicontkx.com>  
*  
* ADS5121 IIM (Fusebox) Interface  
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* MA 02111-1307 USA  
*/
```

```
./arch/powerpc/cpu/mpc512x/interrupts.c
```

```
interrupts.c
```

```
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* MA 02111-1307 USA
*
* Derived from the MPC83xx code.
*/
```

```
./arch/powerpc/cpu/mpc512x/iopin.c
```

```
iopin.c
```

```
/*
```

```
* (C) Copyright 2008
* Martha J Marx, Silicon Turnkey Express, mmarx@silicontkx.com
* mpc512x I/O pin/pad initialization for the ADS5121 board
* See file CREDITS for list of people who contributed to this
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*/
```

```
./arch/powerpc/cpu/mpc512x/pci.c
```

```
pci.c
```

```
/*
```

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*/
```

```
./arch/powerpc/cpu/mpc512x/serial.c
```

```
serial.c
```

```
/*
```

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*  
* Based on the MPC5200 PSC driver.  
* Adapted for MPC512x by Jan Wrobel <wrr@semihalf.com>  
*/
```

```
./arch/powerpc/cpu/mpc512x/speed.c
```

```
speed.c
```

```
/*
```

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*
* Based on the MPC83xx code.
*/
```

```
./arch/powerpc/cpu/mpc512x/traps.c
```

```
traps.c
```

```
/*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
* Derived from the MPC83xx code.
*/
```

```
./arch/powerpc/cpu/mpc5xx/cpu.c
```

```
cpu.c
```

```
/*
* (C) Copyright 2003
* Martin Winistoerfer, martinwinistoerfer@gmx.ch.
*
*/
```

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```

```
./arch/powerpc/cpu/mpc5xx/cpu_init.c
cpu_init.c
```

```
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 * Foundation,
*/
```

```
./arch/powerpc/cpu/mpc5xx/interrupts.c
interrupts.c
```

```
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```

```
./arch/powerpc/cpu/mpc5xx/serial.c
serial.c
```

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*/
```

```
./arch/powerpc/cpu/mpc5xx/speed.c
speed.c
```

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*/
```

```
./arch/powerpc/cpu/mpc5xx/spi.c
```

```
spi.c
```

```
/*
* Copyright (c) 2001 Navin Boppuri / Prashant Patel
* <nboppuri@trinetcommunication.com>,
* <pmpatel@trinetcommunication.com>
* Copyright (c) 2001 Gerd Mennchen <Gerd.Mennchen@icn.siemens.de>
* Copyright (c) 2001 Wolfgang Denk, DENX Software Engineering, <wd@denx.de>.
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* MA 02111-1307 USA
*/
```

```
./arch/powerpc/cpu/mpc5xx/traps.c
```

```
traps.c
```

```
/*
* linux/arch/powerpc/kernel/traps.c
*
* Copyright (C) 1995-1996 Gary Thomas (gdt@linuxppc.org)
*
* Modified by Cort Dougan (cort@cs.nmt.edu)
```

```
* and Paul Mackerras (paulus@cs.anu.edu.au)
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/powerpc/cpu/mpc5xxx/cpu.c
cpu.c
/*
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/powerpc/cpu/mpc5xxx/cpu_init.c
```

cpu_init.c

```
/*  
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
 * MA 02111-1307 USA  
 */
```

./arch/powerpc/cpu/mpc5xxx/i2c.c

i2c.c

```
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
 * MA 02111-1307 USA  
 */
```

./arch/powerpc/cpu/mpc5xxx/ide.c

ide.c

```
/*
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 *
 * Init is derived from Linux code.
 */
```

./arch/powerpc/cpu/mpc5xxx/interrupts.c

interrupts.c

```
/*
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 *
 * (C) Copyright -2003
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
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*/
```

```
./arch/powerpc/cpu/mpc5xxx/loadtask.c
loadtask.c
```

```
/*
* (C) Copyright 2003
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*
* This file is based on code
* (C) Copyright Motorola, Inc., 2000
*/
/* BestComm/SmartComm microcode */
```

```
./arch/powerpc/cpu/mpc5xxx/pci_mpc5200.c
pci_mpc5200.c
```

```
/*
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*/
```

```
./arch/powerpc/cpu/mpc5xxx/serial.c
serial.c
```

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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*  
* Hacked for MPC8260 by Murray.Jensen@cmst.csiro.au, 19-Oct-00, with  
* changes based on the file arch/powerpc/mbxboot/m8260_tty.c from the  
* Linux/PPC sources (m8260_tty.c had no copyright info in it).  
*  
* Martin Krause, 8 Jun 2006  
* Added SERIAL_MULTI support  
*/
```

```
./arch/powerpc/cpu/mpc5xxx/speed.c  
speed.c
```

```
/*  
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* MA 02111-1307 USA  
*/
```

./arch/powerpc/cpu/mpc5xxx/spl_boot.c

spl_boot.c

/*

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*/

./arch/powerpc/cpu/mpc5xxx/traps.c

traps.c

/*

* linux/arch/powerpc/kernel/traps.c

*

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*

* Modified by Cort Dougan (cort@cs.nmt.edu)

* and Paul Mackerras (paulus@cs.anu.edu.au)

* fixed Machine Check Reasons by Reinhard Meyer (r.meyer@emk-elektronik.de)

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```
*/  
  
./arch/powerpc/cpu/mpc5xxx/usb.c  
usb.c  
/*  
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* Markus Klotzbuecher, DENX Software Engineering <mk@denx.de>  
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* MA 02111-1307 USA  
*/
```

```
./arch/powerpc/cpu/mpc5xxx/usb_ohci.c  
usb_ohci.c  
/*  
* URB OHCI HCD (Host Controller Driver) for USB on the MPC5200.  
*  
* (C) Copyright 2003-2004  
* Gary Jennejohn, DENX Software Engineering <garyj@denx.de>  
*  
* (C) Copyright 2004  
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* Note: Much of this code has been derived from Linux 2.4  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*  
*/  
/*  
* IMPORTANT NOTES  
* 1 - this driver is intended for use with USB Mass Storage Devices  
* (BBB) ONLY. There is NO support for Interrupt or Isochronous pipes!  
*/
```

```
./arch/powerpc/cpu/mpc5xxx/usb_ohci.h  
usb_ohci.h
```

```
/*  
* URB OHCI HCD (Host Controller Driver) for USB.  
*  
* (C) Copyright 1999 Roman Weissgaerber <weissg@vienna.at>  
* (C) Copyright 2000-2001 David Brownell <dbrownell@users.sourceforge.net>  
*  
* usb-ohci.h  
*/  
/* mapping of the OHCI CC status to error codes */
```

```
./arch/powerpc/cpu/mpc8220/cpu.c  
cpu.c
```

```
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```

```
./arch/powerpc/cpu/mpc8220/cpu_init.c  
cpu_init.c
```

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*/
```

```
./arch/powerpc/cpu/mpc8220/dma.h  
dma.h
```

```
/*  
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*  
* This file is based on code  
* (C) Copyright Motorola, Inc., 2000  
*  
* MPC8220 dma header file  
*/
```

```
./arch/powerpc/cpu/mpc8220/dramSetup.c  
dramSetup.c
```

```
/*  
* (C) Copyright 2004, Freescale, Inc  
* TsiChung Liew, Tsi-Chung.Liew@freescale.com  
*  
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*  
*/
```

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*/
```

```
./arch/powerpc/cpu/mpc8220/dramSetup.h
dramSetup.h
```

```
/*
* dramSetup.h
*
* Prototypes, etc. for the Motorola MPC8220
* embedded cpu chips
*
* 2004 (c) Freescale, Inc.
* Author: TsiChung Liew <Tsi-Chung.Liew@freescale.com>
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*/
```

```
./arch/powerpc/cpu/mpc8220/fec.c
fec.c
```

```
/*
* (C) Copyright 2003-2010
```

```
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
* This file is based on mpc4200fec.c,  
* (C) Copyright Motorola, Inc., 2000  
*/
```

```
./arch/powerpc/cpu/mpc8220/fec.h  
fec.h
```

```
/*  
* (C) Copyright 2003-2004  
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*  
* This file is based on mpc4200fec.h  
* (C) Copyright Motorola, Inc., 2000  
*  
* odin ethernet header file  
*/
```

```
./arch/powerpc/cpu/mpc8220/i2c.c  
i2c.c
```

```
/*  
* (C) Copyright 2004, Freescale, Inc  
* TsiChung Liew, Tsi-Chung.Liew@freescale.com.  
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* MA 02111-1307 USA  
*/
```

```
./arch/powerpc/cpu/mpc8220/i2cCore.c  
i2cCore.c
```

```
/* I2cCore.c - MPC8220 PPC I2C Library */  
/* Copyright 2004 Freescale Semiconductor, Inc. */  
/*  
*/
```

```
./arch/powerpc/cpu/mpc8220/i2cCore.h
```

```
i2cCore.h
```

```
/*  
 * i2cCore.h  
 *  
 * Prototypes, etc. for the Motorola MPC8220  
 * embedded cpu chips  
 *  
 * 2004 (c) Freescale, Inc.  
 * Author: TsiChung Liew <Tsi-Chung.Liew@freescale.com>  
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
 * MA 02111-1307 USA  
 */
```

```
./arch/powerpc/cpu/mpc8220/interrupts.c
```

```
interrupts.c
```

```
/*  
 * (C) Copyright -2003  
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
 *  
 * (C) Copyright 2001  
 * Josh Huber <huber@mclx.com>, Mission Critical Linux, Inc.  
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* MA 02111-1307 USA
*/
```

```
./arch/powerpc/cpu/mpc8220/loadtask.c
```

```
loadtask.c
```

```
/*
* (C) Copyright 2003
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* This file is based on code
* (C) Copyright Motorola, Inc., 2000
*/
/* Multichannel DMA microcode */
```

```
./arch/powerpc/cpu/mpc8220/pci.c
```

```
pci.c
```

```
/*
* Copyright 2004 Freescale Semiconductor.
* Copyright (C) 2003 Motorola Inc.
* Xianghua Xiao (x.xiao@motorola.com)
*
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*/
```

```
./arch/powerpc/cpu/mpc8220/speed.c
```

```
speed.c
```

```
/*
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* TsiChung Liew, Tsi-Chung.Liew@freescale.com.
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./arch/powerpc/cpu/mpc8220/traps.c
traps.c
/*
* linux/arch/powerpc/kernel/traps.c
*
* Copyright (C) 1995-1996 Gary Thomas (gdt@linuxppc.org)
*
* Modified by Cort Dougan (cort@cs.nmt.edu)
* and Paul Mackerras (paulus@cs.anu.edu.au)
* fixed Machine Check Reasons by Reinhard Meyer (r.meyer@emk-elektronik.de)
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```

* MA 02111-1307 USA
*/

./arch/powerpc/cpu/mpc8220/uart.c
uart.c

```
/*
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/powerpc/cpu/mpc824x/cpu.c
cpu.c

```
/*
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```

```
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/powerpc/cpu/mpc824x/cpu_init.c  
cpu_init.c
```

```
/*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/powerpc/cpu/mpc824x/drivers/epic/epic.h  
epic.h
```

```
/*  
* mpc8240epic.h - EPIC module of the MPC8240 micro-controller  
*  
* Copyright 1999 Motorola Inc.  
*  
* Modification History:  
* =====  
* 01a,04Feb99,My Created.  
* 15Nov200, robt -modified to use in U-Boot  
*  
*/
```

```
./arch/powerpc/cpu/mpc824x/drivers/epic/epic1.c  
epic1.c
```

```
/*  
* copyright @ motorola, 1999  
*  
*****/
```

```
./arch/powerpc/cpu/mpc824x/drivers/epic.h
epic.h
No license information found.
```

```
./arch/powerpc/cpu/mpc824x/drivers/errors.h
errors.h
/*      Copyright Motorola, Inc. 1993, 1994
```

```
./arch/powerpc/cpu/mpc824x/drivers/i2c/i2c.c
i2c.c
/*
 * (C) Copyright 2003
 * Gleb Natapov <gnatapov@mr.v.com>
 * Some bits are taken from linux driver written by adrian@humboldt.co.uk
 *
 * Hardware I2C driver for MPC107 PCI bridge.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./arch/powerpc/cpu/mpc824x/drivers/i2c_export.h
i2c_export.h
/*****
 *
 * Copyright Motrola 1999
 *
 *****/
/* These are the defined return values for the I2C_do_transaction function.
 * Any non-zero value indicates failure. Failure modes can be added for
 * more detailed error reporting.
 */
```

```
./arch/powerpc/cpu/mpc824x/interrupts.c
```

interrupts.c

```
/*
 * (C) Copyright 2000-2002
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 * Rob Taylor, Flying Pig Systems. robt@flyingpig.com
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
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 */
```

./arch/powerpc/cpu/mpc824x/pci.c

pci.c

```
/*
 * arch/powerpc/kernel/mpc10x_common.c
 *
 * Common routines for the Motorola SPS MPC106, MPC107 and MPC8240 Host bridge,
 * Mem ctlr, EPIC, etc.
 *
 * Author: Mark A. Greer
 *         mgreer@mvista.com
 *
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 * under the terms of the GNU General Public License as published by the
 * Free Software Foundation; either version 2 of the License, or (at your
 * option) any later version.
 */
```

./arch/powerpc/cpu/mpc824x/speed.c

speed.c

```
/*
 * (C) Copyright 2001
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
 */
```

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* Gregory E. Allen, gallen@arlut.utexas.edu
* Applied Research Laboratories, The University of Texas at Austin
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/powerpc/cpu/mpc824x/traps.c
```

```
traps.c
```

```
/*
```

```
* linux/arch/powerpc/kernel/traps.c
```

```
*
```

```
* Copyright (C) 1995-1996 Gary Thomas (gdt@linuxppc.org)
```

```
*
```

```
* Modified by Cort Dougan (cort@cs.nmt.edu)
```

```
* and Paul Mackerras (paulus@cs.anu.edu.au)
```

```
*
```

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* (C) Copyright 2000
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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*/
```

```
./arch/powerpc/cpu/mpc8260/bedbug_603e.c
bedbug_603e.c
```

```
/*
* Bedbug Functions specific to the MPC603e core
*/
```

```
./arch/powerpc/cpu/mpc8260/commproc.c
```

```
commproc.c
```

```
/*
* This file is based on "arch/powerpc/8260_io/commproc.c" - here is it's
* copyright notice:
*
* General Purpose functions for the global management of the
* 8260 Communication Processor Module.
* Copyright (c) 1999 Dan Malek (dmalek@jlc.net)
* Copyright (c) 2000 MontaVista Software, Inc (source@mvista.com)
* 2.3.99 Updates
*
* In addition to the individual control of the communication
* channels, there are a few functions that globally affect the
* communication processor.
*
* Buffer descriptors must be allocated from the dual ported memory
* space. The allocator for that is here. When the communication
* process is reset, we reclaim the memory available. There is
* currently no deallocator for this memory.
*/
```

```
./arch/powerpc/cpu/mpc8260/cpu.c
```

```
cpu.c
```

```
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* MA 02111-1307 USA  
*/
```

```
./arch/powerpc/cpu/mpc8260/cpu_init.c  
cpu_init.c
```

```
/*  
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
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* MA 02111-1307 USA  
*/
```

```
./arch/powerpc/cpu/mpc8260/ether_fcc.c  
ether_fcc.c
```

```
/*  
* MPC8260 FCC Fast Ethernet  
*  
* Copyright (c) 2000 MontaVista Software, Inc. Dan Malek (dmalek@jlc.net)  
*  
* (C) Copyright 2000 Sysgo Real-Time Solutions, GmbH <www.elinos.com>  
* Marius Groeger <mgroeger@sysgo.de>  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./arch/powerpc/cpu/mpc8260/ether_scc.c
ether_scc.c
/*
* MPC8260 SCC Ethernet
*
* Copyright (c) 2000 MontaVista Software, Inc. Dan Malek (dmalek@jlc.net)
*
* (C) Copyright 2000 Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*
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* Advent Networks, Inc. <http://www.adventnetworks.com>
* Jay Monkman <jtm@smoothsmoothie.com>
*
* Modified so that it plays nicely when more than one ETHERNET interface
* is in use a la ether_fcc.c.
* (C) Copyright 2008
* DENX Software Engineerin GmbH
* Gary Jennejohn <garyj@denx.de>
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* MA 02111-1307 USA
*/
```

./arch/powerpc/cpu/mpc8260/i2c.c

i2c.c

/*

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*
* (C) Copyright 2000 Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
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*/

./arch/powerpc/cpu/mpc8260/interrupts.c

interrupts.c

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```
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*  
* Hacked for MPC8260 by Murray.Jensen@cmst.csiro.au, 22-Oct-00  
*/
```

```
./arch/powerpc/cpu/mpc8260/pci.c
```

```
pci.c
```

```
/*  
* (C) Copyright 2003  
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*  
* Copyright (c) 2005 MontaVista Software, Inc.  
* Vitaly Bordug <vbordug@ru.mvista.com>  
* Added support for PCI bridge on MPC8272ADS  
*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/powerpc/cpu/mpc8260/serial_scc.c
```

```
serial_scc.c
```

```
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* MA 02111-1307 USA
*
* Hacked for MPC8260 by Murray.Jensen@cmst.csiro.au, 19-Oct-00.
*/
```

```
./arch/powerpc/cpu/mpc8260/serial_smc.c
serial_smc.c
```

```
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*
* Hacked for MPC8260 by Murray.Jensen@cmst.csiro.au, 19-Oct-00, with
* changes based on the file arch/powerpc/mbxboot/m8260_tty.c from the
* Linux/PPC sources (m8260_tty.c had no copyright info in it).
*/
```

```
./arch/powerpc/cpu/mpc8260/speed.c
speed.c
```

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```

```
./arch/powerpc/cpu/mpc8260/speed.h
speed.h
```

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*/
```

```
./arch/powerpc/cpu/mpc8260/spi.c
spi.c
```

```
/*
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* <pmpatel@trinetcommunication.com>
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* MA 02111-1307 USA
*/
```

```
./arch/powerpc/cpu/mpc8260/traps.c
```

```
traps.c
```

```
/*
* linux/arch/powerpc/kernel/traps.c
*
* Copyright (C) 1995-1996 Gary Thomas (gdt@linuxppc.org)
*
* Modified by Cort Dougan (cort@cs.nmt.edu)
* and Paul Mackerras (paulus@cs.anu.edu.au)
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*/
```

```
./arch/powerpc/cpu/mpc83xx/cache.c  
cache.c
```

```
/*  
 * Copyright (C) 2012 Marek Vasut <marex@denx.de>  
 *  
 * This file contains stub implementation of  
 *   invalidate_dcache_range()  
 *   flush_dcache_range()  
 *  
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```

```
./arch/powerpc/cpu/mpc83xx/cpu.c  
cpu.c
```

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```

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*/
```

```
./arch/powerpc/cpu/mpc83xx/cpu_init.c
cpu_init.c
```

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```
./arch/powerpc/cpu/mpc83xx/ecc.c
ecc.c
```

```
/*
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 *
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 */
```

```
./arch/powerpc/cpu/mpc83xx/fdt.c
fdt.c
```

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*/
```

```
./arch/powerpc/cpu/mpc83xx/interrupts.c  
interrupts.c
```

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```

```
./arch/powerpc/cpu/mpc83xx/law.c  
law.c
```

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nand_init.c
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 */
```

```
./arch/powerpc/cpu/mpc83xx/pci.c
pci.c
/*
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 *
 * Author: Scott Wood <scottwood@freescale.com>,
 * with some bits from older board-specific PCI initialization.
 *
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```

```
./arch/powerpc/cpu/mpc83xx/pcie.c
pcie.c
```

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```
./arch/powerpc/cpu/mpc83xx/qe_io.c
qe_io.c
```

```
/*
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* based on source code of Shlomi Gridish
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```

```
./arch/powerpc/cpu/mpc83xx/serdes.c
```

```
serdes.c
```

```
/*
 * Freescale SerDes initialization routine
 *
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 *
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```

```
./arch/powerpc/cpu/mpc83xx/spd_sdram.c
```

```
spd_sdram.c
```

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./arch/powerpc/cpu/mpc83xx/speed.c
```

```
speed.c
```

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```
./arch/powerpc/cpu/mpc83xx/traps.c
```

```
traps.c
```

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```

```
./arch/powerpc/cpu/mpc85xx/b4860_ids.c
b4860_ids.c
```

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```
./arch/powerpc/cpu/mpc85xx/b4860_serdes.c
b4860_serdes.c
```

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```

```
./arch/powerpc/cpu/mpc85xx/cache.c  
cache.c
```

```
/*  
* Copyright (C) 2012 Marek Vasut <marex@denx.de>  
*  
* This file contains stub implementation of  
* invalidate_dcache_range()  
* flush_dcache_range()  
*  
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```

```
./arch/powerpc/cpu/mpc85xx/cmd_errata.c  
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```

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```

```
./arch/powerpc/cpu/mpc85xx/commproc.c
commproc.c
```

```
/*
 * Adapted for Motorola MPC8560 chips
 * Xianghua Xiao <x.xiao@motorola.com>
 *
 * This file is based on "arch/powerpc/8260_io/commproc.c" - here is it's
 * copyright notice:
 *
 * General Purpose functions for the global management of the
 * 8220 Communication Processor Module.
 * Copyright (c) 1999 Dan Malek (dmalek@jlc.net)
 * Copyright (c) 2000 MontaVista Software, Inc (source@mvista.com)
 * 2.3.99 Updates
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 *
 * In addition to the individual control of the communication
 * channels, there are a few functions that globally affect the
 * communication processor.
 *
 * Buffer descriptors must be allocated from the dual ported memory
 * space. The allocator for that is here. When the communication
 * process is reset, we reclaim the memory available. There is
 * currently no deallocator for this memory.
 */
```

```
./arch/powerpc/cpu/mpc85xx/cpu.c
cpu.c
```

```
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```

```
./arch/powerpc/cpu/mpc85xx/cpu_init.c
```

```
cpu_init.c
```

```
/*
```

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```

```
*
```

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```

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*
```

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```
*/
```

```
./arch/powerpc/cpu/mpc85xx/cpu_init_early.c
```

```
cpu_init_early.c
```

```
/*
```

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./arch/powerpc/cpu/mpc85xx/ddr-gen1.c
ddr-gen1.c
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./arch/powerpc/cpu/mpc85xx/ether_fcc.c
ether_fcc.c
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```

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```

```
./arch/powerpc/cpu/mpc85xx/fsl_corenet2_serdes.c
```

```
fsl_corenet2_serdes.c
```

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*/
```

```
./arch/powerpc/cpu/mpc85xx/fsl_corenet2_serdes.h
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```
fsl_corenet2_serdes.h
```

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./arch/powerpc/cpu/mpc85xx/fsl_corenet_serdes.c
```

```
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```

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```

```
fsl_corenet_serdes.h
```

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```
./arch/powerpc/cpu/mpc85xx/interrupts.c
```

```
interrupts.c
```

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```
./arch/powerpc/cpu/mpc85xx/liodn.c
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```

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```
./arch/powerpc/cpu/mpc85xx/mp.c
mp.c
/*
```

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```
./arch/powerpc/cpu/mpc85xx/mp.h
mp.h
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```

```
./arch/powerpc/cpu/mpc85xx/mpc8536_serdes.c
mpc8536_serdes.c
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```
./arch/powerpc/cpu/mpc85xx/mpc8544_serdes.c  
mpc8544_serdes.c
```

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```
./arch/powerpc/cpu/mpc85xx/mpc8548_serdes.c  
mpc8548_serdes.c
```

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```
./arch/powerpc/cpu/mpc85xx/mpc8568_serdes.c  
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./arch/powerpc/cpu/mpc85xx/mpc8569_serdes.c
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p1010_serdes.c
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p1021_serdes.c
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```
./arch/powerpc/cpu/mpc85xx/p1022_serdes.c
p1022_serdes.c
```

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```
./arch/powerpc/cpu/mpc85xx/p2041_ids.c  
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./arch/powerpc/cpu/mpc85xx/p2041_serdes.c  
p2041_serdes.c
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./arch/powerpc/cpu/mpc85xx/p3041_serdes.c
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p4080_ids.c

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./arch/powerpc/cpu/mpc85xx/p5020_ids.c

p5020_ids.c

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```

```
./arch/powerpc/cpu/mpc85xx/p5040_serdes.c
p5040_serdes.c
```

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```
./arch/powerpc/cpu/mpc85xx/pci.c
pci.c
```

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```
./arch/powerpc/cpu/mpc85xx/portals.c
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```

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```

```
./arch/powerpc/cpu/mpc85xx/qe_io.c
qe_io.c
```

```
/*
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```

```
* based on source code of Shlomi Gridish
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```

```
./arch/powerpc/cpu/mpc85xx/serial_scc.c
serial_scc.c
```

```
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*
* Hacked for MPC8260 by Murray.Jensen@cmst.csiro.au, 19-Oct-00.
*/
```

```
./arch/powerpc/cpu/mpc85xx/speed.c
```

speed.c

```
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./arch/powerpc/cpu/mpc85xx/spl_minimal.c

spl_minimal.c

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```
./arch/powerpc/cpu/mpc85xx/t4240_ids.c
t4240_ids.c
```

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```
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```

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```
./arch/powerpc/cpu/mpc85xx/tlb.c
```

```
tlb.c
```

```
/*
```

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```
*/
```

```
./arch/powerpc/cpu/mpc85xx/traps.c
```

```
traps.c
```

```
/*
```

```
* linux/arch/powerpc/kernel/traps.c
```

```
*
```

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```

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```

```
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```

```
./arch/powerpc/cpu/mpc86xx/cpu.c
```

```
cpu.c
```

```
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```

```
./arch/powerpc/cpu/mpc86xx/cpu_init.c
```

```
cpu_init.c
```

```
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```
./arch/powerpc/cpu/mpc86xx/ddr-8641.c
ddr-8641.c
```

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./arch/powerpc/cpu/mpc86xx/fdt.c
fdt.c
```

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```
./arch/powerpc/cpu/mpc86xx/interrupts.c
interrupts.c
```

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```

```
./arch/powerpc/cpu/mpc86xx/mp.c
```

```
mp.c
```

```
/*
```

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```

```
./arch/powerpc/cpu/mpc86xx/mpc8610_serdes.c
```

```
mpc8610_serdes.c
```

```
/*
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```
./arch/powerpc/cpu/mpc86xx/mpc8641_serdes.c
mpc8641_serdes.c
```

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```
./arch/powerpc/cpu/mpc86xx/speed.c
speed.c
```

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```
./arch/powerpc/cpu/mpc86xx/traps.c
```

```
traps.c
```

```
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```

```
*/
```

```
./arch/powerpc/cpu/mpc8xx/bedbug_860.c  
bedbug_860.c
```

```
/*
```

```
* Bedbug Functions specific to the MPC860 chip
```

```
*/
```

```
./arch/powerpc/cpu/mpc8xx/commproc.c  
commproc.c
```

```
/*
```

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```
*/
```

```
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cpu.c
```

```
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```
./arch/powerpc/cpu/mpc8xx/cpu_init.c  
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```

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```
./arch/powerpc/cpu/mpc8xx/fdt.c  
fdt.c
```

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```
./arch/powerpc/cpu/mpc8xx/fec.c
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```

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```
./arch/powerpc/cpu/mpc8xx/fec.h
fec.h
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* MA 02111-1307 USA
*/
```

```
./arch/powerpc/cpu/mpc8xx/i2c.c
i2c.c
```

```
/*
* (C) Copyright 2000
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*
* (C) Copyright 2000 Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
* Back ported to the 8xx platform (from the 8260 platform) by
* Murray.Jensen@cmst.csiro.au, 27-Jan-01.
*/
```

```
./arch/powerpc/cpu/mpc8xx/interrupts.c
interrupts.c
```

```
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*/
```

```
./arch/powerpc/cpu/mpc8xx/lcd.c
```

```
lcd.c
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/powerpc/cpu/mpc8xx/scc.c
```

```
scc.c
```

```
/*
* File: scc.c
* Description:
*     Basic ET HW initialization and packet RX/TX routines
*
* NOTE <<<IMPORTANT: PLEASE READ>>>:
*     Do not cache Rx/Tx buffers!
*/
```

```

/*
 * MPC823 <-> MC68160 Connections:
 *
 * Setup MPC823 to work with MC68160 Enhanced Ethernet
 * Serial Transceiver as follows:
 *
 * MPC823 Signal          MC68160  Comments
 * -----
 * PA-12 ETHTX   -----> TX          Eth. Port Transmit Data
 * PB-18 E_TENA  -----> TENA        Eth. Transmit Port Enable
 * PA-5 ETHTCK   <----- TCLK        Eth. Port Transmit Clock
 * PA-13 ETHRX   <----- RX          Eth. Port Receive Data
 * PC-8 E_RENA   <----- RENA        Eth. Receive Enable
 * PA-6 ETHRCK   <----- RCLK        Eth. Port Receive Clock
 * PC-9 E_CLSN   <----- CLSN        Eth. Port Collision Indication
 *
 * FADS Board Signal      MC68160  Comments
 * -----
 * (BCSR1) ETHEN*  -----> CS2        Eth. Port Enable
 * (BSCR4) TPSQEL* -----> TPSQEL   Twisted Pair Signal Quality Error Test
Enable
 * (BCSR4) TPFLDL* -----> TPFLDL   Twisted Pair Full-Duplex
 * (BCSR4) ETHLOOP -----> LOOP      Eth. Port Diagnostic Loop-Back
 *
 */

```

```

./arch/powerpc/cpu/mpc8xx/serial.c
serial.c

```

```

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 */

```

```
./arch/powerpc/cpu/mpc8xx/speed.c
speed.c
```

```
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 * MA 02111-1307 USA
 */
```

```
./arch/powerpc/cpu/mpc8xx/spi.c
spi.c
```

```
/*
 * Copyright (c) 2001 Navin Boppuri / Prashant Patel
 * <nboppuri@trinetcommunication.com>,
 * <pmpatel@trinetcommunication.com>
 * Copyright (c) 2001 Gerd Mennchen <Gerd.Mennchen@icn.siemens.de>
 * Copyright (c) 2001 Wolfgang Denk, DENX Software Engineering, <wd@denx.de>.
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```

```
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/powerpc/cpu/mpc8xx/traps.c  
traps.c
```

```
/*  
* linux/arch/powerpc/kernel/traps.c  
*  
* Copyright (C) 1995-1996 Gary Thomas (gdt@linuxppc.org)  
*  
* Modified by Cort Dougan (cort@cs.nmt.edu)  
* and Paul Mackerras (paulus@cs.anu.edu.au)  
*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/powerpc/cpu/mpc8xx/upatch.c  
upatch.c  
No license information found.
```

```
./arch/powerpc/cpu/mpc8xx/video.c  
video.c
```

```
/*  
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* (C) Copyright 2002  
* Wolfgang Denk, wd@denx.de  
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*/
```

```
./arch/powerpc/cpu/mpc8xx/wlkbd.c
wlkbd.c
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/powerpc/cpu/mpc8xxx/cpu.c
cpu.c
```

```
/*
* Copyright 2009-2012 Freescale Semiconductor, Inc.
*
* This file is derived from arch/powerpc/cpu/mpc85xx/cpu.c and
* arch/powerpc/cpu/mpc86xx/cpu.c. Basically this file contains
* cpu specific common code for 85xx/86xx processors.
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/powerpc/cpu/mpc8xxx/ddr/common_timing_params.h
common_timing_params.h
```

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 */
```

```
./arch/powerpc/cpu/mpc8xxx/ddr/ctrl_regs.c
ctrl_regs.c
```

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 * Software Foundation; either version 2 of the License, or (at your option)
 * any later version.
 */
```

```
/*
 * Generic driver for Freescale DDR/DDR2/DDR3 memory controller.
 * Based on code from spd_sdram.c
 * Author: James Yang [at freescale.com]
 */
```

```
./arch/powerpc/cpu/mpc8xxx/ddr/ddr.h
ddr.h
```

```
/*
 * Copyright 2008-2011 Freescale Semiconductor, Inc.
 *
```

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*/
```

```
./arch/powerpc/cpu/mpc8xxx/ddr/ddr1_dimm_params.c
ddr1_dimm_params.c
```

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```

```
./arch/powerpc/cpu/mpc8xxx/ddr/ddr2_dimm_params.c
ddr2_dimm_params.c
```

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*/
```

```
/*
* Calculate the Density of each Physical Rank.
* Returned size is in bytes.
*
* Study these table from Byte 31 of JEDEC SPD Spec.
*
```

	DDR I	DDR II
Bit	Size	Size
---	-----	-----
7 high	512MB	512MB
6	256MB	256MB
5	128MB	128MB
4	64MB	16GB
3	32MB	8GB
2	16MB	4GB
1	2GB	2GB
0 low	1GB	1GB

```
* Reorder Table to be linear by stripping the bottom
* 2 or 5 bits off and shifting them up to the top.
*
*/
```

```
./arch/powerpc/cpu/mpc8xxx/ddr/ddr3_dimm_params.c
ddr3_dimm_params.c
```

```
/*
```

```
* Copyright 2008-2012 Freescale Semiconductor, Inc.
*   Dave Liu <daveliu@freescale.com>
*
* calculate the organization and timing parameter
* from ddr3 spd, please refer to the spec
* JEDEC standard No.21-C 4_01_02_11R18.pdf
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*/
```

```
./arch/powerpc/cpu/mpc8xxx/ddr/interactive.c
interactive.c
```

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*/
/*
* Generic driver for Freescale DDR/DDR2/DDR3 memory controller.
* Based on code from spd_sdram.c
* Author: James Yang [at freescale.com]
*   York Sun [at freescale.com]
*/
```

```
./arch/powerpc/cpu/mpc8xxx/ddr/lc_common_dimm_params.c
lc_common_dimm_params.c
```

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```
./arch/powerpc/cpu/mpc8xxx/ddr/main.c
main.c
```

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*/
/*
* Generic driver for Freescale DDR/DDR2/DDR3 memory controller.
* Based on code from spd_sdram.c
```

```
* Author: James Yang [at freescale.com]
*/
```

```
./arch/powerpc/cpu/mpc8xxx/ddr/options.c
options.c
```

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```

```
./arch/powerpc/cpu/mpc8xxx/ddr/util.c
util.c
```

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```

```
./arch/powerpc/cpu/mpc8xxx/fdt.c
fdt.c
```

```
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 *
 * This file is derived from arch/powerpc/cpu/mpc85xx/cpu.c and
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```

```
./arch/powerpc/cpu/mpc8xxx/fsl_ifc.c
fsl_ifc.c
```

```
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```

```
./arch/powerpc/cpu/mpc8xxx/fsl_lbc.c
fsl_lbc.c
```

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 */
/* Boards should provide their own version of this if they use lbc sdram */
```

```
./arch/powerpc/cpu/mpc8xxx/law.c
law.c
```

```
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```

```
./arch/powerpc/cpu/mpc8xxx/srio.c
```

```
srio.c
```

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*/
```

```
./arch/powerpc/cpu/ppc4xx/40x_spd_sdram.c
```

```
40x_spd_sdram.c
```

```
/*  
* arch/powerpc/cpu/ppc4xx/40x_spd_sdram.c  
* This SPD SDRAM detection code supports IBM/AMCC PPC44x cpu with a  
* SDRAM controller. Those are all current 405 PPC's.  
*  
* (C) Copyright 2001  
* Bill Hunter, Wave 7 Optics, williamhunter@attbi.com  
*  
* Based on code by:  
*  
* Kenneth Johansson ,Ericsson AB.  
* kenneth.johansson@etx.ericsson.se  
*  
* hacked up by bill hunter. fixed so we could run before  
* serial_init and console_init. previous version avoided this by  
* running out of cache memory during serial/console init, then running
```

```
* this code later.
*
* (C) Copyright 2002
* Jun Gu, Artesyn Technology, jung@artesyndcp.com
* Support for AMCC 440 based on OpenBIOS draminit.c from IBM.
*
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* Stefan Roesse, DENX Software Engineering, sr@denx.de.
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*/
```

```
./arch/powerpc/cpu/ppc4xx/44x_spd_ddr.c
44x_spd_ddr.c
```

```
/*
* arch/powerpc/cpu/ppc4xx/44x_spd_ddr.c
* This SPD DDR detection code supports IBM/AMCC PPC44x cpu with a
* DDR controller. Those are 440GP/GX/EP/GR.
*
* (C) Copyright 2001
* Bill Hunter, Wave 7 Optics, williamhunter@attbi.com
*
* Based on code by:
*
* Kenneth Johansson ,Ericsson AB.
* kenneth.johansson@etx.ericsson.se
*
* hacked up by bill hunter. fixed so we could run before
* serial_init and console_init. previous version avoided this by
* running out of cache memory during serial/console init, then running
* this code later.
*
* (C) Copyright 2002
* Jun Gu, Artesyn Technology, jung@artesyndcp.com
```

```
* Support for AMCC 440 based on OpenBIOS draminit.c from IBM.
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* MA 02111-1307 USA
*/
```

```
./arch/powerpc/cpu/ppc4xx/44x_spd_ddr2.c
```

```
44x_spd_ddr2.c
```

```
/*
 * arch/powerpc/cpu/ppc4xx/44x_spd_ddr2.c
 * This SPD SDRAM detection code supports AMCC PPC44x cpu's with a
 * DDR2 controller (non Denali Core). Those currently are:
 *
 * 405:          405EX(r)
 * 440/460:     440SP/440SPe/460EX/460GT
 *
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*/
```

```
./arch/powerpc/cpu/ppc4xx/4xx_ibm_ddr2_autocalib.c
```

```
4xx_ibm_ddr2_autocalib.c
```

```
/*
* arch/powerpc/cpu/ppc4xx/4xx_ibm_ddr2_autocalib.c
* This SPD SDRAM detection code supports AMCC PPC44x cpu's with a
* DDR2 controller (non Denali Core). Those currently are:
*
* 405:          405EX
* 440/460:      440SP/440SPe/460EX/460GT/460SX
*
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*/
```

```
./arch/powerpc/cpu/ppc4xx/4xx_pci.c
```

4xx_pci.c

```
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 *-----*/
/*-----+
```

```
*
 * File Name:   405gp_pci.c
 *
 * Function:    Initialization code for the 405GP PCI Configuration regs.
 *
 * Author:     Mark Game
 *
 * Change Activity-
 *
 * Date        Description of Change                                BY
 * -----
 * 09-Sep-98   Created                                              MCG
 * 02-Nov-98   Removed External arbiter selected message           JWB
 * 27-Nov-98   Zero out PTMBAR2 and disable in PTM2MS              JWB
 * 04-Jan-99   Zero out other unused PMM and PTM regs. Change bus scan
 *             from (0 to n) to (1 to n).                          MCG
 * 17-May-99   Port to Walnut                                       JWB
 * 17-Jun-99   Updated for VGA support                              JWB
 * 21-Jun-99   Updated to allow SRAM region to be a target from PCI bus
 *             JWB
 * 19-Jul-99   Updated for 405GP pass 1 errata #26 (Low PCI subsequent
 *             target latency timer values are not supported).     MCG
 *             Should be fixed in pass 2.
 * 09-Sep-99   Removed use of PTM2 since the SRAM region no longer needs
 *             to be a PCI target. Zero out PTMBAR2 and disable in PTM2MS.
 *             JWB
 * 10-Dec-99   Updated PCI_Write_CFG_Reg for pass2 errata #6      JWB
 * 11-Jan-00   Ensure PMMxMAs disabled before setting PMMxLAs. This is not
```

```

*      really required after a reset since PMMxMAS are already
*      disabled but is a good practice nonetheless.
*      stefan.roese@esd-electronics.com
*      12-Jun-01
*      - PCI host/adapter handling reworked
*      stefan.roese@esd-electronics.com
*      09-Jul-01
*      - PCI host now configures from device 0 (not 1) to max_dev,
*      (host configures itself)
*      - On CPCI-405 pci base address and size is generated from
*      SDRAM and FLASH size (CFG regs not used anymore)
*      - Some minor changes for CPCI-405-A (adapter version)
*      stefan.roese@esd-electronics.com
*      14-Sep-01
*      - CONFIG_PCI_SCAN_SHOW added to print pci devices upon startup
*      stefan.roese@esd-electronics.com
*      28-Sep-01
*      - Changed pci master configuration for linux compatibility
*      (no need for bios_fixup() anymore)
*      stefan.roese@esd-electronics.com
*      26-Feb-02
*      - Bug fixed in pci configuration (Andrew May)
*      - Removed pci class code init for CPCI405 board
*      stefan.roese@esd-electronics.com
*      15-May-02
*      - New vga device handling
*      stefan.roese@esd-electronics.com
*      29-May-02
*      - PCI class code init added (if defined)
*-----*/

```

```

./arch/powerpc/cpu/ppc4xx/4xx_pcie.c
4xx_pcie.c
/*

```

```

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```

```

./arch/powerpc/cpu/ppc4xx/4xx_uart.c
4xx_uart.c

```

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 */
```

```
./arch/powerpc/cpu/ppc4xx/bedbug_405.c
bedbug_405.c
```

```
/*
 * Bedbug Functions specific to the PPC405 chip
 */
```

```
./arch/powerpc/cpu/ppc4xx/cmd_chip_config.c
cmd_chip_config.c
```

```
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```

```
./arch/powerpc/cpu/ppc4xx/cmd_ecctest.c
```

```
cmd_ecctest.c
```

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```

```
./arch/powerpc/cpu/ppc4xx/cpu.c
```

```
cpu.c
```

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```

```
./arch/powerpc/cpu/ppc4xx/cpu_init.c
cpu_init.c
```

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```

```
./arch/powerpc/cpu/ppc4xx/denali_data_eye.c
denali_data_eye.c
```

```
/*
* arch/powerpc/cpu/ppc4xx/denali_data_eye.c
* Extracted from board/amcc/sequoia/sdram.c by Larry Johnson <lrj@acm.org>.
*
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./arch/powerpc/cpu/ppc4xx/denali_spd_ddr2.c
denali_spd_ddr2.c
/*
* arch/powerpc/cpu/ppc4xx/denali_spd_ddr2.c
* This SPD SDRAM detection code supports AMCC PPC44x CPUs with a Denali-core
* DDR2 controller, specifically the 440EPx/GRx.
*
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*
* Based primarily on arch/powerpc/cpu/ppc4xx/4xx_spd_ddr2.c, which is...
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```
./arch/powerpc/cpu/ppc4xx/ecc.c
ecc.c
```

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 *
 * Description:
 * This file implements generic DRAM ECC initialization for
 * PowerPC processors using a SDRAM DDR/DDR2 controller,
 * including the 405EX(r), 440GP/GX/EP/GR, 440SP(E), and
 * 460EX/GT.
 */
```

```
./arch/powerpc/cpu/ppc4xx/ecc.h
ecc.h
```

```
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*      Description:
*      This file implements ECC initialization for PowerPC processors
*      using the IBM SDRAM DDR1 & DDR2 controller.
*
*/
```

```
./arch/powerpc/cpu/ppc4xx/fdt.c
```

```
fdt.c
```

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```
./arch/powerpc/cpu/ppc4xx/gpio.c  
gpio.c
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```

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```

```
./arch/powerpc/cpu/ppc4xx/miiphy.c
miiphy.c
```

```
/*-----+
```

```
./arch/powerpc/cpu/ppc4xx/reginfo.c
reginfo.c
/*
```

```
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```
./arch/powerpc/cpu/ppc4xx/sdram.c
sdram.c
```

```
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```
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```

```
sdram.h
```

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```

```
./arch/powerpc/cpu/ppc4xx/speed.c
```

```
speed.c
```

```
/*
```

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```
*/
```

```
./arch/powerpc/cpu/ppc4xx/tlb.c
```

```
tlb.c
```

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```
*
```

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```

```
*/
```

./arch/powerpc/cpu/ppc4xx/traps.c

traps.c

/*

* linux/arch/powerpc/kernel/traps.c

*

* Copyright (C) 1995-1996 Gary Thomas (gdt@linuxppc.org)

*

* Modified by Cort Dougan (cort@cs.nmt.edu)

* and Paul Mackerras (paulus@cs.anu.edu.au)

*

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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.

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*/

./arch/powerpc/cpu/ppc4xx/uic.c

uic.c

/*

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*

* (C) Copyright 2002 (440 port)

* Scott McNutt, Artesyn Communication Products, smcnutt@artsynpc.com

*

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* Travis B. Sawyer, Sandburst Corporation, tsawyer@sandburst.com

*

* (C) Copyright 2008 (PPC440X05 port for Virtex 5 FX)

* Ricardo Ribalda-Universidad Autonoma de Madrid-ricardo.ribalda@uam.es

* Work supported by Qtechnology (<http://qtec.com>)

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```

```
./arch/powerpc/cpu/ppc4xx/usb.c
usb.c
```

```
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```

```
./arch/powerpc/cpu/ppc4xx/usb_ohci.c
usb_ohci.c
```

```
/*
* URB OHCI HCD (Host Controller Driver) for USB on the PPC440EP.
*
* (C) Copyright 2003-2004
* Gary Jennejohn, DENX Software Engineering <garyj@denx.de>
*/
```

```

* (C) Copyright 2004
* Pierre Aubert, Staubli Faverges <p.aubert@staubli.com>
*
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* (C) Copyright 1999 Roman Weissgaerber <weissg@vienna.at>
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*
*/
/*
* IMPORTANT NOTES
* 1 - this driver is intended for use with USB Mass Storage Devices
*     (BBB) ONLY. There is NO support for Interrupt or Isochronous pipes!
*/

./arch/powerpc/cpu/ppc4xx/usb_ohci.h
usb_ohci.h
/*
* URB OHCI HCD (Host Controller Driver) for USB.
*
* (C) Copyright 1999 Roman Weissgaerber <weissg@vienna.at>
* (C) Copyright 2000-2001 David Brownell <dbrownell@users.sourceforge.net>
*
* usb-ohci.h
*/
/* mapping of the OHCI CC status to error codes */

./arch/powerpc/cpu/ppc4xx/xilinx_irq.c
xilinx_irq.c
/*
* (C) Copyright 2008
* Ricardo Ribalda-Universidad Autonoma de Madrid-ricardo.ribalda@uam.es
* This work has been supported by: QTechnology http://qtec.com/

```

```
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*/
```

```
./arch/powerpc/include/asm/4xx_pci.h
4xx_pci.h
```

```
/*-----+
```

```
./arch/powerpc/include/asm/4xx_pcie.h
4xx_pcie.h
```

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```

```
./arch/powerpc/include/asm/5xx_immap.h
5xx_immap.h
```

```
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* Martin Winistoerfer, martinwinistoerfer@gmx.ch.
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```
./arch/powerpc/include/asm/8xx_immap.h
```

```
8xx_immap.h
```

```
/*
 * MPC8xx Internal Memory Map
 * Copyright (c) 1997 Dan Malek (dmalek@jlc.net)
 *
 * The I/O on the MPC860 is comprised of blocks of special registers
 * and the dual port ram for the Communication Processor Module.
 * Within this space are functional units such as the SIU, memory
 * controller, system timers, and other control functions. It is
 * a combination that I found difficult to separate into logical
 * functional files.....but anyone else is welcome to try. -- Dan
 */
```

```
./arch/powerpc/include/asm/apm821xx.h
```

```
apm821xx.h
```

```
/*
 * Copyright (c) 2010, Applied Micro Circuits Corporation
 * Author: Tirumala R Marri <tmarri@apm.com>
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 */
```

```
./arch/powerpc/include/asm/arch-mpc83xx/gpio.h
```

```
gpio.h
```

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*/
```

```
./arch/powerpc/include/asm/atomic.h
atomic.h
```

```
/*
 * PowerPC atomic operations
 */
```

```
./arch/powerpc/include/asm/bitops.h
bitops.h
```

```
/*
 * bitops.h: Bit string operations on the ppc
 */
```

```
./arch/powerpc/include/asm/byteorder.h
byteorder.h
```

```
No license information found.
```

```
./arch/powerpc/include/asm/cache.h
cache.h
```

```
/*
 * include/asm-ppc/cache.h
 */
/* bytes per L1 cache line */
```

```
./arch/powerpc/include/asm/config.h
config.h
```

```
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*  
*/
```

```
./arch/powerpc/include/asm/config_mpc85xx.h  
config_mpc85xx.h
```

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*/
```

```
./arch/powerpc/include/asm/config_mpc86xx.h  
config_mpc86xx.h
```

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* MA 02111-1307 USA  
*  
*/
```

```
./arch/powerpc/include/asm/cpm_8260.h
```

cpm_8260.h

```
/*
 * MPC8260 Communication Processor Module.
 * Copyright (c) 1999 Dan Malek (dmalek@jlc.net)
 *
 * This file contains structures and information for the communication
 * processor channels found in the dual port RAM or parameter RAM.
 * All CPM control and status is available through the MPC8260 internal
 * memory map. See immap.h for details.
 */
```

./arch/powerpc/include/asm/cpm_85xx.h

cpm_85xx.h

```
/*
 * MPC85xx Communication Processor Module
 * Copyright (c) 2003, Motorola Inc.
 * Xianghua Xiao (X.Xiao@motorola.com)
 *
 * MPC8260 Communication Processor Module.
 * Copyright (c) 1999 Dan Malek (dmalek@jlc.net)
 *
 * This file contains structures and information for the communication
 * processor channels found in the dual port RAM or parameter RAM.
 * All CPM control and status is available through the MPC8260 internal
 * memory map. See immap.h for details.
 */
```

./arch/powerpc/include/asm/e300.h

e300.h

```
/*
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 * Liberty Eran (liberty@freescale.com)
 */
```

./arch/powerpc/include/asm/errno.h

errno.h

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./arch/powerpc/include/asm/fsl_ddr_dimm_params.h

fsl_ddr_dimm_params.h

```
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```

./arch/powerpc/include/asm/fsl_ddr_sdram.h

fsl_ddr_sdram.h

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 */
/*
 * Pick a basic DDR Technology.
 */

./arch/powerpc/include/asm/fsl_dma.h
fsl_dma.h
/*
 * Freescale DMA Controller
 *
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 */
```

```
./arch/powerpc/include/asm/fsl_dtsec.h
fsl_dtsec.h
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*/
```

```
./arch/powerpc/include/asm/fsl_enet.h
```

```
fsl_enet.h
```

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*/
```

```
./arch/powerpc/include/asm/fsl_fman.h
```

```
fsl_fman.h
```

```
/*
 * MPC85xx Internal Memory Map
 *
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*/
```

```
./arch/powerpc/include/asm/fsl_i2c.h
```

```
fsl_i2c.h
```

```
/*
 * Freescale I2C Controller
 *
 * Copyright 2006 Freescale Semiconductor, Inc.
 *
 * Based on earlier versions by Gleb Natapov <gnatapov@mrvt.com>,
 * Xianghua Xiao <x.xiao@motorola.com>, Eran Liberty (liberty@freescale.com),
 * and Jeff Brown.
 * Some bits are taken from linux driver written by adrian@humboldt.co.uk.
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 */
```

```
./arch/powerpc/include/asm/fsl_ifc.h
fsl_ifc.h
```

```
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```

```
./arch/powerpc/include/asm/fsl_law.h
```

```
fsl_law.h
```

```
/*
```

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```

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```

```
*/
```

```
./arch/powerpc/include/asm/fsl_lbc.h
```

```
fsl_lbc.h
```

```
/*
```

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```

```
*/
```

```
./arch/powerpc/include/asm/fsl_liodn.h
```

```
fsl_liodn.h
```

```
/*
```

```
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```
*/
```

```
./arch/powerpc/include/asm/fsl_mamac.h
```

fsl_memac.h

```
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 * Roy Zang <tie-fei.zang@freescale.com>  
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```

./arch/powerpc/include/asm/fsl_mpc83xx_serdes.h

fsl_mpc83xx_serdes.h

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 */
```

./arch/powerpc/include/asm/fsl_pci.h

fsl_pci.h

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*/
```

```
./arch/powerpc/include/asm/fsl_portals.h
```

```
fsl_portals.h
```

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 */
```

```
./arch/powerpc/include/asm/fsl_secure_boot.h
```

```
fsl_secure_boot.h
```

```
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*/
```

```
./arch/powerpc/include/asm/fsl_serdes.h
fsl_serdes.h
```

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```

```
./arch/powerpc/include/asm/fsl_srio.h
fsl_srio.h
```

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*/
```

```
./arch/powerpc/include/asm/fsl_tgec.h
```

```
fsl_tgec.h
```

```
/*
```

```
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```

```
* Dave Liu <daveliu@freescale.com>
```

```
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```

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```

```
*/
```

```
./arch/powerpc/include/asm/global_data.h
```

```
global_data.h
```

```
/*
```

```
* (C) Copyright 2002-2010
```

```
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
```

```
/*
```

```
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* project.
```

```
/*
```

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* MA 02111-1307 USA  
*/
```

```
./arch/powerpc/include/asm/gpio.h  
gpio.h  
No license information found.
```

```
./arch/powerpc/include/asm/immap_512x.h  
immap_512x.h
```

```
/*  
 * (C) Copyright 2007-2009 DENX Software Engineering  
 *  
 * MPC512x Internal Memory Map  
 *  
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
 * MA 02111-1307 USA  
 *  
 * Based on the MPC83xx header.  
*/
```

```
./arch/powerpc/include/asm/immap_8220.h  
immap_8220.h
```

```
/*  
 * MPC8220 Internal Memory Map  
 * Copyright (c) 2004 TsiChung Liew (Tsi-Chung.Liew@freescale.com)  
 *  
 * The Internal Memory Map of the 8220.  
 *  
 */  
/*  
 * System configuration registers.  
*/
```

```
./arch/powerpc/include/asm/immap_8260.h  
immap_8260.h
```

```
/*  
 * MPC8260 Internal Memory Map
```

```

* Copyright (c) 1999 Dan Malek (dmalek@jlc.net)
*
* The Internal Memory Map of the 8260. I don't know how generic
* this will be, as I don't have any knowledge of the subsequent
* parts at this time. I copied this from the 8xx_immap.h.
*/
/* System configuration registers.
*/

./arch/powerpc/include/asm/immap_83xx.h
immap_83xx.h
/*
* Copyright 2004-2011 Freescale Semiconductor, Inc.
*
* MPC83xx Internal Memory Map
*
* Contributors:
*   Dave Liu <daveliu@freescale.com>
*   Tanya Jiang <tanya.jiang@freescale.com>
*   Mandy Lavi <mandy.lavi@freescale.com>
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* MA 02111-1307 USA
*/

./arch/powerpc/include/asm/immap_85xx.h
immap_85xx.h
/*
* MPC85xx Internal Memory Map
*
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*
* Copyright(c) 2002,2003 Motorola Inc.
* Xianghua Xiao (x.xiao@motorola.com)
*
* See file CREDITS for list of people who contributed to this

```

```
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* MA 02111-1307 USA  
*/
```

```
./arch/powerpc/include/asm/immap_86xx.h
```

```
immap_86xx.h
```

```
/*  
* MPC86xx Internal Memory Map  
*  
* Copyright 2004, 2011 Freescale Semiconductor  
* Jeff Brown (Jeffrey@freescale.com)  
* Srikanth Srinivasan (srikanth.srinivasan@freescale.com)  
*  
*/
```

```
./arch/powerpc/include/asm/immap_qe.h
```

```
immap_qe.h
```

```
/*  
* QUICC Engine (QE) Internal Memory Map.  
* The Internal Memory Map for devices with QE on them. This  
* is the superset of all QE devices (8360, etc.).  
*  
* Copyright (c) 2006-2009, 2011 Freescale Semiconductor, Inc.  
* Author: Shlomi Gridih <gridish@freescale.com>  
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* option) any later version.  
*/
```

```
./arch/powerpc/include/asm/interrupt.h
```

```
interrupt.h
```

```
/*  
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```

```
* This work has been supported by: QTechnology http://qttec.com/
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*/
```

```
./arch/powerpc/include/asm/io.h
io.h
```

```
/* originally from linux source.
* removed the dependencies on CONFIG_ values
* removed virt_to_phys stuff (and in fact everything surrounded by #if __KERNEL__)
* Modified By Rob Taylor, Flying Pig Systems, 2000
*/
```

```
./arch/powerpc/include/asm/iopin_8260.h
iopin_8260.h
```

```
/*
* MPC8260 I/O port pin manipulation functions
*/
```

```
./arch/powerpc/include/asm/iopin_85xx.h
iopin_85xx.h
```

```
/*
* MPC85xx I/O port pin manipulation functions
*/
```

```
./arch/powerpc/include/asm/iopin_8xx.h
iopin_8xx.h
```

```
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*/
```

```
./arch/powerpc/include/asm/m8260_pci.h
m8260_pci.h
/* Bit definitions for PCIBR registers */
```

```
./arch/powerpc/include/asm/mc146818rtc.h
mc146818rtc.h
/*
* Machine dependent access functions for RTC registers.
*/
```

```
./arch/powerpc/include/asm/mmu.h
mmu.h
/*
* PowerPC memory management structures
*/
/* Hardware Page Table Entry */
```

```
./arch/powerpc/include/asm/mp.h
mp.h
/*
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*/
```

```
./arch/powerpc/include/asm/mpc512x.h
mpc512x.h
```

```
/*
 * include/asm-ppc/mpc512x.h
 *
 * Prototypes, etc. for the Freescale MPC512x embedded cpu chips
 *
 * 2009 (C) Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
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 * MA 02111-1307 USA
 */
```

```
./arch/powerpc/include/asm/mpc8349_pci.h
mpc8349_pci.h
/* Bit definitions for PCIBR registers */
```

```
./arch/powerpc/include/asm/mpc85xx_gpio.h
mpc85xx_gpio.h
/*
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 */
```

```
./arch/powerpc/include/asm/mpc8xxx_spi.h
mpc8xxx_spi.h
```

```
/*
 * Freescale non-CPM SPI Controller
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./arch/powerpc/include/asm/pci_io.h
pci_io.h
```

```
/* originally from linux source (asm-ppc/io.h).
 * Sanity added by Rob Taylor, Flying Pig Systems, 2000
 */
```

```
./arch/powerpc/include/asm/pnp.h
pnp.h
```

```
/* 11/02/95 */
/*-----*/
/* Plug and Play header definitions */
/*-----*/
/* Structure map for PnP on PowerPC Reference Platform */
/* See Plug and Play ISA Specification, Version 1.0, May 28, 1993. It */
/* (or later versions) is available on CompuServe in the PLUGPLAY area. */
/* This code has extensions to that specification, namely new short and */
/* long tag types for platform dependent information */
/* Warning: LE notation used throughout this file */
```

```
./arch/powerpc/include/asm/posix_types.h
posix_types.h
```

```
/*
 * This file is generally used by user-level software, so you need to
```

```
* be a little careful about namespace pollution etc. Also, we cannot
* assume GCC is being used.
*/
```

```
./arch/powerpc/include/asm/ppc405.h
ppc405.h
```

```
/*-----+
```

```
./arch/powerpc/include/asm/ppc405cr.h
ppc405cr.h
```

```
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```

```
./arch/powerpc/include/asm/ppc405ep.h
ppc405ep.h
```

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```

*/

./arch/powerpc/include/asm/ppc405ex.h
ppc405ex.h

/*

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*/

./arch/powerpc/include/asm/ppc405ez.h
ppc405ez.h

/*

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*/

./arch/powerpc/include/asm/ppc405gp.h
ppc405gp.h

/*

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*/

./arch/powerpc/include/asm/ppc440.h
ppc440.h
/*-----+

./arch/powerpc/include/asm/ppc440epx_grx.h
ppc440epx_grx.h
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*/

./arch/powerpc/include/asm/ppc440ep_gr.h
ppc440ep_gr.h
/*
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*

```

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*/
```

```
./arch/powerpc/include/asm/ppc440gp.h
ppc440gp.h
```

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*/
```

```
./arch/powerpc/include/asm/ppc440gx.h
ppc440gx.h
```

```
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*/
```

```
./arch/powerpc/include/asm/ppc440sp.h
```

```
ppc440sp.h
```

```
/*
```

```
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*/
```

```
./arch/powerpc/include/asm/ppc440spe.h
```

```
ppc440spe.h
```

```
/*
```

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```

```
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*/
```

```
./arch/powerpc/include/asm/ppc460ex_gt.h  
ppc460ex_gt.h
```

```
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*/
```

```
./arch/powerpc/include/asm/ppc460sx.h  
ppc460sx.h
```

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*/
```

```
./arch/powerpc/include/asm/ppc4xx-ebc.h  
ppc4xx-ebc.h
```

```
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 */

./arch/powerpc/include/asm/ppc4xx-emas.h
ppc4xx-emas.h
/*-----+

./arch/powerpc/include/asm/ppc4xx-gpio.h
ppc4xx-gpio.h
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```

```
*/  
  
./arch/powerpc/include/asm/ppc4xx-i2c.h  
ppc4xx-i2c.h  
/*  
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*/
```

```
./arch/powerpc/include/asm/ppc4xx-isram.h  
ppc4xx-isram.h  
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*/
```

```
./arch/powerpc/include/asm/ppc4xx-mal.h
```

```
ppc4xx-mal.h
/* include/mal.h, openbios_walnut, walnut_bios 8/6/99 08:48:40 */
/*-----+
```

```
./arch/powerpc/include/asm/ppc4xx-sdram.h
ppc4xx-sdram.h
```

```
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 */
```

```
./arch/powerpc/include/asm/ppc4xx-uic.h
ppc4xx-uic.h
```

```
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*/
```

```
./arch/powerpc/include/asm/ppc4xx.h
```

```
ppc4xx.h
```

```
/*-----+
```

```
./arch/powerpc/include/asm/ppc4xx_config.h
```

```
ppc4xx_config.h
```

```
/*
```

```
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*
```

```
*/
```

```
./arch/powerpc/include/asm/processor.h
```

```
processor.h
```

```
/*
```

```
* Default implementation of macro that returns current
* instruction pointer ("program counter").
*
```

```
*/
```

```
./arch/powerpc/include/asm/ptrace.h
```

```
ptrace.h
```

```
/*
```

```
* This struct defines the way the registers are stored on the
```

```
* kernel stack during a system call or other kernel entry.
*
* this should only contain volatile regs
* since we can keep non-volatile in the thread_struct
* should set this up when only volatiles are saved
* by intr code.
*
* Since this is going on the stack, *CARE MUST BE TAKEN* to insure
* that the overall structure is a multiple of 16 bytes in length.
*
* Note that the offsets of the fields in this struct correspond with
* the PT_* values below. This simplifies arch/powerpc/kernel/ptrace.c.
*/
```

```
./arch/powerpc/include/asm/residual.h
```

```
residual.h
```

```
/* 7/18/95 */
/*-----*/
/*      Residual Data header definitions and prototypes      */
/*-----*/
/* Structure map for RESIDUAL on PowerPC Reference Platform */
/* residual.h - Residual data structure passed in r3.        */
/*      Load point passed in r4 to boot image.              */
/* For enum's: if given in hex then they are bit significant, */
/*      i.e. only one bit is on for each enum                */
/* Reserved fields must be filled with zeros.                */
```

```
./arch/powerpc/include/asm/sigcontext.h
```

```
sigcontext.h
```

```
No license information found.
```

```
./arch/powerpc/include/asm/signal.h
```

```
signal.h
```

```
/* Avoid too many header ordering problems. */
/* Most things should be clean enough to redefine this at will, if care
```

```
./arch/powerpc/include/asm/spl.h
```

```
spl.h
```

```
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*/
```

```
./arch/powerpc/include/asm/status_led.h
```

```
status_led.h
```

```
/*  
 * asm/status_led.h  
 *  
 * MPC8xx/MPC8260/MPC5xx based status led support functions  
 */  
/* if not overridden */
```

```
./arch/powerpc/include/asm/string.h
```

```
string.h
```

```
No license information found.
```

```
./arch/powerpc/include/asm/types.h
```

```
types.h
```

```
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```

```
./arch/powerpc/include/asm/u-boot.h
```

```
u-boot.h
```

```
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 *  
 *****
```

```
* NOTE: This header file defines an interface to U-Boot. Including
* this (unmodified) header file in another file is considered normal
* use of U-Boot, and does *not* fall under the heading of "derived
* work".
```

```
*****
*/
```

```
./arch/powerpc/include/asm/unaligned.h
unaligned.h
```

```
/*
* The PowerPC can do unaligned accesses itself in big endian mode.
*/
```

```
./arch/powerpc/include/asm/xilinx_irq.h
xilinx_irq.h
```

```
/*
* (C) Copyright 2008
* Ricardo Ribalda-Universidad Autonoma de Madrid-ricardo.ribalda@uam.es
* This work has been supported by: QTechnology http://qtec.com/
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*/
```

```
./arch/powerpc/lib/bat_rw.c
bat_rw.c
```

```
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* MA 02111-1307 USA
*
*/
```

```
./arch/powerpc/lib/board.c
```

```
board.c
```

```
/*
```

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*/
```

```
./arch/powerpc/lib/bootm.c
```

```
bootm.c
```

```
/*
```

```
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*/
```

```
./arch/powerpc/lib/cache.c
cache.c
```

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*/
```

```
./arch/powerpc/lib/extable.c
extable.c
```

```
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```

```
./arch/powerpc/lib/ide.c
```

```
ide.c
```

```
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*/
```

```
./arch/powerpc/lib/ide.h
```

```
ide.h
```

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```

```
./arch/powerpc/lib/interrupts.c
interrupts.c
```

```
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```

```
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kgdb.c
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```

```
./arch/powerpc/lib/memcpy_mpc5200.c
memcpy_mpc5200.c
/*
```

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```
./arch/powerpc/lib/spl.c
```

```
spl.c
```

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```
./arch/powerpc/lib/time.c
```

```
time.c
```

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*/
```

```
./arch/sandbox/cpu/cpu.c
```

```
cpu.c
```

```
/*
```

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```

```
./arch/sandbox/cpu/os.c
```

```
os.c
```

```
/*
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/sandbox/cpu/start.c
start.c
```

```
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*/
```

```
./arch/sandbox/cpu/state.c
state.c
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/sandbox/include/asm/bitops.h
```

```
bitops.h
```

```
/*
 * Copyright (c) 2011 The Chromium OS Authors.
 *
 * Copyright 1995, Russell King.
 * Various bits and pieces copyrights include:
 * Linus Torvalds (test_bit).
 *
 * bit 0 is the LSB of addr; bit 32 is the LSB of (addr+1).
 *
 * Please note that the code in this file should never be included
 * from user space. Many of these are not implemented in assembler
 * since they would be too costly. Also, they require privileged
 * instructions (which are not available from user mode) to ensure
 * that they are atomic.
 */
```

```
./arch/sandbox/include/asm/byteorder.h
```

```
byteorder.h
```

```
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/sandbox/include/asm/cache.h

cache.h

/*

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* MA 02111-1307 USA

*/

./arch/sandbox/include/asm/config.h

config.h

/*

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* MA 02111-1307 USA

*

*/

./arch/sandbox/include/asm/errno.h

errno.h

No license information found.

./arch/sandbox/include/asm/getopt.h

getopt.h

```
/*  
 * Code for setting up command line flags like `./u-boot --help`  
 *  
 * Copyright (c) 2011 The Chromium OS Authors.  
 *  
 * Licensed under the GPL-2 or later.  
 */
```

./arch/sandbox/include/asm/global_data.h

global_data.h

```
/*  
 * Copyright (c) 2011 The Chromium OS Authors.  
 *  
 * (C) Copyright 2002-2010  
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
 *  
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 * project.  
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 * along with this program; if not, write to the Free Software  
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
 * MA 02111-1307 USA  
 */
```

./arch/sandbox/include/asm/gpio.h

gpio.h

```
/*  
 * This is the interface to the sandbox GPIO driver for test code which  
 * wants to change the GPIO values reported to U-Boot.  
 *  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/sandbox/include/asm/io.h
```

```
io.h
```

```
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/sandbox/include/asm/posix_types.h
```

```
posix_types.h
```

```
/*
* linux/include/asm-arm/posix_types.h
*
* Copyright (C) 1996-1998 Russell King.
*
* This program is free software; you can redistribute it and/or modify
* it under the terms of the GNU General Public License version 2 as
* published by the Free Software Foundation.
*
* Changelog:
* 27-06-1996 RMK Created
*/
```

```
./arch/sandbox/include/asm/ptrace.h
```

```
ptrace.h
```

```
/*
```

```
* Copyright (c) 2011 The Chromium OS Authors.
```

```
*
```

```
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```

```
*
```

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```

```
*
```

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* MA 02111-1307 USA
```

```
*/
```

```
./arch/sandbox/include/asm/sections.h
```

```
sections.h
```

```
/*
```

```
* decls for symbols defined in the linker script
```

```
*
```

```
* Copyright (c) 2012 The Chromium OS Authors.
```

```
*
```

```
* Licensed under the GPL-2 or later.
```

```
*/
```

```
./arch/sandbox/include/asm/state.h
```

```
state.h
```

```
/*
```

```
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```

```
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* project.
```

```
*
```

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```

```
*
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/sandbox/include/asm/string.h
```

```
string.h
```

```
/*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/sandbox/include/asm/system.h
```

```
system.h
```

```
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*  
*/
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/sandbox/include/asm/types.h
```

```
types.h
```

```
/*
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./arch/sandbox/include/asm/u-boot-sandbox.h
```

```
u-boot-sandbox.h
```

```
/*
 * Copyright (c) 2011 The Chromium OS Authors.
 *
 * (C) Copyright 2002
 * Sysgo Real-Time Solutions, GmbH <www.elinos.com>
 * Marius Groeger <mgroeger@sysgo.de>
 *
 * (C) Copyright 2002
 * Sysgo Real-Time Solutions, GmbH <www.elinos.com>
 * Alex Zuepke <azu@sysgo.de>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/sandbox/include/asm/u-boot.h
u-boot.h
```

```
/*
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* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*
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* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Alex Zuepke <azu@sysgo.de>
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*****
* NOTE: This header file defines an interface to U-Boot. Including
* this (unmodified) header file in another file is considered normal
* use of U-Boot, and does *not* fall under the heading of "derived
* work".
*****
*/
```

```
./arch/sandbox/include/asm/unaligned.h
```

unaligned.h

```
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/sandbox/lib/board.c

board.c

```
/*
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 *
 * (C) Copyright 2002-2006
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
 * (C) Copyright 2002
 * Sysgo Real-Time Solutions, GmbH <www.elinos.com>
 * Marius Groeger <mgroeger@sysgo.de>
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```

```
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/sandbox/lib/interrupts.c  
interrupts.c
```

```
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* MA 02111-1307 USA  
*/
```

```
./arch/sh/cpu/sh2/cache.c  
cache.c
```

```
/*  
* (C) Copyright 2007  
* Yoshihiro Shimoda <shimoda.yoshihiro@renesas.com>  
*  
* Copyright (C) 2007, 2008 Nobobuhiro Iwamatsu <iwamatsu@nigauri.org>  
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* MA 02111-1307 USA  
*/
```

```
./arch/sh/cpu/sh2/cpu.c
```

```
cpu.c
```

```
/*  
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*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/sh/cpu/sh2/interrupts.c
```

```
interrupts.c
```

```
/*  
* Copyright 2007,2008 Nobuhiro Iwamatsu <iwamatsu@nigauri.org>  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/sh/cpu/sh2/watchdog.c
```

```
watchdog.c
```

```
/*  
* Copyright (C) 2008,2010 Nobuhiro Iwamatsu <nobuhiro.iwamatsu.yj@renesas.com>  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/sh/cpu/sh3/cache.c
```

```
cache.c
```

```
/*  
* (C) Copyright 2007  
* Yoshihiro Shimoda <shimoda.yoshihiro@renesas.com>  
*  
* (C) Copyright 2007  
* Nobobuhiro Iwamatsu <iwamatsu@nigauri.org>  
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* MA 02111-1307 USA  
*/
```

```
./arch/sh/cpu/sh3/cpu.c
```

```
cpu.c
```

```
/*  
* (C) Copyright 2007  
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*  
* (C) Copyright 2007  
* Nobuhiro Iwamatsu <iwamatsu@nigauri.org>  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/sh/cpu/sh3/interrupts.c
```

```
interrupts.c
```

```
/*  
* (C) Copyright 2007  
* Yoshihiro Shimoda <shimoda.yoshihiro@renesas.com>  
*  
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* MA 02111-1307 USA
*/
```

```
./arch/sh/cpu/sh3/watchdog.c
```

```
watchdog.c
```

```
/*
* (C) Copyright 2010
* Nobuhiro Iwamatsu <nobuhiro.iwamatsu.yj@renesas.com>
*
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* Yoshihiro Shimoda <shimoda.yoshihiro@renesas.com>
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*/
```

```
./arch/sh/cpu/sh4/cache.c
```

```
cache.c
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/sh/cpu/sh4/cpu.c
```

```
cpu.c
```

```
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*/
```

```
./arch/sh/cpu/sh4/interrupts.c
```

```
interrupts.c
```

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*/
```

```
./arch/sh/cpu/sh4/watchdog.c
watchdog.c
```

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* MA 02111-1307 USA
*/
```

```
./arch/sh/include/asm/bitops.h
bitops.h
```

```
/* For __swab32 */
```

```
./arch/sh/include/asm/byteorder.h
byteorder.h
```

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*
*/
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*/
```

```
./arch/sh/include/asm/cache.h
cache.h
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```

```
./arch/sh/include/asm/config.h
config.h
```

```
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 *
 */
```

```
./arch/sh/include/asm/cpu_sh2.h
cpu_sh2.h
```

```
/*
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```

```
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/sh/include/asm/cpu_sh3.h
```

```
cpu_sh3.h
```

```
/*  
* (C) Copyright 2007-2009 Nobuhiro Iwamatsu <iwamatsu@nigauri.org>  
* (C) Copyright 2007 Yoshihiro Shimoda <shimoda.yoshihiro@renesas.com>  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/sh/include/asm/cpu_sh4.h
```

```
cpu_sh4.h
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/sh/include/asm/cpu_sh7203.h
```

```
cpu_sh7203.h
```

```
/* Cache */
```

```
/* PFC */

./arch/sh/include/asm/cpu_sh7264.h
cpu_sh7264.h
/* Cache */
/* PFC */

./arch/sh/include/asm/cpu_sh7269.h
cpu_sh7269.h
/* Cache */
/* SCIF */

./arch/sh/include/asm/cpu_sh7706.h
cpu_sh7706.h
/* MMU and Cache control */
/* PFC */

./arch/sh/include/asm/cpu_sh7710.h
cpu_sh7710.h
/* MMU and Cache control */
/* PFC */

./arch/sh/include/asm/cpu_sh7720.h
cpu_sh7720.h
/*
 * Copyright 2007 (C)
 * Yoshihiro Shimoda <shimoda.yoshihiro@renesas.com>
 *
 * Copyright 2008 (C)
 * Mark Jonas <mark.jonas@de.bosch.com>
 *
 * SH7720 Internal I/O register
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */

./arch/sh/include/asm/cpu_sh7722.h
```

cpu_sh7722.h

```
/*
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 *
 * SH7722 Internal I/O register
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/sh/include/asm/cpu_sh7723.h

cpu_sh7723.h

```
/*
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 *
 * SH7723 Internal I/O register
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/sh/include/asm/cpu_sh7724.h

cpu_sh7724.h

```
/*
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 *
```

```
* SH7724 Internal I/O register
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*/
```

```
./arch/sh/include/asm/cpu_sh7734.h
cpu_sh7734.h
/*
```

```
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*
* SH7734 Internal I/O register
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* MA 02111-1307 USA
*/
```

```
./arch/sh/include/asm/cpu_sh7750.h
cpu_sh7750.h
/*
```

```
* (C) Copyright 2007 Nobuhiro Iwamatsu <iwamatsu@nigauri.org>
*
* SH7750/SH7750S/SH7750R/SH7751/SH7751R
* Internal I/O register
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*/
```

```
./arch/sh/include/asm/cpu_sh7752.h
cpu_sh7752.h
```

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*
*/
```

```
./arch/sh/include/asm/cpu_sh7757.h
cpu_sh7757.h
```

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*/
```

```
./arch/sh/include/asm/cpu_sh7763.h
cpu_sh7763.h
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/sh/include/asm/cpu_sh7780.h
cpu_sh7780.h
```

```
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* Copyright (c) 2008 Yusuke Goda <goda.yusuke@renesas.com>
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```

```
* MA 02111-1307 USA
*
*/
```

```
./arch/sh/include/asm/cpu_sh7785.h
cpu_sh7785.h
```

```
/*
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 * Copyright (c) 2008 Yusuke Goda <goda.yusuke@renesas.com>
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 *
*/
```

```
./arch/sh/include/asm/errno.h
errno.h
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```

```
./arch/sh/include/asm/global_data.h
global_data.h
```

```
/*
 * (C) Copyright 2002-2010
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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* MA 02111-1307 USA
*/
```

```
./arch/sh/include/asm/io.h
```

```
io.h
```

```
/*
 * linux/include/asm-sh/io.h
 *
 * Copyright (C) 1996-2000 Russell King
 *
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 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 *
 * Modifications:
 * 16-Sep-1996 RMK      Inlined the inx/outx functions & optimised for both
 *                    constant addresses and variable addresses.
 * 04-Dec-1997 RMK      Moved a lot of this stuff to the new architecture
 *                    specific IO header files.
 * 27-Mar-1999 PJB      Second parameter of memcpy_toio is const..
 * 04-Apr-1999 PJB      Added check_signature.
 * 12-Dec-1999 RMK      More cleanups
 * 18-Jun-2000 RMK      Removed virt_to_* and friends definitions
 */
```

```
./arch/sh/include/asm/irqflags.h
```

```
irqflags.h
```

```
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```

```
./arch/sh/include/asm/macro.h
```

```
macro.h
```

```
/*
 * Copyright (C) 2008 Yoshihiro Shimoda <shimoda.yoshihiro@renesas.com>
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*/
```

```
./arch/sh/include/asm/mmc.h
```

```
mmc.h
```

```
/*
* Renesas SuperH MMCIF driver.
*
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* Copyright (C) 2012 Renesas Solutions Corp.
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* the Free Software Foundation; either version 2 of the License.
*
*/
```

```
./arch/sh/include/asm/pci.h
```

```
pci.h
```

```
/*
* SH4 PCI Controller (PCIC) for U-Boot.
* (C) Dustin McIntire (dustin@sensoria.com)
* (C) 2007,2008 Nobuhiro Iwamatsu <iwamatsu@nigauri.org>
* (C) 2008 Yusuke Goda <goda.yusuke@renesas.com>
*
* u-boot/include/asm-sh/pci.h
*
* See file CREDITS for list of people who contributed to this
* project.
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* MA 02111-1307 USA
*/
```

```
./arch/sh/include/asm/posix_types.h
```

```
posix_types.h
```

```
/*  
 * This file is generally used by user-level software, so you need to  
 * be a little careful about namespace pollution etc. Also, we cannot  
 * assume GCC is being used.  
 */
```

```
./arch/sh/include/asm/processor.h
```

```
processor.h
```

```
No license information found.
```

```
./arch/sh/include/asm/ptrace.h
```

```
ptrace.h
```

```
/*  
 * Copyright (C) 1999, 2000 Niibe Yutaka  
 * from linux kernel code.  
 */  
/*  
 * GCC defines register number like this:  
 * -----  
 *      0 - 15 are integer registers  
 *     17 - 22 are control/special registers  
 *     24 - 39 fp registers  
 *     40 - 47 xd registers  
 *     48 -   fpscr register  
 * -----  
 *  
 * We follows above, except:  
 *     16 --- program counter (PC)  
 *     22 --- syscall #  
 *     23 --- floating point communication register  
 */
```

```
./arch/sh/include/asm/string.h
```

```
string.h
```

```
/*  
 * Copyright (C) 1999 Niibe Yutaka  
 * But consider these trivial functions to be public domain.  
 *  
 * from linux kernel code.  
 */
```

```
./arch/sh/include/asm/system.h
```

```
system.h
```

```
/*  
 * Copyright (C) 1999, 2000 Niibe Yutaka & Kaz Kojima  
 * Copyright (C) 2002 Paul Mundt  
 *  
 * from linux kernel code.
```

```

*/

./arch/sh/include/asm/types.h
types.h
/*
 * __xx is ok: it doesn't pollute the POSIX namespace. Use these in the
 * header files exported to user space
 */

./arch/sh/include/asm/u-boot.h
u-boot.h
/*
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 * published by the Free Software Foundation; either version 2 of
 * the License, or (at your option) any later version.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 *
 * *****
 * NOTE: This header file defines an interface to U-Boot. Including
 * this (unmodified) header file in another file is considered normal
 * use of U-Boot, and does *not* fall under the heading of "derived
 * work".
 * *****
 */

./arch/sh/include/asm/unaligned-sh4a.h
unaligned-sh4a.h
/*
 * SH-4A has support for unaligned 32-bit loads, and 32-bit loads only.
 * Support for 64-bit accesses are done through shifting and masking
 * relative to the endianness. Unaligned stores are not supported by the
 * instruction encoding, so these continue to use the packed
 * struct.
 *
 * The same note as with the movli.l/movco.l pair applies here, as long
 * as the load is guaranteed to be inlined, nothing else will hook in to
 * r0 and we get the return value for free.
 *
 * NOTE: Due to the fact we require r0 encoding, care should be taken to
 * avoid mixing these heavily with other r0 consumers, such as the atomic

```

```
* ops. Failure to adhere to this can result in the compiler running out
* of spill registers and blowing up when building at low optimization
* levels. See http://gcc.gnu.org/bugzilla/show\_bug.cgi?id=34777.
*/
```

```
./arch/sh/include/asm/unaligned.h
```

```
unaligned.h
```

```
/* Copy from linux-kernel. */
```

```
/* SH-4A can handle unaligned loads in a relatively neutered fashion. */
```

```
/* Otherwise, SH can't handle unaligned accesses. */
```

```
./arch/sh/include/asm/zimage.h
```

```
zimage.h
```

```
/*
```

```
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```

```
* Renesas Solutions Corp.
```

```
* Nobuhiro Iwamatsu <nobuhiro.iwamatsu.yj@renesas.com>
```

```
/*
```

```
* See file CREDITS for list of people who contributed to this
* project.
```

```
/*
```

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```

```
/*
```

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```

```
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```

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* MA 02111-1307 USA
```

```
*/
```

```
./arch/sh/lib/ashldi3.c
```

```
ashldi3.c
```

```
No license information found.
```

```
./arch/sh/lib/board.c
```

```
board.c
```

```
/*
```

```
* Copyright (C) 2007, 2008, 2010
```

```
* Nobuhiro Iwamatsu <iwamatsu@nigauri.org>
```

```
/*
```

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* MA 02111-1307 USA
*/
```

```
./arch/sh/lib/bootm.c
```

```
bootm.c
```

```
/*
* (C) Copyright 2003
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* (c) Copyright 2008 Nobuhiro Iwamatsu <iwamatsu.nobuhiro@renesas.com>
* (c) Copyright 2008 Renesas Solutions Corp.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*/
```

```
./arch/sh/lib/libgcc.h
```

```
libgcc.h
```

```
No license information found.
```

```
./arch/sh/lib/lshrdi3.c
```

```
lshrdi3.c
```

```
No license information found.
```

```
./arch/sh/lib/time.c
```

time.c

```
/*
 * (C) Copyright 2009
 * Jean-Christophe PLAGNIOL-VILLARD <plagnioj@jcrosoft.com>
 *
 * (C) Copyright 2007-2012
 * Nobobuhiro Iwamatsu <iwamatsu@nigauri.org>
 *
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 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/sh/lib/time_sh2.c

time_sh2.c

```
/*
 * Copyright (C) 2007,2008 Nobobuhiro Iwamatsu <iwamatsu@nigauri.org>
 * Copyright (C) 2008 Renesas Solutions Corp.
 *
 * (C) Copyright 2003
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/sh/lib/zimageboot.c
zimageboot.c
```

```
/*
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* Nobuhiro Iwamatsu <nobuhiro.iwamatsu.yj@renesas.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/sparc/cpu/leon2/cpu.c
cpu.c
```

```
/* CPU specific code for the LEON2 CPU
*
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* Daniel Hellstrom, Gaisler Research, daniel@gaisler.com
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* MA 02111-1307 USA
*/
```

```
./arch/sparc/cpu/leon2/cpu_init.c
```

```
cpu_init.c
```

```
/* Initializes CPU and basic hardware such as memory
* controllers, IRQ controller and system timer 0.
*
```

```
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* Daniel Hellstrom, Gaisler Research, daniel@gaisler.com
*
```

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*
```

```
*/
```

```
./arch/sparc/cpu/leon2/interrupts.c
```

```
interrupts.c
```

```
/*
```

```
* (C) Copyright 2007
* Daniel Hellstrom, Gaisler Research, daniel@gaisler.com
*
```

```
* (C) Copyright 2006
* Detlev Zundel, DENX Software Engineering, dzu@denx.de
*
```

```
* (C) Copyright -2003
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
```

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* Josh Huber <huber@mclx.com>, Mission Critical Linux, Inc.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/sparc/cpu/leon2/prom.c
```

```
prom.c
```

```
/* prom.c - emulates a sparc v0 PROM for the linux kernel.
```

```
*
* Copyright (C) 2003 Konrad Eisele <eiselekd@web.de>
* Copyright (C) 2004 Stefan Holst <mail@s-holst.de>
* Copyright (C) 2007 Daniel Hellstrom <daniel@gaisler.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/sparc/cpu/leon2/serial.c
serial.c
/* GRLIB APBUART Serial controller driver
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
```

```
./arch/sparc/cpu/leon3/ambapp.c
ambapp.c
/* Gaisler AMBA Plug&Play bus scanning. Functions
* ending on _nomem is intended to be used only during
* initialization, only registers are used (no ram).
*
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* MA 02111-1307 USA
*/
```

```
./arch/sparc/cpu/leon3/cpu.c
```

```
cpu.c
```

```
/* CPU specific code for the LEON3 CPU
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/sparc/cpu/leon3/cpu_init.c
```

```
cpu_init.c
```

```
/* Initializes CPU and basic hardware such as memory
```

```
* controllers, IRQ controller and system timer 0.
*
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* MA 02111-1307 USA
*
*/
```

```
./arch/sparc/cpu/leon3/interrupts.c
interrupts.c
```

```
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*
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*/
```

```
./arch/sparc/cpu/leon3/prom.c
prom.c
```

```
/* prom.c - emulates a sparc v0 PROM for the linux kernel.
*
* Copyright (C) 2003 Konrad Eisele <eiselekd@web.de>
* Copyright (C) 2004 Stefan Holst <mail@s-holst.de>
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```

```
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* MA 02111-1307 USA
*
*/
```

```
./arch/sparc/cpu/leon3/serial.c
```

```
serial.c
```

```
/* GRLIB APBUART Serial controller driver
```

```
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```

```
*/
```

```
./arch/sparc/cpu/leon3/usb_uhci.c
```

```
usb_uhci.c
```

```
/*
 * Part of this code has been derived from linux:
 * Universal Host Controller Interface driver for USB (take II).
 *
 * (c) 1999-2001 Georg Acher, acher@in.tum.de (executive slave) (base guitar)
 *                Deti Fliegl, deti@fliegl.de (executive slave) (lead voice)
 *                Thomas Sailer, sailer@ife.ee.ethz.ch (chief consultant) (cheer
leader)
 *                Roman Weissgaerber, weissg@vienna.at (virt root hub) (studio
porter)
 * (c) 2000        Yggdrasil Computing, Inc. (port of new PCI interface support
 *                from usb-ohci.c by Adam Richter, adam@yggdrasil.com).
 * (C) 2000        David Brownell, david-b@pacbell.net (usb-ohci.c)
 *
 * HW-initialization based on material of
 *
 * (C) Copyright 1999 Linus Torvalds
 * (C) Copyright 1999 Johannes Erdfelt
 * (C) Copyright 1999 Randy Dunlap
 * (C) Copyright 1999 Gregory P. Smith
 *
 *
 * Adapted for U-Boot:
 * (C) Copyright 2001 Denis Peter, MPL AG Switzerland
 * (C) Copyright 2008, Daniel Hellström, daniel@gaisler.com
 *     Added AMBA Plug&Play detection of GRUSB, modified interrupt handler.
 *     Added cache flushes where needed.
 *
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 * MA 02111-1307 USA
 *
 */
```

```
./arch/sparc/cpu/leon3/usb_uhci.h
```

usb_uhci.h

```
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 * Denis Peter, MPL AG Switzerland
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 *
 * Note: Part of this code has been derived from linux
 */
```

./arch/sparc/include/asm/arch-leon2/asi.h

asi.h

```
/* asi.h: Address Space Identifier values for the LEON2 sparc.
 *
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 *
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 * MA 02111-1307 USA
```

```
*
*/

./arch/sparc/include/asm/arch-leon3/asi.h
asi.h
/* asi.h: Address Space Identifier values for the LEON3 sparc.
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
```

```
./arch/sparc/include/asm/asi.h
asi.h
/* Address Space Identifier (ASI) values for sparc processors.
*
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* MA 02111-1307 USA
*
*/
```

```
./arch/sparc/include/asm/asmmacro.h
asmmacro.h
```

```
/* Assembler macros for SPARC
```

```
*
* (C) Copyright 2007, taken from linux asm-sparc/asmmacro.h
* Daniel Hellstrom, Gaisler Research, daniel@gaisler.com.
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*
*/
```

```
./arch/sparc/include/asm/atomic.h
atomic.h
```

```
/* SPARC atomic operations
```

```
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*
*/
```

```
./arch/sparc/include/asm/bitops.h
```

```
bitops.h
```

```
/* Bit string operations on the SPARC
```

```
*
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* Daniel Hellstrom, Gaisler Research, daniel@gaisler.com.
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* MA 02111-1307 USA
*
*/
```

```
./arch/sparc/include/asm/byteorder.h
```

```
byteorder.h
```

```
/*
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*
*/
```

```
./arch/sparc/include/asm/cache.h
```

```
cache.h
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
```

```
./arch/sparc/include/asm/config.h
```

```
config.h
```

```
/*
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```

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* MA 02111-1307 USA
*
*/
```

```
./arch/sparc/include/asm/errno.h
errno.h
No license information found.
```

```
./arch/sparc/include/asm/global_data.h
global_data.h
/*
```

```
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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* MA 02111-1307 USA
*/
```

```
./arch/sparc/include/asm/io.h
io.h
/* SPARC I/O definitions
*
```

```
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* MA 02111-1307 USA
*
*/
```

```
./arch/sparc/include/asm/irq.h
irq.h
```

```
/* IRQ functions
```

```
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*
*/
```

```
./arch/sparc/include/asm/leon.h
leon.h
```

```
/* LEON Header File select
```

```
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```

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* MA 02111-1307 USA
*
*/
```

```
./arch/sparc/include/asm/leon2.h
```

```
leon2.h
```

```
/* LEON2 header file. LEON2 is a SOC processor.
```

```
*/
```

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```

```
*/
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```

```
*/
```

```
*/
```

```
./arch/sparc/include/asm/leon3.h
```

```
leon3.h
```

```
/* LEON3 header file. LEON3 is a free GPL SOC processor available
```

```
*/ at www.gaisler.com.
```

```
*/
```

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*
*/
```

```
./arch/sparc/include/asm/machines.h
machines.h
```

```
/* machines.h: Defines for taking apart the machine type value in the
*             idprom and determining the kind of machine we are on.
*
* Taken from the SPARC port of Linux.
*
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*
*/
```

```
./arch/sparc/include/asm/page.h
page.h
```

```
/* page.h: Various defines and such for MMU operations on the Sparc for
*         the Linux kernel.
*
* Copyright (C) 1995 David S. Miller (davem@caip.rutgers.edu)
```

```
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*
*/
```

```
./arch/sparc/include/asm/posix_types.h
```

```
posix_types.h
```

```
/*
* (C) Copyright 2000 - 2002
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
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*
*/
```

```
./arch/sparc/include/asm/processor.h
```

```
processor.h
```

```
/* SPARC Processor specifics
* taken from the SPARC port of Linux (ptrace.h).
*
*/
```

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*
*/
```

```
./arch/sparc/include/asm/prom.h
```

```
prom.h
```

```
/* OpenProm defines mainly taken from linux kernel header files
```

```
*
* openprom.h: Prom structures and defines for access to the OPENBOOT
* prom routines and data areas.
```

```
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```

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```

```
*/
```

```
./arch/sparc/include/asm/psr.h
```

```
psr.h
```

```
/* psr.h: This file holds the macros for masking off various parts of
```

```
* the processor status register on the Sparc. This is valid
* for Version 8. On the V9 this is renamed to the PSTATE
* register and its members are accessed as fields like
* PSTATE.PRIV for the current CPU privilege level.
```

```
*
* taken from the SPARC port of Linux,
```

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```

```
*/
```

```
./arch/sparc/include/asm/ptrace.h
```

```
ptrace.h
```

```
/* Contain the Stack frame layout on interrupt. pt_regs.
```

```
* taken from the SPARC port of Linux (ptrace.h).
```

```
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```

```
*
```

```
*/  
  
./arch/sparc/include/asm/srmmu.h  
srmmu.h  
/* SRMMU page table defines and code,  
* taken from the SPARC port of Linux  
*  
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* MA 02111-1307 USA  
*  
*/
```

```
./arch/sparc/include/asm/stack.h  
stack.h  
/* SPARC stack layout Macros and structures,  
* mainly taken from BCC (the Bare C compiler for  
* SPARC LEON2/3) sources.  
*  
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```

```
*
*/

./arch/sparc/include/asm/string.h
string.h
/*
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*
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*
*/
```

```
./arch/sparc/include/asm/types.h
types.h
/*
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```

```

*/

./arch/sparc/include/asm/u-boot.h
u-boot.h
/*
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*
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* Daniel Hellstrom, Gaisler Research, daniel@gaisler.com.
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* MA 02111-1307 USA
*
*****
* NOTE: This header file defines an interface to U-Boot. Including
* this (unmodified) header file in another file is considered normal
* use of U-Boot, and does *not* fall under the heading of "derived
* work".
*****
*/

./arch/sparc/include/asm/unaligned.h
unaligned.h
/*
* The SPARC can not do unaligned accesses, it must be split into multiple
* byte accesses. The SPARC is in big endian mode.
*/

./arch/sparc/include/asm/winmacro.h
winmacro.h
/*
* Added to U-boot,
* Daniel Hellstrom, Gaisler Research, daniel@gaisler.com
* Copyright (C) 2007
*
* LEON2/3 LIBIO low-level routines
* Written by Jiri Gaisler.

```

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```

```
./arch/sparc/lib/board.c
```

```
board.c
```

```
/* SPARC Board initialization
```

```
*
```

```
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```

```
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
```

```
*
```

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*
```

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*
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```

```
*/
```

```
./arch/sparc/lib/bootm.c
```

```
bootm.c
```

```
/* SPARC code for booting linux 2.6
```

```
*
```

```
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```
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```

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```
*
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/sparc/lib/cache.c
```

```
cache.c
```

```
/* Sparc cache library
```

```
*
```

```
* (C) Copyright 2007  
* Daniel Hellstrom, Gaisler Research, daniel@gaisler.com.
```

```
*
```

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* MA 02111-1307 USA
```

```
*/
```

```
./arch/sparc/lib/interrupts.c
```

```
interrupts.c
```

```
/*
```

```
* (C) Copyright 2000-2002  
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
```

```
*
```

```
* (C) Copyright 2003  
* Gleb Natapov <gnatapov@mr.v.com>
```

```
*
```

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* MA 02111-1307 USA  
*/
```

```
./arch/sparc/lib/time.c
```

```
time.c
```

```
/*  
* (C) Copyright 2000, 2001  
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
* (C) Copyright 2007  
* Daniel Hellstrom, Gaisler Research, daniel@gaisler.com.  
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* MA 02111-1307 USA  
*/
```

```
./arch/x86/cpu/coreboot/asm-offsets.c
```

```
asm-offsets.c
```

```
/*  
* Adapted from Linux v2.6.36 kernel: arch/powerpc/kernel/asm-offsets.c
```

```
*
* This program is used to generate definitions needed by
* assembly language modules.
*
* We use the technique used in the OSF Mach kernel code:
* generate asm statements containing #defines,
* compile this file to assembler, and then extract the
* #defines from the assembly-language output.
*
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* 2 of the License, or (at your option) any later version.
*/
```

```
./arch/x86/cpu/coreboot/coreboot.c
coreboot.c
```

```
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* Graeme Russ, graeme.russ@gmail.com.
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*/
```

```
./arch/x86/cpu/coreboot/ipchecksum.c
ipchecksum.c
```

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*/
```

```
./arch/x86/cpu/coreboot/pci.c
```

```
pci.c
```

```
/*
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* Graeme Russ, <graeme.russ@gmail.com>
*
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```

```
* MA 02111-1307 USA
*/
```

```
./arch/x86/cpu/coreboot/sdram.c
sdram.c
```

```
/*
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 * MA 02111-1307 USA
*/
```

```
./arch/x86/cpu/coreboot/tables.c
tables.c
```

```
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* SUCH DAMAGE.
*/
```

```
./arch/x86/cpu/coreboot/timestamp.c
```

```
timestamp.c
```

```
/*
```

```
* This file is part of the coreboot project.
```

```
*
```

```
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```

```
*
```

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```

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```

```
*/
```

```
./arch/x86/cpu/cpu.c
```

```
cpu.c
```

```
/*
```

```
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```

```
* Graeme Russ, <graeme.russ@gmail.com>
```

```
*
```

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* Daniel Engström, Omicron Ceti AB, <daniel@omicron.se>
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```
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```

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```

```
* Marius Groeger <mgroeger@sysgo.de>
```

```
*
```

```
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* Alex Zuepke <azu@sysgo.de>
```

```
*
```

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* MA 02111-1307 USA  
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```

```
./arch/x86/cpu/interrupts.c  
interrupts.c
```

```
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*/
```

```
./arch/x86/cpu/sc520/asm-offsets.c
```

asm-offsets.c

```
/*
 * Adapted from Linux v2.6.36 kernel: arch/powerpc/kernel/asm-offsets.c
 *
 * This program is used to generate definitions needed by
 * assembly language modules.
 *
 * We use the technique used in the OSF Mach kernel code:
 * generate asm statements containing #defines,
 * compile this file to assembler, and then extract the
 * #defines from the assembly-language output.
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 */
```

./arch/x86/cpu/sc520/sc520.c

sc520.c

```
/*
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 *
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 */
```

./arch/x86/cpu/sc520/sc520_pci.c

sc520_pci.c

```
/*
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```

```
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*/
```

```
./arch/x86/cpu/sc520/sc520_reset.c
sc520_reset.c
```

```
/*
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```

```
*/  
  
./arch/x86/cpu/sc520/sc520_sdram.c  
sc520_sdram.c  
/*  
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*/
```

```
./arch/x86/cpu/sc520/sc520_ssi.c  
sc520_ssi.c  
/*  
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```

```
*/  
  
./arch/x86/cpu/sc520/sc520_timer.c  
sc520_timer.c  
/*  
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* Graeme Russ, <graeme.russ@gmail.com>  
*  
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* MA 02111-1307 USA  
*/
```

```
./arch/x86/cpu/timer.c  
timer.c  
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*/
```

```
./arch/x86/include/asm/arch-coreboot/ipchecksum.h  
ipchecksum.h  
/*  
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*  
* It has originally been taken from the FreeBSD project.  
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*/
```

```
./arch/x86/include/asm/arch-coreboot/sysinfo.h
sysinfo.h
```

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```

```
./arch/x86/include/asm/arch-coreboot/tables.h
tables.h
```

```
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*/
```

```
./arch/x86/include/asm/arch-coreboot/timestamp.h
timestamp.h
```

```
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*/
```

```
./arch/x86/include/asm/arch-sc520/pci.h
```

```
pci.h
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/x86/include/asm/arch-sc520/sc520.h
```

```
sc520.h
```

```
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*/
```

```
./arch/x86/include/asm/arch-sc520/ssi.h
ssi.h
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/x86/include/asm/bitops.h
bitops.h
```

```
/*
* Copyright 1992, Linus Torvalds.
*/
/*
* These have to be done with inline assembly: that way the bit-setting
* is guaranteed to be atomic. All bit operations return 0 if the bit
* was cleared before the operation and != 0 if it was not.
*
* bit 0 is the LSB of addr; bit 32 is the LSB of (addr+1).
*/
```

```
./arch/x86/include/asm/bootparam.h
bootparam.h
/* setup data types */
```

```
./arch/x86/include/asm/byteorder.h
byteorder.h
No license information found.
```

```
./arch/x86/include/asm/cache.h
cache.h
```

```
/*
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./arch/x86/include/asm/config.h
config.h
```

```
/*
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 *
 */
```

```
./arch/x86/include/asm/control_regs.h
```

control_regs.h

```
/*
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 *
 * (C) Copyright 2008-2011
 * Graeme Russ, <graeme.russ@gmail.com>
 *
 * (C) Copyright 2002
 * Daniel Engström, Omicron Ceti AB, <daniel@omicron.se>
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./arch/x86/include/asm/e820.h

e820.h

```
/*
 * Legacy E820 BIOS limits us to 128 (E820MAX) nodes due to the
 * constrained space in the zeropage. If we have more nodes than
 * that, and if we've booted off EFI firmware, then the EFI tables
 * passed us from the EFI firmware can list more nodes. Size our
 * internal memory map tables to have room for these additional
 * nodes, based on up to three entries per node for which the
 * kernel was built: MAX_NUMNODES == (1 << CONFIG_NODES_SHIFT),
 * plus E820MAX, allowing space for the possible duplicate E820
 * entries that might need room in the same arrays, prior to the
 * call to sanitize_e820_map() to remove duplicates. The allowance
 * of three memory map entries per node is "enough" entries for
 * the initial hardware platform motivating this mechanism to make
 * use of additional EFI map entries. Future platforms may want
 * to allow more than three entries per node or otherwise refine
 * this size.
 */
```

*/

./arch/x86/include/asm/errno.h

errno.h

No license information found.

./arch/x86/include/asm/global_data.h

global_data.h

/*

* (C) Copyright 2002-2010

* Wolfgang Denk, DENX Software Engineering, wd@denx.de.

*

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* project.

*

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*/

./arch/x86/include/asm/gpio.h

gpio.h

/*

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```
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/x86/include/asm/i8254.h  
i8254.h
```

```
/*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/x86/include/asm/i8259.h  
i8259.h
```

```
/*  
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```

```
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* MA 02111-1307 USA  
*/
```

```
./arch/x86/include/asm/ibmpc.h  
ibmpc.h
```

```
/*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/x86/include/asm/init_helpers.h  
init_helpers.h
```

```
/*  
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```

```
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*/
```

```
./arch/x86/include/asm/init_wrappers.h  
init_wrappers.h
```

```
/*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./arch/x86/include/asm/interrupt.h  
interrupt.h
```

```
/*  
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*  
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* MA 02111-1307 USA
*/
```

```
./arch/x86/include/asm/io.h
```

```
io.h
```

```
/*
 * This file contains the definitions for the x86 IO instructions
 * inb/inw/inl/outb/outw/outl and the "string versions" of the same
 * (insb/insw/insl/outsb/outsw/outsl). You can also use "pausing"
 * versions of the single-IO instructions (inb_p/inw_p/..).
 *
 * This file is not meant to be obfuscating: it's just complicated
 * to (a) handle it all in a way that makes gcc able to optimize it
 * as well as possible and (b) trying to avoid writing the same thing
 * over and over again with slight variations and possibly making a
 * mistake somewhere.
*/
```

```
./arch/x86/include/asm/ioctl.h
```

```
ioctl.h
```

```
No license information found.
```

```
./arch/x86/include/asm/ist.h
```

```
ist.h
```

```
/*
 * Include file for the interface to IST BIOS
 * Copyright 2002 Andy Grover <andrew.grover@intel.com>
 *
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 * under the terms of the GNU General Public License as published by the
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 * MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the GNU
 * General Public License for more details.
*/
```

```
./arch/x86/include/asm/msr-index.h
```

```
msr-index.h
```

```
/*
 * Taken from the linux kernel file of the same name
 *
 * (C) Copyright 2012
 * Graeme Russ, <graeme.russ@gmail.com>
```

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* MA 02111-1307 USA
*/
```

```
./arch/x86/include/asm/msr.h
msr.h
```

```
/*
* Taken from the linux kernel file of the same name
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/x86/include/asm/mtrr.h
mtrr.h
```

```
/*
* Generic MTRR (Memory Type Range Register) ioctls.
* Taken from the Linux kernel
*
* (C) Copyright 2012
* Graeme Russ, <graeme.russ@gmail.com>
*
*/
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/x86/include/asm/pci.h
```

```
pci.h
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/x86/include/asm/posix_types.h
```

```
posix_types.h
```

```
/*
* This file is generally used by user-level software, so you need to
* be a little careful about namespace pollution etc. Also, we cannot
* assume GCC is being used.
*/
```

```
./arch/x86/include/asm/processor-flags.h
processor-flags.h
/* Various flags defined: can be included from assembler. */
/*
 * EFLAGS bits
 */
```

```
./arch/x86/include/asm/processor.h
processor.h
/*
 * (C) Copyright 2002
 * Daniel Engström, Omicron Ceti AB, daniel@omicron.se
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./arch/x86/include/asm/ptrace.h
ptrace.h
No license information found.
```

```
./arch/x86/include/asm/realmode.h
realmode.h
/*
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 *
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*/
```

```
./arch/x86/include/asm/relocate.h
relocate.h
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./arch/x86/include/asm/string.h
string.h
```

```
/*
* We don't do inline string functions, since the
* optimised inline asm versions are not small.
*/
```

```
./arch/x86/include/asm/types.h
types.h
```

```
/*
* __xx is ok: it doesn't pollute the POSIX namespace. Use these in the
* header files exported to user space
*/
```

```
./arch/x86/include/asm/u-boot-x86.h
u-boot-x86.h
```

```
/*
 * (C) Copyright 2002
 * Daniel Engström, Omicron Ceti AB, daniel@omicron.se.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./arch/x86/include/asm/u-boot.h
u-boot.h
```

```
/*
 * (C) Copyright 2002
 * Sysgo Real-Time Solutions, GmbH <www.elinos.com>
 * Marius Groeger <mgroeger@sysgo.de>
 *
 * (C) Copyright 2002
 * Sysgo Real-Time Solutions, GmbH <www.elinos.com>
 * Alex Zuepke <azu@sysgo.de>
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 */
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
```

```

*****
* NOTE: This header file defines an interface to U-Boot. Including
* this (unmodified) header file in another file is considered normal
* use of U-Boot, and does *not* fall under the heading of "derived
* work".
*****
*/
```

```
./arch/x86/include/asm/unaligned.h
unaligned.h
No license information found.
```

```
./arch/x86/include/asm/video/edid.h
edid.h
No license information found.
```

```
./arch/x86/include/asm/zimage.h
zimage.h
/*
* (C) Copyright 2002
* Daniel Engström, Omicron Ceti AB, daniel@omicron.se
*
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* project.
*
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* MA 02111-1307 USA
*/
```

```
./arch/x86/lib/bios.h
bios.h
/*
* (C) Copyright 2002
```

```
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*/
```

```
./arch/x86/lib/bios_setup.c
```

```
bios_setup.c
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/x86/lib/board.c
```

```
board.c
```

```
/*
* (C) Copyright 2008-2011
```

```
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*
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* Daniel Engström, Omicron Ceti AB, <daniel@omicron.se>
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* MA 02111-1307 USA
*/
```

```
./arch/x86/lib/bootm.c
```

```
bootm.c
```

```
/*
```

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* Marius Groeger <mgroeger@sysgo.de>
*
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* MA 02111-1307 USA
*/
```

```
./arch/x86/lib/cmd_boot.c
```

```
cmd_boot.c
```

```
/*
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*
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* MA 02111-1307 USA
*/
```

```
./arch/x86/lib/gcc.c
```

```
gcc.c
```

```
/*
* This file is part of the coreboot project.
*
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```

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*/
```

```
./arch/x86/lib/init_helpers.c
```

```
init_helpers.c
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/x86/lib/init_wrappers.c
```

```
init_wrappers.c
```

```
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* MA 02111-1307 USA
*/
```

```
./arch/x86/lib/interrupts.c
interrupts.c
```

```
/*
* (C) Copyright 2009
* Graeme Russ, <graeme.russ@gmail.com>
*
* (C) Copyright 2007
* Daniel Hellstrom, Gaisler Research, <daniel@gaisler.com>
*
* (C) Copyright 2006
* Detlev Zundel, DENX Software Engineering, <dzu@denx.de>
*
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*/
```

```
./arch/x86/lib/pcat_interrupts.c
```

```
pcat_interrupts.c
```

```
/*
```

```
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* Graeme Russ, <graeme.russ@gmail.com>
```

```
*
```

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```

```
*/
```

```
./arch/x86/lib/pcat_timer.c
```

```
pcat_timer.c
```

```
/*
```

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```

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* MA 02111-1307 USA  
*/
```

```
./arch/x86/lib/pci.c
```

```
pci.c
```

```
/*  
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```

```
./arch/x86/lib/pci_type1.c
```

```
pci_type1.c
```

```
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* MA 02111-1307 USA  
*/
```

```
./arch/x86/lib/phymem.c
```

```
phymem.c
```

```
/*  
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*/
```

```
./arch/x86/lib/realmode.c
```

```
realmode.c
```

```
/*  
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*/
```

```
./arch/x86/lib/relocate.c
```

```
relocate.c
```

```
/*  
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*/

./arch/x86/lib/string.c
string.c
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*/
```

```
./arch/x86/lib/timer.c
```

```
timer.c
```

```
/*
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 *
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*/
```

```
./arch/x86/lib/video.c
```

```
video.c
```

```
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```

```
./arch/x86/lib/video_bios.c
video_bios.c
```

```
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* MA 02111-1307 USA
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```
./arch/x86/lib/zimage.c
zimage.c
```

```
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*/

./board/8dtech/eco5pk/eco5pk.c

eco5pk.c

/*
* eco5pk.c - board file for 8D Technology's AM3517 based eco5pk board
*
* Based on am3517evm.c
*
* Copyright (C) 2011-2012 8D Technologies inc.
* Copyright (C) 2009 Texas Instruments Incorporated
*
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*/

./board/8dtech/eco5pk/eco5pk.h

eco5pk.h

/*
* eco5.h - Header file for the 8D Technologies EC05 board.
*
* Based on am3517evm.h
* Based on ti/evm/evm.h
*
* Copyright (C) 2011 8D Technologies inc.
* Copyright (C) 2009 Texas Instruments Incorporated
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*/
```

```
./board/a3000/a3000.c
```

```
a3000.c
```

```
/*
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*
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* Ken Chou, kchou@ieee.org
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*/
```

```
./board/a3000/flash.c
```

```
flash.c
```

```
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*/
```

```
./board/a3m071/a3m071.c
a3m071.c
```

```
/*
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*
* (C) Copyright 2004
* Mark Jonas, Freescale Semiconductor, mark.jonas@motorola.com.
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*/
```

```
./board/a3m071/mt46v16m16-75.h
mt46v16m16-75.h
```

```
/*
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*/

./board/a4m072/a4m072.c
a4m072.c
/*
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*
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* MA 02111-1307 USA
*/
```

```
./board/a4m072/mt46v32m16.h
mt46v32m16.h
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./board/actux1/actux1.c
```

```
actux1.c
```

```
/*
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*
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* Marius Groeger <mgroeger@sysgo.de>
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*/
```

```
./board/actux1/actux1_hw.h
```

```
actux1_hw.h
```

```
/*
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 *
 * hardware register definitions for the AcTux-1 board.
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./board/actux2/actux2.c
actux2.c
```

```
/*
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 *
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 *
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*/
```

```
./board/actux2/actux2_hw.h
actux2_hw.h
```

```
/*
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*
* hardware register definitions for the AcTux-2 board.
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* MA 02111-1307 USA
*/
```

```
./board/actux3/actux3.c
actux3.c
```

```
/*
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*
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* MA 02111-1307 USA
*/
```

```
./board/actux3/actux3_hw.h
```

```
actux3_hw.h
```

```
/*
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*
* hardware register definitions for the AcTux-3 board.
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* MA 02111-1307 USA
*/
```

```
./board/actux4/actux4.c
```

actux4.c

```
/*
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 *
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 *
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 */
```

./board/actux4/actux4_hw.h

actux4_hw.h

```
/*
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 *
 * hardware register definitions for the AcTux-4 board.
 *
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* MA 02111-1307 USA  
*/
```

```
./board/adder/adder.c
```

```
adder.c
```

```
/*
```

```
* Copyright (C) 2004-2005 Arabella Software Ltd.
```

```
* Yuli Barcohen <yuli@arabellasw.com>
```

```
*
```

```
* Support for Analogue&Micro Adder boards family.
```

```
* Tested on AdderII and Adder87x.
```

```
*
```

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* project.
```

```
*
```

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* MA 02111-1307 USA  
*/
```

```
./board/afeb9260/afeb9260.c
```

```
afeb9260.c
```

```
/*
```

```
* (C) Copyright 2007-2008
```

```
* Stelian Pop <stelian@popies.net>
```

```
* Lead Tech Design <www.leadtechdesign.com>
```

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*/
```

```
./board/afeb9260/partition.c
partition.c
```

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* MA 02111-1307 USA
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*/
```

```
./board/ait/cam_enc_4xx/cam_enc_4xx.c
cam_enc_4xx.c
```

```
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```

```
./board/alaska/alaska.c
```

```
alaska.c
```

```
/*
```

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*/
```

```
./board/alaska/flash.c
```

```
flash.c
```

```
/*
```

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```

```
./board/alphaproject/ap_sh4a_4a/ap_sh4a_4a.c
ap_sh4a_4a.c
```

```
/*
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*/
```

```
./board/altera/common/AMDLV065D.c
AMDLV065D.c
```

```
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*/
```

```
./board/altera/common/cfide.c
cfide.c
```

```
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* Altera CF drvier
*
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```
./board/altera/common/eplcd.c
eplcd.c
```

```
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* Scott McNutt <smcnutt@psyent.com>
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*/
```

```
./board/altera/common/flash.c
flash.c
```

```
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```

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```

```
./board/altera/common/sevenseg.c
```

```
sevenseg.c
```

```
/*  
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* MA 02111-1307 USA  
*  
* common/sevenseg.c  
*  
* NIOS PIO based seven segment led support functions  
*/
```

```
./board/altera/common/sevenseg.h
sevenseg.h
/*
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 * Stephan Linz <linz@li-pro.net>
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 *
 * common/sevenseg.h
 *
 * NIOS PIO based seven segment led support functions
 */
```

```
./board/altera/nios2-generic/custom_fpga.h
custom_fpga.h
/*
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 *
 * This file is generated by socp-create-config-files.
 */
/* generated from std_1c20.sopc */
```

```
./board/altera/nios2-generic/nios2-generic.c
nios2-generic.c
/*
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*/

./board/altera/socfpga/build.h
build.h
/* GENERATED FILE - DO NOT EDIT */
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./board/altera/socfpga/iocsr_config_arria5.c
```

iocsr_config_arria5.c

```
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./board/altera/socfpga/iocsr_config_arria5.h

iocsr_config_arria5.h

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```
./board/altera/socfpga/iocsr_config_cyclone5.c
iocsr_config_cyclone5.c
```

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*/
```

```
./board/altera/socfpga/iocsr_config_cyclone5.h
iocsr_config_cyclone5.h
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```

```
./board/altera/socfpga/pinmux_config.h
```

```
pinmux_config.h
```

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```
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pinmux_config_arria5.c  
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```
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```

```
./board/altera/socfpga/pll_config.h
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```
pll_config.h
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```
./board/altera/socfpga/reset_config.h
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```
reset_config.h
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```
./board/altera/socfpga/sdram/alt_types.h
alt_types.h
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```
./board/altera/socfpga/sdram/sdram.h
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```
sdram.h
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```
./board/altera/socfpga/sdram/sdram_config.h
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```
sdram_config.h
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```
./board/altera/socfpga/sdram/sdram_io.h
sdram_io.h
```

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```
./board/altera/socfpga/sdram/sequencer.c
sequencer.c
```

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```
./board/altera/socfpga/sdram/sequencer.h
sequencer.h
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```
./board/altera/socfpga/sdram/sequencer_auto.h
sequencer_auto.h
/*
```

```
./board/altera/socfpga/sdram/sequencer_auto_ac_init.c
sequencer_auto_ac_init.c
/*
```

```
./board/altera/socfpga/sdram/sequencer_auto_inst_init.c
sequencer_auto_inst_init.c
/*
```

```
./board/altera/socfpga/sdram/sequencer_defines.h
sequencer_defines.h
/*
```

```
./board/altera/socfpga/sdram/system.h
system.h
/*
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tclrpt.c

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tclrpt.h

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```

```
./board/altera/socfpga/socfpga_arria5.c
socfpga_arria5.c
```

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```

```
./board/altera/socfpga/socfpga_common.c
socfpga_common.c
```

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```

```
./board/altera/socfpga/socfpga_cyclone5.c
socfpga_cyclone5.c
```

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```

```
./board/amcc/acadia/acadia.c
acadia.c
```

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```

```
./board/amcc/acadia/cmd_acadia.c
cmd_acadia.c
```

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*/
```

```
./board/amcc/acadia/memory.c
```

```
memory.c
```

```
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```

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```

```
./board/amcc/acadia/pll.c
```

```
pll.c
```

```
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```

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```

```
./board/amcc/bamboo/bamboo.c
bamboo.c
```

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```
./board/amcc/bamboo/bamboo.h
bamboo.h
```

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```
./board/amcc/bamboo/flash.c
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```

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```

```
./board/amcc/bluestone/bluestone.c
bluestone.c
```

```
/*
* Bluestone board support
*
```

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```
./board/amcc/bubinga/bubinga.c
```

```
bubinga.c
```

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./board/amcc/bubinga/flash.c
```

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```
./board/amcc/canyonlands/canyonlands.c
canyonlands.c
```

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./board/amcc/canyonlands/chip_config.c
chip_config.c
```

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```
./board/amcc/common/flash.c
flash.c
```

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```
./board/amcc/ebony/ebony.c
ebony.c
```

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./board/amcc/ebony/flash.c
```

```
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```

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```
./board/amcc/katmai/chip_config.c
```

```
chip_config.c
```

```
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```

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```
./board/amcc/katmai/katmai.c
katmai.c
```

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```
./board/amcc/kilauea/chip_config.c
```

chip_config.c

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./board/amcc/kilauea/kilauea.c

kilauea.c

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 */
```

```
./board/amcc/luan/epld.h
epld.h
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```

```
./board/amcc/luan/flash.c
flash.c
```

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```
./board/amcc/luan/luan.c
luan.c
```

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```

```
./board/amcc/makalu/cmd_pll.c
```

```
cmd_pll.c
```

```
/*
```

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```
*
```

```
*/
```

```
./board/amcc/makalu/makalu.c
```

```
makalu.c
```

```
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```
./board/amcc/ocotea/flash.c
```

```
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```

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```

```
./board/amcc/ocotea/ocotea.c
```

```
ocotea.c
```

```
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```
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```

```
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*/
```

```
./board/amcc/redwood/redwood.c
```

```
redwood.c
```

```
/*
* This is the main board level file for the Redwood AMCC board.
*
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*/
```

```
./board/amcc/redwood/redwood.h
redwood.h
```

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```

```
./board/amcc/sequoia/chip_config.c
chip_config.c
```

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*
*/
```

```
./board/amcc/sequoia/sdram.c
```

```
sdram.c
```

```
/*
```

```
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* Jacqueline Pira-Ferriol, AMCC/IBM, jpira-ferriol@fr.ibm.com
* Thierry Roman, AMCC/IBM, thierry_roman@fr.ibm.com
* Alain Saurel, AMCC/IBM, alain.saurel@fr.ibm.com
* Robert Snyder, AMCC/IBM, rob.snyder@fr.ibm.com
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```

```
./board/amcc/sequoia/sequoia.c
```

```
sequoia.c
```

```
/*
```

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```

```
./board/amcc/taihu/flash.c  
flash.c
```

```
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```

```
./board/amcc/taihu/lcd.c  
lcd.c  
/*
```

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```

```
./board/amcc/taihu/taihu.c
```

```
taihu.c
```

```
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*/
```

```
./board/amcc/taihu/update.c
```

```
update.c
```

```
/*
```

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```

```
./board/amcc/taishan/lcd.c
```

```
lcd.c
```

```
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*/
```

```
./board/amcc/taishan/showinfo.c
```

```
showinfo.c
```

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*/
```

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*/
```

```
./board/amcc/taishan/taishan.c
```

```
taishan.c
```

```
/*
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*
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```
./board/amcc/taishan/update.c
```

```
update.c
```

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*/
```

```
./board/amcc/walnut/flash.c
```

```
flash.c
```

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```

```
./board/amcc/walnut/walnut.c
```

```
walnut.c
```

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```

```
./board/amcc/yosemite/yosemite.c
```

```
yosemite.c
```

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```

```
./board/amcc/yucca/cmd_yucca.c
```

```
cmd_yucca.c
```

```
/*  
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```

```
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*
* hacked for evb440spe
*/
```

```
./board/amcc/yucca/flash.c
flash.c
```

```
/*
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*
* (C) Copyright 2002 Jun Gu <jung@artesyincp.com>
* Add support for Am29F016D and dynamic switch setting.
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```
./board/amcc/yucca/yucca.c
```

```
yucca.c
```

```
/*
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```
*
```

```
* Port to AMCC-440SPE Evaluation Board SOP - April 2005
```

```
*
```

```
* PCIe supporting routines derived from Linux 440SPe PCIe driver.
```

```
*/
```

```
./board/amcc/yucca/yucca.h
```

```
yucca.h
```

```
/*
```

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```

```
./board/AndesTech/adp-ag101/adp-ag101.c
```

```
adp-ag101.c
```

```
/*
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 * Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
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*/
```

```
./board/AndesTech/adp-ag101p/adp-ag101p.c
```

```
adp-ag101p.c
```

```
/*
 * Copyright (C) 2011 Andes Technology Corporation
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 * Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
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```

```
./board/AndesTech/adp-ag102/adp-ag102.c
```

```
adp-ag102.c
```

```
/*
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 * Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
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```

```
./board/arm ltd/integrator/arm-ebi.h
```

```
arm-ebi.h
```

```
/*
 * (C) Copyright 2011
 * Linaro
 * Linus Walleij <linus.walleij@linaro.org>
 * Register definitions for the External Bus Interface (EBI)
 * found in the ARM Integrator AP and CP reference designs
 *
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```

```
./board/arm ltd/integrator/integrator-sc.h
integrator-sc.h
```

```
/*
* (C) Copyright 2011
* Linaro
* Linus Walleij <linus.walleij@linaro.org>
* Register definitions for the System Controller (SC) and
* the similar "CP Controller" found in the ARM Integrator/AP and
* Integrator/CP reference designs
*
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*/
```

```
./board/arm ltd/integrator/integrator.c
integrator.c
```

```
/*
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* Marius Groeger <mgroeger@sysgo.de>
*
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* David Mueller, ELSOFT AG, <d.mueller@elsoft.ch>
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*/
```

```
./board/arm ltd/integrator/pci.c
```

```
pci.c
```

```
/*
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*/
```

```
./board/arm ltd/integrator/pci_v3.h
pci_v3.h
```

```
/*
 * arch/arm/include/asm/hardware/pci_v3.h
 *
 * Internal header file PCI V3 chip
 *
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```
./board/arm ltd/integrator/timer.c
timer.c
```

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```

```
./board/arm ltd/versatile/versatile.c
```

```
versatile.c
```

```
/*
```

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```
./board/arm1td/vexpress/ca9x4_ct_vxp.c
ca9x4_ct_vxp.c
```

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```

```
./board/astro/mcf53731/astro.h
astro.h
/* in mcf53731.c */
/* in fpga.c */
```

```
./board/astro/mcf53731/fpga.c
fpga.c
/*
```

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```

```
./board/astro/mcf53731/mcf53731.c
mcf53731.c
/*
```

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```
./board/atc/atc.c
```

```
atc.c
```

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```
./board/atc/flash.c
```

```
flash.c
```

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```
./board/atc/ti113x.c
ti113x.c
```

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```

```
./board/atmark-techno/armadillo-800eva/armadillo-800eva.c
armadillo-800eva.c
```

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```

```
./board/atmel/at91rm9200ek/at91rm9200ek.c
at91rm9200ek.c
```

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```
*/  
  
./board/atmel/at91rm9200ek/led.c  
led.c  
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*/
```

```
./board/atmel/at91sam9260ek/at91sam9260ek.c  
at91sam9260ek.c  
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```

```
./board/atmel/at91sam9260ek/led.c
```

```
led.c
```

```
/*
```

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```
*/
```

```
./board/atmel/at91sam9260ek/partition.c
```

```
partition.c
```

```
/*
```

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```
./board/atmel/at91sam9261ek/at91sam9261ek.c
at91sam9261ek.c
```

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```
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at91sam9263ek.c
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```
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```

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```

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partition.c
```

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```
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at91sam9m10g45ek.c
```

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```
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* MA 02111-1307 USA  
*/
```

```
./board/atmel/at91sam9rlek/led.c  
led.c
```

```
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* MA 02111-1307 USA
*/
```

```
./board/atmel/at91sam9rlek/partition.c
partition.c
```

```
/*
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* Ulf Samuelsson <ulf@atmel.com>
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* MA 02111-1307 USA
*
*/
```

```
./board/atmel/at91sam9x5ek/at91sam9x5ek.c
at91sam9x5ek.c
```

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```

```
./board/atmel/atngw100/atngw100.c
atngw100.c
```

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*/
```

```
./board/atmel/atngw100mkii/atngw100mkii.c
atngw100mkii.c
```

```
/*
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*
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* MA 02111-1307 USA
*/
```

```
./board/atmel/atstk1000/atstk1000.c
```

```
atstk1000.c
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./board/avionic-design/common/tamonten.c
```

```
tamonten.c
```

```
/*
* (C) Copyright 2010,2011
* NVIDIA Corporation <www.nvidia.com>
* (C) Copyright 2011-2012
* Avionic Design GmbH <www.avionic-design.de>
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* MA 02111-1307 USA  
*/
```

```
./board/avnet/fx12mm/fx12mm.c
```

```
fx12mm.c
```

```
/*  
* (C) Copyright 2008  
*  
* Author: Xilinx Inc.  
*  
* Modified by:  
* Georg Schardt <schardt@team-ctech.de>  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*  
*/
```

```
./board/avnet/fx12mm/xparameters.h
```

```
xparameters.h
```

```
/*  
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*  
* Georg Schardt <schardt@team-ctech.de>  
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* MA 02111-1307 USA
*
* CAUTION: This file is based on the xparameters.h automatically
* generated by libgen. Version: Xilinx EDK 10.1.02 Build EDK_K_SP2.5
*/
```

```
./board/avnet/v5fx30teval/v5fx30teval.c
v5fx30teval.c
```

```
/*
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*/
```

```
./board/avnet/v5fx30teval/xparameters.h
xparameters.h
```

```
/*
* (C) Copyright 2008
* Ricardo Ribalda-Universidad Autonoma de Madrid-ricardo.ribalda@uam.es
* This work has been supported by: QTechnology http://qtec.com/
* based on xparameters.h by Xilinx
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*/
```

```
./board/balloon3/balloon3.c  
balloon3.c
```

```
/*  
* Balloon3 Support  
*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./board/bc3450/bc3450.c  
bc3450.c
```

```
/*  
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* (C) Copyright 2004  
* Mark Jonas, Freescale Semiconductor, mark.jonas@motorola.com.  
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* Martin Krause, TQ-Systems GmbH, martin.krause@tqs.de  
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* MA 02111-1307 USA
*/
```

```
./board/bc3450/cmd_bc3450.c
cmd_bc3450.c
```

```
/*
* (C) Copyright 2005
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*
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```

```
./board/bc3450/mt48lc16m16a2-75.h
mt48lc16m16a2-75.h
```

```
/*
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* Mark Jonas, Freescale Semiconductor, mark.jonas@motorola.com.
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*/
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*/
```

```
./board/bct-brettl2/bct-brettl2.c
```

```
bct-brettl2.c
```

```
/*
 * U-boot - main board file for BCT brettl2
 *
 * Copyright (c) 2010 BCT Electronic GmbH
 *
 * Licensed under the GPL-2 or later.
 */
```

```
./board/bct-brettl2/cled.c
```

```
cled.c
```

```
/*
 * cled.c - control color led
 *
 * Copyright (c) 2010 BCT Electronic GmbH
 *
 * Licensed under the GPL-2 or later.
 */
```

```
./board/bct-brettl2/gpio_cfi_flash.c
```

```
gpio_cfi_flash.c
```

```
No license information found.
```

```
./board/bct-brettl2/smsc9303.c
```

```
smsc9303.c
```

```
/*
 * smsc9303.c - routines to initialize SMSC 9303 switch
 *
 * Copyright (c) 2010 BCT Electronic GmbH
 */
```

```
* Licensed under the GPL-2 or later.
*/

./board/bct-brett12/smsc9303.h
smsc9303.h
/*
 * smsc9303.h - routines to initialize SMSC 9303 switch
 *
 * Copyright (c) 2010 BCT Electronic GmbH
 *
 * Licensed under the GPL-2 or later.
 */

./board/bf506f-ezkit/bf506f-ezkit.c
bf506f-ezkit.c
/*
 * U-boot - main board file
 *
 * Copyright (c) 2008-2010 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */

./board/bf518f-ezbrd/bf518f-ezbrd.c
bf518f-ezbrd.c
/*
 * U-boot - main board file
 *
 * Copyright (c) 2008-2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */

./board/bf525-ucr2/bf525-ucr2.c
bf525-ucr2.c
/* U-boot - bf525-ucr2.c board specific routines
 *
 * (C) Copyright 2000-2004
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
 * Licensed under the GPL-2 or later.
 */

./board/bf526-ezbrd/bf526-ezbrd.c
bf526-ezbrd.c
/*
 * U-boot - main board file
 *
 * Copyright (c) 2005-2009 Analog Devices Inc.
 *
```

```
* Licensed under the GPL-2 or later.  
*/
```

```
./board/bf527-ad7160-eval/bf527-ad7160-eval.c  
bf527-ad7160-eval.c
```

```
/*  
* U-boot - main board file  
*  
* Copyright (c) 2010 Analog Devices Inc.  
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*/
```

```
./board/bf527-ezkit/bf527-ezkit.c  
bf527-ezkit.c
```

```
/*  
* U-boot - main board file  
*  
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```

```
./board/bf527-ezkit/video.c  
video.c
```

```
/*  
* video.c - run splash screen on lcd  
*  
* Copyright (c) 2007-2008 Analog Devices Inc.  
*  
* Licensed under the GPL-2 or later.  
*/
```

```
./board/bf527-sdp/bf527-sdp.c  
bf527-sdp.c
```

```
/*  
* U-boot - main board file  
*  
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*/
```

```
./board/bf533-ezkit/bf533-ezkit.c  
bf533-ezkit.c
```

```
/*  
* U-boot - main board file  
*  
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*/
```

```
./board/bf533-ezkit/flash-defines.h
flash-defines.h
```

```
/*
* U-boot - flash-defines.h
*
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*/
```

./board/bf533-ezkit/flash.c

flash.c

/*

* U-boot - flash.c Flash driver for PSD4256GV

*

* Copyright (c) 2005-2007 Analog Devices Inc.

* This file is based on BF533EzFlash.c originally written by Analog Devices, Inc.

*

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* MA 02110-1301 USA

*/

./board/bf533-ezkit/psd4256.h

psd4256.h

/*

* U-boot - psd4256.h

*

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* MA 02110-1301 USA
*/
```

```
./board/bf533-stamp/bf533-stamp.c
```

```
bf533-stamp.c
```

```
/*
* U-boot - main board file
*
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*
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* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./board/bf533-stamp/ide-cf.c
```

```
ide-cf.c
```

```
/*
* CF IDE addon card code
*
* Enter bugs at http://blackfin.uclinux.org/
*
* Copyright (c) 2005-2009 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/
```

./board/bf533-stamp/video.c
video.c

```
/*
 * BF533-STAMP splash driver
 *
 * Copyright (c) 2006-2008 Analog Devices Inc.
 * (C) Copyright 2000
 * Paolo Scaffardi, AIRVENT SAM s.p.a - RIMINI(ITALY), arsenio@tin.it
 * (C) Copyright 2002
 * Wolfgang Denk, wd@denx.de
 *
 * Licensed under the GPL-2 or later.
 */
```

./board/bf533-stamp/video.h
video.h
No license information found.

./board/bf537-minotaur/bf537-minotaur.c
bf537-minotaur.c

```
/*
 * U-boot - main board file
 *
 * Copyright (c) 2005-2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */
```

./board/bf537-pnav/bf537-pnav.c
bf537-pnav.c

```
/*
 * U-boot - main board file
 *
 * Copyright (c) 2005-2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */
```

./board/bf537-srv1/bf537-srv1.c
bf537-srv1.c

```
/*
 * U-boot - main board file
 *
 * Copyright (c) 2005-2009 Analog Devices Inc.
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 */
```

./board/bf537-stamp/bf537-stamp.c

bf537-stamp.c

```
/*
 * U-boot - main board file
 *
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 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
 * MA 02110-1301 USA
 */
```

./board/bf537-stamp/ide-cf.c

ide-cf.c

```
/*
 * CF IDE addon card code
 *
 * Enter bugs at http://blackfin.uclinux.org/
 *
 * Copyright (c) 2005-2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */
```

./board/bf537-stamp/post-memory.c

post-memory.c

No license information found.

./board/bf538f-ezkit/bf538f-ezkit.c

bf538f-ezkit.c

```
/*
 * U-boot - main board file
 *
 * Copyright (c) 2008 Analog Devices Inc.
```

```
*
* Licensed under the GPL-2 or later.
*/

./board/bf548-ezkit/bf548-ezkit.c
bf548-ezkit.c
/*
* U-boot - main board file
*
* Copyright (c) 2005-2008 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/

./board/bf548-ezkit/video.c
video.c
/*
* video.c - run splash screen on lcd
*
* Copyright (c) 2007-2008 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/

./board/bf561-acvilon/bf561-acvilon.c
bf561-acvilon.c
/*
* File:          board/bf561-acvilon/bf561-acvilon.c
* Based on:      board/bf561-ezkit/bf561-ezkit.c
* Author:
*
* Created:       2009-06-23
* Description:   Acvilon System On Module board file
*
* Modified:
*               Copyright 2009 CJSC "NII STT", http://www.niistt.ru/
*               Copyright (c) 2005-2008 Analog Devices Inc.
*
*               (C) Copyright 2000-2004
*               Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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*/
```

```
./board/bf561-ezkit/bf561-ezkit.c
bf561-ezkit.c
```

```
/*
* U-boot - main board file
*
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*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./board/blackstamp/blackstamp.c
blackstamp.c
```

```
/*
* U-boot - blackstamp.c BlackStamp board specific routines
* Most code stolen from boards/bf533-stamp/bf533-stamp.c
* Edited to the BlackStamp by Ben Matthews for UR LLE
*
* Copyright (c) 2005-2009 Analog Devices Inc.
*
* (C) Copyright 2000-2004
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
*/
```

```
* Licensed under the GPL-2 or later.  
*/
```

```
./board/blackvme/blackvme.c  
blackvme.c
```

```
/* U-boot - blackvme.c board specific routines  
* (c) Wojtek Skulski 2010 info@skutek.com  
* Board info: http://www.skutek.com  
* Copyright (c) 2005-2009 Analog Devices Inc.  
*  
* (C) Copyright 2000-2004  
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
* Licensed under the GPL-2 or later.  
*/
```

```
./board/bluegiga/apx4devkit/apx4devkit.c  
apx4devkit.c
```

```
/*  
* Bluegiga APX4 Development Kit  
*  
* Copyright (C) 2012 Bluegiga Technologies Oy  
*  
* Authors:  
* Veli-Pekka Peltola <veli-pekka.peltola@bluegiga.com>  
* Lauri Hintsala <lauri.hintsala@bluegiga.com>  
*  
* Based on m28evk.c:  
* Copyright (C) 2011 Marek Vasut <marek.vasut@gmail.com>  
* on behalf of DENX Software Engineering GmbH  
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*/
```

```
./board/bluegiga/apx4devkit/spl_boot.c  
spl_boot.c
```

```
/*  
* Bluegiga APX4 Development Kit  
*  
*/
```

```
* Copyright (C) 2012 Bluegiga Technologies Oy
*
* Authors:
* Veli-Pekka Peltola <veli-pekka.peltola@bluegiga.com>
* Lauri Hintsala <lauri.hintsala@bluegiga.com>
*
* Based on spl_boot.c:
* Copyright (C) 2011 Marek Vasut <marek.vasut@gmail.com>
* on behalf of DENX Software Engineering GmbH
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*/
```

```
./board/bluewater/snapper9260/snapper9260.c
snapper9260.c
```

```
/*
* Bluewater Systems Snapper 9260/9G20 modules
*
* (C) Copyright 2011 Bluewater Systems
* Author: Andre Renaud <andre@bluewatersys.com>
* Author: Ryan Mallon <ryan@bluewatersys.com>
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./board/br4/br4.c
```

br4.c

```
/*  
 * U-boot - main board file  
 *  
 * Copyright (c) Switchfin Org. <dpn@switchfin.org>  
 *  
 * Copyright (c) 2005-2008 Analog Devices Inc.  
 *  
 * (C) Copyright 2000-2004  
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
 *  
 * Licensed under the GPL-2 or later.  
 */
```

./board/buffalo/lsx1/lsx1.c

lsx1.c

```
/*  
 * Copyright (c) 2012 Michael Walle  
 * Michael Walle <michael@walle.cc>  
 *  
 * Based on sheevaplug/sheevaplug.c by  
 * Marvell Semiconductor <www.marvell.com>  
 *  
 * See file CREDITS for list of people who contributed to this  
 * project.  
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 * along with this program; if not, write to the Free Software  
 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,  
 * MA 02110-1301 USA  
 */
```

./board/buffalo/lsx1/lsx1.h

lsx1.h

```
/*  
 * Copyright (c) 2012 Michael Walle  
 * Michael Walle <michael@walle.cc>  
 *  
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* MA 02110-1301 USA
*/
```

```
./board/BuS/eb_cpu5282/eb_cpu5282.c
eb_cpu5282.c
```

```
/*
* (C) Copyright 2005-2009
* BuS Elektronik GmbH & Co.KG <esw@bus-elektronik.de>
*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./board/BuS/eb_cpux9k2/cpux9k2.c
cpux9k2.c
```

```
/*
* (C) Copyright 2008-2009
* BuS Elektronik GmbH & Co. KG <www.bus-elektronik.de>
```

```
* Jens Scharsig <esw@bus-elektronik.de>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./board/BuS/v1_ma2sc/v1_ma2sc.c
```

```
v1_ma2sc.c
```

```
/*
* (C) Copyright 2009-2012
* Jens Scharsig <esw@bus-elektronik.de>
* BuS Elektronik GmbH & Co. KG
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./board/calao/sbc35_a9g20/sbc35_a9g20.c
```

```
sbc35_a9g20.c
```

```
/*
```

```
* (C) Copyright 2007-2008
* Stelian Pop <stelian@popies.net>
* Lead Tech Design <www.leadtechdesign.com>
*
* Copyright (C) 2009
* Albin Tonnerre, Free-Electrons <albin.tonnerre@free-electrons.com>
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*/
```

```
./board/calao/sbc35_a9g20/spi.c
```

```
spi.c
```

```
/*
```

```
* Copyright (C) 2009
* Albin Tonnerre, Free Electrons <albin.tonnerre@free-electrons.com>
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* MA 02111-1307 USA
*/
```

```
./board/calao/tny_a9260/spi.c
```

```
spi.c
```

```
/*
```

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```

```
*
```

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*
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```

```
*/
```

```
./board/calao/tny_a9260/tny_a9260.c
```

```
tny_a9260.c
```

```
/*
```

```
* (C) Copyright 2007-2008
```

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* Stelian Pop <stelian@popies.net>
```

```
* Lead Tech Design <www.leadtechdesign.com>
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```

```
./board/canmb/canmb.c
```

```
canmb.c
```

```
/*
```

```
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*
* (C) Copyright 2004
* Mark Jonas, Freescale Semiconductor, mark.jonas@motorola.com.
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* MA 02111-1307 USA
*/
```

```
./board/canmb/mt48lc16m32s2-75.h
```

```
mt48lc16m32s2-75.h
```

```
/*
```

```
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```

```
./board/CarMediaLab/flea3/flea3.c
```

```
flea3.c
```

```
/*
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 *
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 * Copyright (C) 2011, Stefano Babic <sbabic@denx.de>
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 * MA 02111-1307 USA
*/
```

```
./board/cloudengines/pogo_e02/pogo_e02.c
```

```
pogo_e02.c
```

```
/*
 * Copyright (C) 2012
 * David Purdy <david.c.purdy@gmail.com>
 *
 * Based on Kirkwood support:
 * (C) Copyright 2009
 * Marvell Semiconductor <www.marvell.com>
 * Written-by: Prafulla Wadaskar <prafulla@marvell.com>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
```

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*/
```

```
./board/cloudengines/pogo_e02/pogo_e02.h
pogo_e02.h
```

```
/*
* Copyright (C) 2012
* David Purdy <david.c.purdy@gmail.com>
*
* Based on Kirkwood support:
* (C) Copyright 2009
* Marvell Semiconductor <www.marvell.com>
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
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*/
```

```
./board/cm-bf527/cm-bf527.c
cm-bf527.c
```

```
/*
* U-boot - main board file
*
* Copyright (c) 2005-2009 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
```

```
*/

./board/cm-bf527/gpio_cfi_flash.c
gpio_cfi_flash.c
No license information found.

./board/cm-bf533/cm-bf533.c
cm-bf533.c
/*
 * U-boot - main board file
 *
 * Copyright (c) 2005-2008 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */

./board/cm-bf537e/cm-bf537e.c
cm-bf537e.c
/*
 * U-boot - main board file
 *
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 */

./board/cm-bf537e/gpio_cfi_flash.c
gpio_cfi_flash.c
/*
 * gpio_cfi_flash.c - GPIO-assisted Flash Chip Support
 *
 * Copyright (c) 2009-2010 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */

./board/cm-bf537e/gpio_cfi_flash.h
gpio_cfi_flash.h
/*
 * gpio_cfi_flash.c - GPIO-assisted Flash Chip Support
 *
 * Copyright (c) 2009 Analog Devices Inc.
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 * Licensed under the GPL-2 or later.
 */

./board/cm-bf537u/cm-bf537u.c
cm-bf537u.c
/*
 * U-boot - main board file
```

```
*
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*/

./board/cm-bf537u/gpio_cfi_flash.c
gpio_cfi_flash.c
No license information found.

./board/cm-bf548/cm-bf548.c
cm-bf548.c
/*
* U-boot - main board file
*
* Copyright (c) 2005-2008 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/

./board/cm-bf548/video.c
video.c
/*
* video.c - run splash screen on lcd
*
* Copyright (c) 2007-2008 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/

./board/cm-bf561/cm-bf561.c
cm-bf561.c
/*
* U-boot - main board file
*
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*/

./board/cm4008/cm4008.c
cm4008.c
/*
* (C) Copyright 2005
* Greg Ungerer, OpenGear Inc, <greg.ungerer@opengear.com>
*
* (C) Copyright 2002
* Kyle Harris, Nexus Technologies, Inc. kharris@nexus-tech.net
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* Marius Groeger <mgroeger@sysgo.de>
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* MA 02111-1307 USA
*/
```

```
./board/cm4008/flash.c
```

```
flash.c
```

```
/*
* (C) Copyright 2005
* Greg Ungerer, OpenGear Inc, greg.ungerer@opengear.com
*
* (C) Copyright 2001
* Kyle Harris, Nexus Technologies, Inc. kharris@nexus-tech.net
*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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```

* MA 02111-1307 USA

*/

./board/cm41xx/cm41xx.c

cm41xx.c

/*

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*

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*/

./board/cm41xx/flash.c

flash.c

/*

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*/
```

```
./board/cm5200/cm5200.c
```

```
cm5200.c
```

```
/*
```

```
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```

```
/*
```

```
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* Mark Jonas, Freescale Semiconductor, mark.jonas@motorola.com.
```

```
/*
```

```
* (C) Copyright 2004-2005
* Martin Krause, TQ-Systems GmbH, martin.krause@tqs.de
```

```
/*
```

```
* Adapted to U-Boot 1.2 by:
* Bartlomiej Sieka <tur@semihalf.com>:
*   - HW ID readout from EEPROM
*   - module detection
* Grzegorz Bernacki <gjb@semihalf.com>:
*   - run-time SDRAM controller configuration
*   - LIBFDT support
```

```
/*
```

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```

```
/*
```

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```
/*
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```

```
./board/cm5200/cm5200.h
```

```
cm5200.h
```

```
/*
```

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```

```
*
```

```
* Author: Bartlomiej Sieka <tur@semihalf.com>
```

```
* Author: Grzegorz Bernacki <gjb@semihalf.com>
```

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```

```
*/
```

```
./board/cm5200/cmd_cm5200.c
```

```
cmd_cm5200.c
```

```
/*
```

```
* (C) Copyright 2007 Markus Kappeler <markus.kappeler@objectxp.com>
```

```
*
```

```
* Adapted for U-Boot 1.2 by Piotr Kruszynski <ppk@semihalf.com>
```

```
*
```

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```

```
./board/cm5200/fwupdate.c
```

```
fwupdate.c
```

```
/*
```

```
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*
* Author: Michel Marti <mma@objectxp.com>
* Adapted for U-Boot 1.2 by Piotr Kruszynski <ppek@semihalf.com>:
*   - code clean-up
*   - bugfix for overwriting bootargs by user
*
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* project.
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*/
```

```
./board/cm5200/fwupdate.h
```

```
fwupdate.h
```

```
/*
```

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*/
```

```
./board/cmi/cmi.c
```

```
cmi.c
```

```
/*
```

```
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```

```
./board/cmi/flash.c
```

```
flash.c
```

```
/*
```

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*/
```

```
./board/cm_t35/cm_t35.c
```

```
cm_t35.c
```

```
/*
```

```
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```

```
*
```

```
* Authors: Mike Rapoport <mike@compulab.co.il>
```

```
*         Igor Grinberg <grinberg@compulab.co.il>
```

```
*
```

```
* Derived from omap3evm and Beagle Board by
```

```
*     Manikandan Pillai <mani.pillai@ti.com>
```

```
*     Richard Woodruff <r-woodruff2@ti.com>
```

```
*     Syed Mohammed Khasim <x0khasim@ti.com>
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```
*/
```

```
./board/cm_t35/eeeprom.c
```

```
eeeprom.c
```

```
/*
```

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```
*
```

```
* Authors: Nikita Kiryanov <nikita@compulab.co.il>
```

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*         Igor Grinberg <grinberg@compulab.co.il>
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*/
```

```
./board/cm_t35/eprom.h
eprom.h
```

```
/*
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*
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*          Igor Grinberg <grinberg@compulab.co.il>
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```

```
./board/cm_t35/leds.c
leds.c
```

```
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```

```
./board/cobra5272/cobra5272.c
```

```
cobra5272.c
```

```
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```

```
./board/cobra5272/flash.c
```

```
flash.c
```

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*/
```

```
./board/cogent/dipsw.c  
dipsw.c  
No license information found.
```

```
./board/cogent/dipsw.h  
dipsw.h  
No license information found.
```

```
./board/cogent/flash.c  
flash.c  
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*/
```

```
./board/cogent/flash.h  
flash.h  
/***** DEFINES for Intel 28F008S5 FLASH chip *****/  
/* register addresses, valid only following a I8S5_CMD_RD_ID command */  
/* Commands */
```

```

./board/cogent/kbm.c
kbm.c
/* keyboard/mouse not implemented yet */

./board/cogent/kbm.h
kbm.h
/* keyboard/mouse not implemented yet */
/***** DEFINES for H8542B Keyboard/Mouse Controller *****/
/*
 * note the auxillary port is used to control the mouse
 */
/* 8542B Commands (Sent to the Command Port) */

./board/cogent/lcd.c
lcd.c
/* most of this is taken from the file */
/* hal/powerpc/cogent/current/src/hal_diag.c in the */
/* Cygnus eCos source. Here is the copyright notice: */
/* */
/*===== */
/* */
/*     hal_diag.c */
/* */
/*     HAL diagnostic output code */
/* */
/*===== */
/*####COPYRIGHTBEGIN#### */
/* */
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/* ----- */
/* */
/*####COPYRIGHTEND#### */
/*===== */
/*#####DESCRIPTIONBEGIN#### */
/* */

```

```

/* Author(s):    nickg, jskov */
/* Contributors: nickg, jskov */
/* Date:        1999-03-23 */
/* Purpose:     HAL diagnostic output */
/* Description:  Implementations of HAL diagnostic output support. */
/* */
/#####DESCRIPTIONEND#### */
/* */
/*===== */

./board/cogent/lcd.h
lcd.h
/* most of this is taken from the file */
/* hal/powerpc/cogent/current/src/hal_diag.c in the */
/* Cygnus eCos source. Here is the copyright notice: */
/* */
/*===== */
/* */
/*     hal_diag.c */
/* */
/*     HAL diagnostic output code */
/* */
/*===== */
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/* */
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/* by Cygnus are Copyright (C) 1998,1999 Cygnus Solutions. All Rights Reserved. */
/* ----- */
/* */
/#####COPYRIGHTEND#### */
/*===== */
/#####DESCRIPTIONBEGIN#### */
/* */
/* Author(s):    nickg, jskov */
/* Contributors: nickg, jskov */
/* Date:        1999-03-23 */

```

```
/* Purpose:      HAL diagnostic output */
/* Description:  Implementations of HAL diagnostic output support. */
/* */
/*#####DESCRIPTIONEND##### */
/* */
/*===== */
```

```
./board/cogent/mb.c
```

```
mb.c
```

```
/*
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```

```
./board/cogent/mb.h
```

```
mb.h
```

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*/
```

```
./board/cogent/par.c
par.c
/* parallel not implemented yet */
```

```
./board/cogent/par.h
par.h
/* parallel not implemented yet */
```

```
./board/cogent/pci.c
pci.c
/* pci not implemented yet */
```

```
./board/cogent/pci.h
pci.h
/* pci not implemented yet */
```

```
./board/cogent/rtc.c
rtc.c
/* rtc not implemented yet */
```

```
./board/cogent/rtc.h
rtc.h
/* rtc not implemented yet */
```

```
./board/cogent/serial.c
serial.c
/*
* Simple serial driver for Cogent motherboard serial ports
* for use during boot
*/
```

```
./board/cogent/serial.h
serial.h
/* Line Status Register bits */
```

```
./board/comelit/dig297/dig297.c
dig297.c
/*
* (C) Copyright 2011 Comelit Group SpA
* Luca Ceresoli <luca.ceresoli@comelit.it>
*
* Based on board/ti/beagle/beagle.c:
* (C) Copyright 2004-2008
```

```
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* Author :
*   Sunil Kumar <sunilsaini05@gmail.com>
*   Shashi Ranjan <shashiranjanmca05@gmail.com>
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```

```
./board/comelit/dig297/dig297.h
```

```
dig297.h
```

```
/*
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* Luca Ceresoli <luca.ceresoli@comelit.it>
*
* Based on board/ti/beagle/beagle.h:
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```
./board/compal/paz00/paz00.c
paz00.c
```

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```
./board/compulab/trimslice/trimslice.c
trimslice.c
```

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```
*/  
  
./board/corscience/tricorder/tricorder.c  
tricorder.c  
/*  
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```

```
./board/corscience/tricorder/tricorder.h  
tricorder.h  
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```
./board/cpc45/cpc45.c
cpc45.c
```

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```

```
./board/cpc45/flash.c
flash.c
```

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```
./board/cpc45/ide.c
ide.c
```

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```
./board/cpc45/pd67290.c
pd67290.c
```

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* i82365.c 1.352 - Linux driver for Intel 82365 and compatible
* PC Card controllers, and Yenta-compatible PCI-to-CardBus controllers.
* (C) 1999 David A. Hinds <dahinds@users.sourceforge.net>
*/

./board/cpc45/plx9030.c
plx9030.c

/* Plx9030.c - system configuration module for PLX9030 PCI to Local Bus Bridge */
/*

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./board/cpu86/cpu86.c

cpu86.c

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*/

./board/cpu86/cpu86.h

cpu86.h

/* CPU86 register definitions */

./board/cpu86/flash.c

flash.c

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```
./board/cpu87/cpu87.c
```

```
cpu87.c
```

```
/*
```

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```
./board/cpu87/cpu87.h
```

```
cpu87.h
```

```
/* CPU86 register definitions */
```

```
./board/cpu87/flash.c
```

```
flash.c
```

```
/*
```

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```
./board/cray/L1/flash.c
flash.c
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```
./board/cray/L1/L1.c
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```

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```
./board/cray/L1/L1.h
L1.h
```

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```
./board/csb272/csb272.c
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```

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```
./board/csb472/csb472.c
csb472.c
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```
./board/cu824/cu824.c
cu824.c
```

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```
./board/cu824/flash.c
```

```
flash.c
```

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```
./board/d-link/dns325/dns325.c
```

```
dns325.c
```

```
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*
*/
```

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```
./board/d-link/dns325/dns325.h
```

```
dns325.h
```

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./board/dave/common/flash.c
flash.c

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./board/dave/common/fpga.c
fpga.c

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```
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```
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```
./board/dave/PPChameleonEVB/flash.c
```

```
flash.c
```

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*/

./board/dave/PPChameleonEVB/fpgadata.c
fpgadata.c
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./board/dave/PPChameleonEVB/nand.c
nand.c

```
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 */
```

./board/dave/PPChameleonEVB/PPChameleonEVB.c
PPChameleonEVB.c

```
/*
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 * http://www.dave-tech.it
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*/
```

```
./board/davedenx/aria/aria.c
```

```
aria.c
```

```
/*
```

```
* (C) Copyright 2009 Wolfgang Denk <wd@denx.de>
```

```
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```
/*
```

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/*
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```
/*
```

```
*/
```

```
./board/davedenx/qong/fpga.c
```

```
fpga.c
```

```
/*
```

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*
*/
```

```
./board/davedenx/qong/qong.c
```

```
qong.c
```

```
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*/
```

```
./board/davedenx/qong/qong_fpga.h
```

```
qong_fpga.h
```

```
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*/

./board/davinci/da8xxevm/da830evm.c
da830evm.c
/*
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*/

./board/davinci/da8xxevm/da850evm.c
```

da850evm.c

```
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 */
```

./board/davinci/da8xxevm/hawkboard.c

hawkboard.c

```
/*
 * Modified for Hawkboard - Syed Mohammed Khasim <khasim@beagleboard.org>
 *
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 */
```

```
./board/davinci/dm355evm/dm355evm.c
dm355evm.c
```

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 */
```

```
./board/davinci/dm355leopard/dm355leopard.c
dm355leopard.c
```

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```
./board/davinci/dm365evm/dm365evm.c
dm365evm.c
```

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```

```
./board/davinci/dm6467evm/dm6467evm.c
dm6467evm.c
```

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```
./board/davinci/dvevm/dvevm.c
dvevm.c
```

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```

```
./board/davinci/ea20/ea20.c
ea20.c
```

```
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```
./board/davinci/schmoogie/schmoogie.c
schmoogie.c
```

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./board/davinci/sffsdr/sffsdr.c
sffsdr.c
```

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```
./board/davinci/sonata/sonata.c
sonata.c
```

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 */
```

```
./board/dbau1x00/dbau1x00.c
dbau1x00.c
```

```
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```

```
./board/denx/m28evk/m28evk.c  
m28evk.c
```

```
/*  
 * DENX M28 module  
 *  
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 */
```

```
./board/denx/m28evk/spl_boot.c  
spl_boot.c
```

```
/*  
 * DENX M28 Boot setup  
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```

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*/
```

```
./board/dnp5370/dnp5370.c
```

```
dnp5370.c
```

```
/*
```

```
* U-boot - main board file
```

```
*
```

```
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*
```

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```
*/
```

```
./board/dvlhost/dvlhost.c
```

```
dvlhost.c
```

```
/*
```

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*/
```

```
./board/dvlhost/dvlhost_hw.h
```

```
dvlhost_hw.h
```

```
/*  
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*  
* hardware register definitions for the  
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```

```
./board/dvlhost/watchdog.c
```

```
watchdog.c
```

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```

```
./board/earthlcd/favr-32-ezkit/favr-32-ezkit.c
favr-32-ezkit.c
```

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*/
```

```
./board/earthlcd/favr-32-ezkit/flash.c
flash.c
```

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```

```
./board/egnite/ethernut5/ethernut5.c
ethernut5.c
```

```
/*
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*
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```

```
./board/egnite/ethernut5/ethernut5_pwrman.c
ethernut5_pwrman.c
```

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*/
```

```
./board/egnite/ethernut5/ethernut5_pwrman.h
```

```
ethernut5_pwrman.h
```

```
/*
```

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```
*/
```

```
./board/eltec/elppc/eeepro100_srom.c
```

```
eeepro100_srom.c
```

```
/*
```

```
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```

```
./board/eltec/elppc/elppc.c
```

```
elppc.c
```

```
/*
```

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```

```
*/
```

```
./board/eltec/elppc/flash.c
```

```
flash.c
```

```
/*
```

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```

```
./board/eltec/elppc/misc.c
```

```
misc.c
```

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```

```
./board/eltec/elppc/mpc107_i2c.c
```

```
mpc107_i2c.c
```

```
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```

```
./board/eltec/elppc/pci.c
```

```
pci.c
```

```
/*
```

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```

```
*/
```

```
./board/eltec/elppc/srom.h
```

```
srom.h
```

```
/*
```

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```

```
./board/eltec/mhpc/flash.c
```

```
flash.c
```

```
/*
```

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```

```
./board/eltec/mhpc/mhpc.c
```

```
mhpc.c
```

```
/*
```

```
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* Frank Gottschling <fgottschling@eltec.de>  
*  
* Board specific routines for the miniHiPerCam  
*  
* - initialisation (eeprom)  
* - memory controller  
* - serial io initialisation  
* - ethernet io initialisation  
*  
*/
```

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```

```
./board/emk/common/am79c874.c  
am79c874.c
```

```
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```

```
./board/emk/common/flash.c  
flash.c
```

```
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```

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```
./board/emk/common/vpd.c
```

```
vpd.c
```

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```
./board/emk/top5200/top5200.c
```

```
top5200.c
```

```
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```

```
./board/emk/top860/top860.c
top860.c
```

```
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 * Reinhard Meyer <r.meyer@emk-elektronik.de>
 *
 * Board specific routines for the TOP860
 *
 * - initialisation
 * - interface to VPD data (mac address, clock speeds)
 * - memory controller
 * - serial io initialisation
 * - ethernet io initialisation
 *
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```

```
./board/emk/top9000/spi.c
```

```
spi.c
```

```
/*
```

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```

```
./board/emk/top9000/top9000.c
```

```
top9000.c
```

```
/*
```

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```
./board/enbw/enbw_cmc/enbw_cmc.c
enbw_cmc.c
```

```
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```

```
./board/eNET/eNET.c
eNET.c
```

```
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```

```
./board/eNET/eNET_pci.c
```

```
eNET_pci.c
```

```
/*
```

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```
*/
```

```
./board/eNET/hardware.h
```

```
hardware.h
```

```
/*
```

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```

```
./board/ep8248/ep8248.c
```

```
ep8248.c
```

```
/*
```

```
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```

./board/ep8260/ep8260.c

ep8260.c

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./board/ep8260/ep8260.h

ep8260.h

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./board/ep8260/flash.c

flash.c

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* Flash Routines for AMD device AM29DL323DB on the EP8260 board.

*

* This file is based on board/tqm8260/flash.c.

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```
./board/ep8260/mii_phy.c
mii_phy.c
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```

```
./board/ep82xxm/ep82xxm.c
ep82xxm.c
/*
```

```
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*
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```
./board/ep88x/ep88x.c
ep88x.c
/*
```

```
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* Yuli Barcohen <yuli@arabellasw.com>
```

```
*
* Support for Embedded Planet EP88x boards.
* Tested on EP88xC with MPC885 CPU, 64MB SDRAM and 16MB flash.
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```
./board/esd/adciop/adciop.c
```

```
adciop.c
```

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```
./board/esd/adciop/adciop.h
```

```
adciop.h
```

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```

```
./board/esd/adciop/flash.c
flash.c
```

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```
./board/esd/apc405/apc405.c
apc405.c
```

```
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 */
```

```
./board/esd/apc405/fpgadata.c
fpgadata.c
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```

```
./board/esd/apc405/logo_640_480_24bpp.c
logo_640_480_24bpp.c
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```

```
./board/esd/ar405/ar405.c
ar405.c
```

```
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```

```
./board/esd/ar405/ar405.h
ar405.h
```

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```
./board/esd/ar405/flash.c
flash.c
```

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*/
```

```
./board/esd/ar405/fpgadata.c
fpgadata.c
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```

```
./board/esd/ar405/fpgadata_xl30.c
fpgadata_xl30.c
No license information found.
```

```
./board/esd/ash405/ash405.c
ash405.c
```

```
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```
./board/esd/ash405/flash.c
flash.c
```

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*/
```

```
./board/esd/ash405/fpgadata.c
fpgadata.c
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```
./board/esd/canbt/canbt.c
canbt.c
```

```
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```

```
./board/esd/canbt/canbt.h
canbt.h
```

```
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```

```
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```

```
flash.c
```

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```

```
./board/esd/canbt/fpgadata.c
```

```
fpgadata.c
```

```
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```

./board/esd/cms700/cms700.c

cms700.c

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./board/esd/cms700/flash.c

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*/

./board/esd/common/auto_update.c

auto_update.c

```
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./board/esd/common/auto_update.h

auto_update.h

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```
./board/esd/common/cmd_loadpci.c  
cmd_loadpci.c
```

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```

```
./board/esd/common/esd405ep_nand.c  
esd405ep_nand.c
```

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```
./board/esd/common/flash.c  
flash.c
```

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```
./board/esd/common/fpga.c  
fpga.c
```

```
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```
*/  
  
./board/esd/common/lcd.c  
lcd.c  
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```

```
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lcd.h  
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```
./board/esd/common/misc.c
```

```
misc.c
```

```
/*
```

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```

```
*/
```

```
./board/esd/common/pci.c
```

```
pci.c
```

```
/*
```

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```

```
./board/esd/common/s1d13505_640_480_16bpp.h
s1d13505_640_480_16bpp.h
```

```
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*/
```

```
./board/esd/common/s1d13704_320_240_4bpp.h
s1d13704_320_240_4bpp.h
```

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```

```
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* MA 02111-1307 USA  
*  
* Generic Header information generated by 13704CFG.EXE (Build 10)  
* Panel: 320x240x4bpp 78Hz Mono 4-Bit STN, Disabled (PCLK=6.666MHz)  
*/
```

```
./board/esd/common/s1d13705_320_240_8bpp.h  
s1d13705_320_240_8bpp.h
```

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*  
* Generic Header information generated by 13704CFG.EXE (Build 10)  
* Panel: 320x240x8bpp 78Hz Mono 8-Bit STN, Disabled (PCLK=6.666MHz)  
*/
```

```
./board/esd/common/s1d13806_1024_768_8bpp.h  
s1d13806_1024_768_8bpp.h
```

```
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*
* File generated by S1D13806CFG.EXE
* Panel: (active) 1024x768 34Hz TFT Single 12-bit (PCLK=BUSCLK=33.333MHz)
* Memory: Embedded SDRAM (MCLK=CLKI=49.100MHz) (BUSCLK=33.333MHz)
*/
```

```
./board/esd/common/s1d13806_320_240_4bpp.h
s1d13806_320_240_4bpp.h
```

```
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*
* File generated by S1D13806CFG.EXE
* Panel: (active) 320x240 62Hz STN Single 4-bit (PCLK=CLKI2/4=6.250MHz)
* Memory: Embedded SDRAM (MCLK=CLKI=49.500MHz) (BUSCLK=33.333MHz)
*/
```

```
./board/esd/common/s1d13806_640_480_16bpp.h
s1d13806_640_480_16bpp.h
```

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* MA 02111-1307 USA
*
* File generated by S1D13806CFG.EXE
* Panel: (active) 640x480 59Hz TFT Single 18-bit (PCLK=CLKI2=25.000MHz)
* Memory: Embedded SDRAM (MCLK=CLKI=49.152MHz) (BUSCLK=33.333MHz)
*/
```

```
./board/esd/common/s1d13806_640_480_8bpp.h
s1d13806_640_480_8bpp.h
```

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*
* File generated by S1D13806CFG.EXE
* Panel: (active) 640x480 59Hz TFT Single 18-bit (PCLK=CLKI2=25.000MHz)
* Memory: Embedded SDRAM (MCLK=CLKI=49.152MHz) (BUSCLK=33.333MHz)
*/
```

```
./board/esd/common/xilinx_jtag/lenal.c
lenal.c
```

```
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```

```
./board/esd/common/xilinx_jtag/lenal.h
```

```
lenal.h
```

```
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```

```
./board/esd/common/xilinx_jtag/micro.c
```

```
micro.c
```

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*/
```

```
./board/esd/common/xilinx_jtag/micro.h
```

```
micro.h
```

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```
./board/esd/common/xilinx_jtag/ports.c
```

```
ports.c
```

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```
./board/esd/common/xilinx_jtag/ports.h
```

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```

```
./board/esd/cpci2dp/cpci2dp.c
```

```
cpci2dp.c
```

```
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```
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flash.c
```

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*/
```

```
./board/esd/cpci405/cpci405.c
cpci405.c
```

```
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```
./board/esd/cpci405/flash.c
flash.c
```

```
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```

```
./board/esd/cpci405/fpgadata_cpci405.c
fpgadata_cpci405.c
No license information found.
```

```
./board/esd/cpci405/fpgadata_cpci4052.c
fpgadata_cpci4052.c
No license information found.
```

```
./board/esd/cpci405/fpgadata_cpci405ab.c
fpgadata_cpci405ab.c
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```

```
./board/esd/cpci5200/cpci5200.c
cpci5200.c
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```
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mt46v16m16-75.h
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```

```
./board/esd/cpci5200/strataflash.c  
strataflash.c
```

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```

```
./board/esd/cpci750/64360.h  
64360.h
```

```
/*  
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cpci750.c
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*
* modifications for the DB64360 eval board based by Ingo.Assmus@keymile.com
* modifications for the cpci750 by reinhard.arlt@esd-electronics.com
*/
```

```
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eth.h
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```
./board/esd/cpci750/i2c.c
```

```
i2c.c
```

```
/*
```

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*
* Hacked for the DB64360 board by Ingo.Assmus@keymile.com
* extra improvements by Brain Waite
* for cpci750 by reinhard.arlt@esd-electronics.com
*/
```

```
./board/esd/cpci750/i2c.h
```

```
i2c.h
```

```
/*
```

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```

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*/
```

```
./board/esd/cpci750/ide.c
```

```
ide.c
```

```
/*
```

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*/
```

```
/* ide.c - ide support functions */
```

```
./board/esd/cpci750/local.h
```

```
local.h
```

```
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```

```
./board/esd/cpci750/mpsc.c
```

```
mpsc.c
```

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 */
```

```
./board/esd/cpci750/mpsc.h
```

```
mpsc.h
```

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 */
```

```
./board/esd/cpci750/mv_eth.c
```

```
mv_eth.c
```

```
/*
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 *
 * based on - Driver for MV64360X ethernet ports
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 */
```

./board/esd/cpci750/mv_eth.h

mv_eth.h

```
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 */
```

./board/esd/cpci750/mv_regs.h

mv_regs.h

```
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```

```
./board/esd/cpci750/pci.c
```

```
pci.c
```

```
/*
```

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```

```
*/
```

```
/* PCI.c - PCI functions */
```

```
./board/esd/cpci750/sdram_init.c
```

```
sdram_init.c
```

```
/*
```

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*/
```

```
./board/esd/cpci750/serial.c
serial.c
```

```
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*/
```

```
./board/esd/cpci750/serial.h
serial.h
```

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*/
```

```
./board/esd/cpciiser4/cpciiser4.c
```

```
cpciiser4.c
```

```
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*/
```

```
./board/esd/cpciiser4/cpciiser4.h
```

```
cpciiser4.h
```

```
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```

```
./board/esd/cpciiser4/flash.c
```

```
flash.c
```

```
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*/
```

```
./board/esd/cpciiser4/fpgadata.c
```

```
fpgadata.c
```

```
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```

```
./board/esd/dasa_sim/cmd_dasa_sim.c
```

```
cmd_dasa_sim.c
```

```
/*
```

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```

```
./board/esd/dasa_sim/dasa_sim.c
dasa_sim.c
```

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```
./board/esd/dasa_sim/dasa_sim.h
dasa_sim.h
```

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```

```
./board/esd/dasa_sim/eprom.c
eprom.c
```

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```
./board/esd/dasa_sim/flash.c
```

flash.c

```
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 */
```

./board/esd/dasa_sim/fpgadata.c

fpgadata.c

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./board/esd/dp405/dp405.c

dp405.c

```
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*/

./board/esd/dp405/flash.c

flash.c

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./board/esd/du405/du405.c

du405.c

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```
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./board/esd/du405/flash.c
flash.c

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fpgadata.c
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```
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du440.h

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 */
```

./board/esd/hh405/flash.c

flash.c

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*/

./board/esd/hh405/fpgadata.c

fpgadata.c

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./board/esd/hh405/hh405.c

hh405.c

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```

```
./board/esd/hh405/logo_1024_768_8bpp.c  
logo_1024_768_8bpp.c  
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```

```
./board/esd/hh405/logo_320_240_4bpp.c  
logo_320_240_4bpp.c  
No license information found.
```

```
./board/esd/hh405/logo_320_240_8bpp.c  
logo_320_240_8bpp.c  
No license information found.
```

```
./board/esd/hh405/logo_640_480_24bpp.c  
logo_640_480_24bpp.c  
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```

```
./board/esd/hub405/flash.c  
flash.c  
/*
```

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```
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hub405.c  
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*/
```

```
./board/esd/mecp5123/mecp5123.c  
mecp5123.c  
/*  
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* (C) Copyright 2009 Dave Sr1 www.dave.eu  
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*

*/

./board/esd/mecp5200/mecp5200.c

mecp5200.c

/*

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*/

./board/esd/mecp5200/mt46v16m16-75.h

mt46v16m16-75.h

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```

```
./board/esd/meesc/meesc.c
```

```
meesc.c
```

```
/*
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* Lead Tech Design <www.leadtechdesign.com>
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```

```
./board/esd/meesc/partition.c
```

```
partition.c
```

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```

```
./board/esd/ocrtc/cmd_ocrtc.c
```

```
cmd_ocrtc.c
```

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```

```
./board/esd/ocrtc/flash.c
```

```
flash.c
```

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```

```
./board/esd/ocrtc/ocrtc.c
```

```
ocrtc.c
```

```
/*
```

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```

```
*/
```

```
./board/esd/ocrtc/ocrtc.h
```

```
ocrtc.h
```

```
/*
```

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```

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```

```
./board/esd/otc570/otc570.c
otc570.c
```

```
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```

```
./board/esd/otc570/partition.c
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```

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*/
```

```
./board/esd/pci405/cmd_pci405.c
```

```
cmd_pci405.c
```

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```
./board/esd/pci405/flash.c
```

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```

```
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pci405.h
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```
./board/esd/pf5200/mt46v16m16-75.h
mt46v16m16-75.h
```

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```
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pf5200.c
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```
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fpgadata.c
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```
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plu405.c
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```
./board/esd/pmc405/pmc405.c
```

pmc405.c

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chip_config.c

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```
*
*/

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```

```
./board/esd/pmc440/cmd_pmc440.c
cmd_pmc440.c
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*

*/

./board/esd/pmc440/fpga.c

fpga.c

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./board/esd/pmc440/fpga.h

fpga.h

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```

```
./board/esd/pmc440/pmc440.c  
pmc440.c
```

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```

```
./board/esd/pmc440/pmc440.h  
pmc440.h
```

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```

```
./board/esd/pmc440/sdram.c
sdram.c
```

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```
./board/esd/tasreg/flash.c
flash.c
```

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```
./board/esd/tasreg/fpgadata.c
fpgadata.c
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```

```
./board/esd/tasreg/tasreg.c
tasreg.c
```

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```
./board/esd/vme8349/caddy.c
caddy.c
```

```
/*
* caddy.c -- esd VME8349 support for "missing" access modes in TSI148.
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```

```
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```

```
./board/esd/vme8349/caddy.h
caddy.h
```

```
/*
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```

./board/esd/vme8349/pci.c

pci.c

/*

* pci.c -- esd VME8349 PCI board support.
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*
* Reinhard Arlt <reinhard.arlt@esd-electronics.com>
*
* Based on MPC8349 PCI support but w/o PIB related code.
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*/

./board/esd/vme8349/vme8349.c

vme8349.c

/*

* vme8349.c -- esd VME8349 board support
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*
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*
* Reinhard Arlt <reinhard.arlt@esd-electronics.com>
* Based on board/mpc8349emds/mpc8349emds.c (and previous 834x releases.)
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*/
```

```
./board/esd/vme8349/vme8349pin.h
vme8349pin.h
```

```
/*
* vme8349pin.h -- esd VME8349 MPC8349 I/O pin definition.
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```

```
./board/esd/voh405/flash.c
flash.c
```

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```
./board/esd/voh405/fpgadata.c
fpgadata.c
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```
./board/esd/voh405/logo_320_240_4bpp.c
logo_320_240_4bpp.c
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```

```
./board/esd/voh405/logo_640_480_24bpp.c
logo_640_480_24bpp.c
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```
./board/esd/voh405/voh405.c
voh405.c
/*
```

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```
./board/esd/vom405/flash.c
```

```
flash.c
```

```
/*
```

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```

```
./board/esd/vom405/vom405.c
```

```
vom405.c
```

```
/*
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```
./board/esd/wuh405/flash.c
flash.c
```

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./board/esd/wuh405/fpgadata.c
fpgadata.c
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```
./board/esd/wuh405/wuh405.c
wuh405.c
```

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./board/esg/ima3-mx53/ima3-mx53.c
```

```
ima3-mx53.c
```

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```
./board/espt/espt.c
```

```
espt.c
```

```
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```
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```

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* Modified By Conn Clark to work with Esteem 192E 7/31/00
*/
```

```
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```

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```
./board/etin/debris/debris.c
```

```
debris.c
```

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```
./board/etin/debris/flash.c
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```
flash.c
```

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```

```
./board/etin/debris/phantom.c
phantom.c
```

```
/*
* board/eva/phantom.c
*
* Phantom RTC device driver for EVA
*
* Author: Sangmoon Kim
*         dogoil@etinsys.com
*
* Copyright 2002 Etinsys Inc.
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* option) any later version.
*/
```

```
./board/etin/debris/speed.h
speed.h
```

```
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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* MA 02111-1307 USA
*/
```

```
./board/etin/kvme080/kvme080.c
```

```
kvme080.c
```

```
/*
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* Sangmoon Kim, Etin Systems. dogoil@etinsys.com.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./board/etin/kvme080/multiverse.c
```

```
multiverse.c
```

```
/*
* multiverse.c
*
* VME driver for Multiverse
*
* Author : Sangmoon Kim
*         dogoil@etinsys.com
*
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* option) any later version.
*/
```

```
./board/etin/kvme080/multiverse.h
multiverse.h
```

```
/*
 * multiverse.h
 *
 * VME driver for Multiverse
 *
 * Author : Sangmoon Kim
 *         dogoil@etinsys.com
 *
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 * option) any later version.
 */
```

```
./board/eukrea/cpu9260/cpu9260.c
cpu9260.c
```

```
/*
 * (C) Copyright 2007-2008
 * Stelian Pop <stelian@popies.net>
 * Lead Tech Design <www.leadtechdesign.com>
 * Ilko Iliev <www.ronetix.at>
 *
 * (C) Copyright 2009-2011
 * Eric Benard <eric@eukrea.com>
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./board/eukrea/cpu9260/led.c
```

led.c

```
/*
 * Copyright (c) 2009 Wind River Systems, Inc.
 * Tom Rix <Tom.Rix@windriver.com>
 * (C) Copyright 2009
 * Eric Benard <eric@eukrea.com>
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./board/eukrea/cpuat91/cpuat91.c

cpuat91.c

```
/*
 * (C) Copyright 2006-2010 Eukrea Electromatique <www.eukrea.com>
 * Eric Benard <eric@eukrea.com>
 * based on at91rm9200dk.c which is :
 * (C) Copyright 2002
 * Sysgo Real-Time Solutions, GmbH <www.elinos.com>
 * Marius Groeger <mgroeger@sysgo.de>
 *
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```

```

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*/

./board/evb64260/64260.h
64260.h
/* CPU Configuration bits */

./board/evb64260/ecctest.c
ecctest.c
    *(volatile int *) (INTERNAL_REG_BASE_ADDR + 0x4b4) &= ~0x00200000;
    *(volatile int *) (INTERNAL_REG_BASE_ADDR + 0x4b4) |= 0x00200000;

./board/evb64260/eth.c
eth.c
/*****
*****/
/*
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* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2, or (at
* your option) any later version.
*/
/*
* This file is a modified version from the Galileo polled mode
* network driver for the ethernet contained within the GT64260
* chip. It has been modified to fit into the U-Boot framework, from
* the original (etherboot) setup. Also, additional cleanup and features
* were added.
*
* - Josh Huber <huber@mclx.com>
*/

./board/evb64260/eth.h
eth.h
/*
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*/
```

```
./board/evb64260/eth_addrtbl.c
eth_addrtbl.c
No license information found.
```

```
./board/evb64260/eth_addrtbl.h
eth_addrtbl.h
/*
* -----
* addressTable.h - this file has all the declarations of the address table
*/
```

```
./board/evb64260/evb64260.c
evb64260.c
/*
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* Josh Huber <huber@mclx.com>, Mission Critical Linux, Inc.
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*/
```

```
./board/evb64260/flash.c
flash.c
/*
* (C) Copyright 2001
* Josh Huber <huber@mclx.com>, Mission Critical Linux, Inc.
```

```
*
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*/
```

```
./board/evb64260/i2c.c
i2c.c
No license information found.
```

```
./board/evb64260/i2c.h
i2c.h
/* function declarations */
```

```
./board/evb64260/intel_flash.c
intel_flash.c
/*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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```

```

* MA 02111-1307 USA
*
* Hacked for the Hymod board by Murray.Jensen@cmst.csiro.au, 20-Oct-00
*/

./board/evb64260/intel_flash.h
intel_flash.h
/***** DEFINES for Intel StrataFlash FLASH chip *****/
/*
* acceptable chips types are:
*
*      28F320J5, 28F640J5, 28F320J3A, 28F640J3A and 28F128J3A
*/
/* register addresses, valid only following an CHIP_CMD_RD_ID command */

./board/evb64260/local.h
local.h
/*
* include/local.h - local configuration options, board specific
*/
/*
* High Level Configuration Options
* (easy to change)
*/
/* This tells U-Boot that the config options are compiled in */
/* #undef ENV_IS_EMBEDDED */
/* Don't touch this! U-Boot figures this out based on other
* magic. */

./board/evb64260/memory.c
memory.c
/* Memory.c - Memory mappings and remapping functions */
/* Copyright - Galileo technology. */
/* modified by Josh Huber to clean some things up, and
* fit it into the U-Boot framework */
/*****
* memoryGetBankBaseAddress - Gets the base address of a memory bank
*      - If the memory bank size is 0 then this base address has no meaning!!!
*
*
* INPUTS:  MEMORY_BANK bank - The bank we ask for its base Address.
* OUTPUT:  N/A
* RETURNS: Memory bank base address.
*****/

./board/evb64260/mpsc.c
mpsc.c
/*
* (C) Copyright 2001
* John Clemens <clemens@mclx.com>, Mission Critical Linux, Inc.

```

```
*
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* project.
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* MA 02111-1307 USA
*/
```

```
./board/evb64260/mpsc.h
```

```
mpsc.h
```

```
/*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./board/evb64260/pci.c
```

```
pci.c
```

```
/* PCI.c - PCI functions */
/* Copyright - Galileo technology. */
```

```
./board/evb64260/sdram_init.c
```

```
sdram_init.c
```

```
/*
```

```
* (C) Copyright 2001
```

```
* Josh Huber <huber@mclx.com>, Mission Critical Linux, Inc.
```

```
*
```

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* See file CREDITS for list of people who contributed to this  
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*
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```

```
*/
```

```
./board/evb64260/serial.c
```

```
serial.c
```

```
/*
```

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```
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```

```
*/
```

```
./board/evb64260/serial.h
serial.h
/* serial.h - mostly useful for DUART serial_init in serial.c */
```

```
./board/evb64260/zuma_pbb.c
zuma_pbb.c
No license information found.
```

```
./board/evb64260/zuma_pbb.h
zuma_pbb.h
No license information found.
```

```
./board/evb64260/zuma_pbb_mbox.c
zuma_pbb_mbox.c
No license information found.
```

```
./board/evb64260/zuma_pbb_mbox.h
zuma_pbb_mbox.h
No license information found.
```

```
./board/eXalion/eXalion.c
eXalion.c
/*
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 *
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 */
```

```
./board/eXalion/eXalion.h
eXalion.h
```

```
/*
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 *
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 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
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 * James Dougherty (jfd@broadcom.com)
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 */
```

```
./board/eXalion/piix_pci.h
```

```
piix_pci.h
```

```
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*/
```

```
./board/exmeritus/hww1u1a/ddr.c
```

```
ddr.c
```

```
/*
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```

```
./board/exmeritus/hww1u1a/gpios.h
```

```
gpios.h
```

```
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```

```
./board/exmeritus/hww1u1a/hww1u1a.c
```

```
hww1u1a.c
```

```
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* MA 02111-1307 USA
*/
```

```
./board/exmeritus/hww1u1a/law.c
```

```
law.c
```

```
/*
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*/
```

```
./board/exmeritus/hww1u1a/tlb.c
```

```
tlb.c
```

```
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*/
```

```
./board/fads/fads.c
```

```
fads.c
```

```
/*
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*
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*/
```

```
./board/fads/fads.h
```

```
fads.h
```

```
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```

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*  
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* and Dan Malek  
*  
* Modified by, Yuli Barcohen, Arabella Software Ltd., yuli@arabellasw.com  
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```

```
./board/fads/flash.c  
flash.c
```

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*/

./board/fads/lamp.c
lamp.c
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./board/fads/pcmcia.c
pcmcia.c
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./board/faraday/a320evb/a320evb.c
a320evb.c
/*

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./board/flagadm/flagadm.c
flagadm.c
/*

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```
./board/flagadm/flash.c
```

```
flash.c
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./board/freescale/bsc9131rdb/bsc9131rdb.c
```

```
bsc9131rdb.c
```

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```
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ddr.c
```

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```
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tlb.c
```

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```

```
./board/freescale/common/cadmus.c  
cadmus.c
```

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*/

./board/freescale/common/cadmus.h

cadmus.h

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*/

./board/freescale/common/cds_pci_ft.c

cds_pci_ft.c

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./board/freescale/common/cds_via.c

cds_via.c

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eeprom.h

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./board/freescale/common/fman.c

fman.c

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```
./board/freescale/common/fman.h
```

```
fman.h
```

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```
./board/freescale/common/ics307_clk.c
```

```
ics307_clk.c
```

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```
./board/freescale/common/ics307_clk.h
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```

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```

```
./board/freescale/common/ngpaxis.c
ngpaxis.c
```

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```
* any later version.
*
* This file provides support for the ngPIXIS, a board-specific FPGA used on
* some Freescale reference boards.
*
* A "switch" is black rectangular block on the motherboard. It contains
* eight "bits". The ngPIXIS has a set of memory-mapped registers (SWx) that
* shadow the actual physical switches. There is also another set of
* registers (ENx) that tell the ngPIXIS which bits of SWx should actually be
* used to override the values of the bits in the physical switches.
*
* The following macros need to be defined:
*
* PIXIS_BASE - The virtual address of the base of the PIXIS register map
*
* PIXIS_LBMAP_SWITCH - The switch number (i.e. the "x" in "SWx"). This value
* is used in the PIXIS_SW() macro to determine which offset in
* the PIXIS register map corresponds to the physical switch that controls
* the boot bank.
*
* PIXIS_LBMAP_MASK - A bit mask the defines which bits in SWx to use.
*
* PIXIS_LBMAP_SHIFT - The shift value that corresponds to PIXIS_LBMAP_MASK.
*
* PIXIS_LBMAP_ALT_BANK - The value to program into SWx to tell the ngPIXIS to
* boot from the alternate bank.
*/
```

```
./board/freescale/common/ngpixmap.h
```

```
ngpixmap.h
```

```
/**
```

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```
*
```

```
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* some Freescale reference boards.
```

```
*/
```

```
./board/freescale/common/pixmap.c
```

```
pixmap.c
```

```
/*
```

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```
* Jeff Brown
```

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* Srikanth Srinivasan (srikanth.srinivasan@freescale.com)
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```
./board/freescale/common/pixis.h
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```
./board/freescale/common/pq-mds-pib.c
```

```
pq-mds-pib.c
```

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```

```
law.c
```

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*
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```

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```
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```

```
tlb.c
```

```
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```

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```

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*
```

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```
*/
```

```
./board/freescale/common/qixis.c
```

```
qixis.c
```

```
/*
```

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```

```
*/
```

```
./board/freescale/common/qixis.h
```

```
qixis.h
```

```
/*
```

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```

```
*/
```

```
./board/freescale/common/sdhc_boot.c
```

```
sdhc_boot.c
```

```
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```
*/
```

```
./board/freescale/common/sgmii_riser.c
```

```
sgmii_riser.c
```

```
/*
```

```
* Freescale SGMII Riser Card
```

```
*
* This driver supports the SGMII Riser card found on the
* "DS" style of development board from Freescale.
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*/

./board/freescale/common/sgmii_riser.h
sgmii_riser.h
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*
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./board/freescale/common/sys_eeprom.c
sys_eeprom.c
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```

```
./board/freescale/common/via.h
via.h
```

```
/* Function 1, IDE */
/* Function 2, USB ports 0-1 */
/* Function 3, USB ports 2-3 */
/* Function 5, Power Management */
```

```
./board/freescale/common/vsc3316_3308.c
vsc3316_3308.c
```

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```
./board/freescale/common/vsc3316_3308.h
vsc3316_3308.h
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./board/freescale/corenet_ds/corenet_ds.c
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corenet_ds.h
```

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*/

./board/freescale/corenet_ds/ddr.c
ddr.c

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./board/freescale/corenet_ds/eth_hydra.c
eth_hydra.c

/*

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* Author: Timur Tabi <timur@freescale.com>

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*/

./board/freescale/corenet_ds/eth_p4080.c
eth_p4080.c

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```
./board/freescale/corenet_ds/eth_superhydra.c
```

```
eth_superhydra.c
```

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```
./board/freescale/corenet_ds/p3041ds_dds.c
```

```
p3041ds_dds.c
```

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./board/freescale/corenet_ds/p4080ds_dds.c
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p4080ds_dds.c
```

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p5020ds_dds.c
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p5040ds_dds.c
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m5208evbe.c
```

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m5235evb.c

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```
./board/freescale/m5249evb/m5249evb.c
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```
./board/freescale/m5253demo/flash.c
flash.c
```

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```
./board/freescale/m5253demo/m5253demo.c
```

```
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```

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```
./board/freescale/m5253evbe/m5253evbe.c
```

```
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```
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```
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m5272c3.c
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```
./board/freescale/m5275evb/m5275evb.c
```

```
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```
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```
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./board/freescale/m5329evb/nand.c
nand.c

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```
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```
./board/freescale/m5373evb/nand.c
nand.c
```

```
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```
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```
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m54451evb.c
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```
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m54455evb.c
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```
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mpc5121ads.c

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*/

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mpc7448hpc2.c

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*

* Roy Zang <tie-fei.zang@freescale.com>

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* modifications for the Tsi108 Emul Board by avb@Tundra
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flash.c
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*
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* I started with board/ip860/flash.c and made changes I found in
* the MTD project by David Schleef.
*
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* Yuli Barcohen <yuli@arabellasw.com>
* Re-written to support multi-bank flash SIMMs.
* Added support for real protection and JFFS2.
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./board/freescale/mpc8260ads/mpc8260ads.c

mpc8260ads.c

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```
./board/freescale/mpc8266ads/flash.c  
flash.c
```

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 * I started with board/ip860/flash.c and made changes I found in  
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```
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```
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mpc8308rdb.c
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* This files is mostly identical to the original from
* board\freescale\mpc8315erdb\sdram.c
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```
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```
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 * Based on mpc832xmds file by Dave Liu <daveliu@freescale.com>
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```
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pci.c
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*/  
/*  
* PCI Configuration space access support for MPC83xx PCI Bridge  
*/
```

```
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```

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ddr.c
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```
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mpc8349emds.c

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 */
```

./board/freescale/mpc8360emds/mpc8360emds.c

mpc8360emds.c

```
/*
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 * Dave Liu <daveliu@freescale.com>
 *
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 */
```

```
./board/freescale/mpc8360emds/pci.c
```

```
pci.c
```

```
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 */
/*
 * PCI Configuration space access support for MPC83xx PCI Bridge
 */
```

```
./board/freescale/mpc8360erdk/mpc8360erdk.c
```

```
mpc8360erdk.c
```

```
/*
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 *
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 *           Peter Barada <peterb@logicpd.com>
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 */
```

```
./board/freescale/mpc8360erdk/nand.c
```

```
nand.c
```

```
/*
 * MPC8360E-RDK support for the NAND on FSL UPM
 *
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 *           Anton Vorontsov <avorontsov@ru.mvista.com>
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 */
```

```
./board/freescale/mpc837xemds/mpc837xemds.c
mpc837xemds.c
```

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```
./board/freescale/mpc837xemds/pci.c
pci.c
```

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 */
```

```
./board/freescale/mpc837xemds/pci.h
pci.h
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```

```
./board/freescale/mpc837xerdb/mpc837xerdb.c
mpc837xerdb.c
```

```
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 * Joe D'Abbraccio <joe.d'abbraccio@freescale.com>
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```

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./board/freescale/mpc837xerdb/pci.c
```

```
pci.c
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```

```
./board/freescale/mpc8536ds/ddr.c
```

```
ddr.c
```

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```
./board/freescale/mpc8536ds/law.c
```

```
law.c
```

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```
mpc8536ds.c
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```

```
./board/freescale/mpc8536ds/tlb.c
```

```
tlb.c
```

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```
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ddr.c
```

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./board/freescale/mpc8540ads/law.c
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```
./board/freescale/mpc8540ads/mpc8540ads.c
```

mpc8540ads.c

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./board/freescale/mpc8540ads/tlb.c

tlb.c

```
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```
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ddr.c
```

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./board/freescale/mpc8541cds/law.c  
law.c
```

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mpc8541cds.c
```

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./board/freescale/mpc8541cds/tlb.c
```

```
tlb.c
```

```
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*/
```

```
./board/freescale/mpc8544ds/ddr.c
```

```
ddr.c
```

```
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```

```
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```

```
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```

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```
*/
```

```
./board/freescale/mpc8548cds/ddr.c
```

```
ddr.c
```

```
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```

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```

```
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```

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```

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mpc8548cds.c
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```
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```

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```
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```

```
ddr.c
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tlb.c
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ddr.c
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```

```
./board/freescale/mpc8560ads/mpc8560ads.c
mpc8560ads.c
```

```
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```

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```
./board/freescale/mpc8560ads/tlb.c
```

```
tlb.c
```

```
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```

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```
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./board/freescale/mpc8568mds/bcsr.c  
bcsr.c  
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./board/freescale/mpc8568mds/ddr.c

ddr.c

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law.c

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./board/freescale/mpc8568mds/mpc8568mds.c

mpc8568mds.c

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```
./board/freescale/mpc8568mds/tlb.c
```

```
tlb.c
```

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./board/freescale/mpc8569mds/bcsr.c
```

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bcsr.c
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./board/freescale/mpc8569mds/bcsr.h
bcsr.h
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ddr.c
```

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./board/freescale/mpc8569mds/law.c
law.c
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```
./board/freescale/mpc8569mds/mpc8569mds.c
mpc8569mds.c
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./board/freescale/mpc8569mds/tlb.c
```

```
tlb.c
```

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```
./board/freescale/mpc8572ds/ddr.c
```

```
ddr.c
```

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mpc8572ds.c
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tlb.c
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```
./board/freescale/mpc8610hpcd/ddr.c
ddr.c
```

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./board/freescale/mpc8610hpcd/mpc8610hpcd.c

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mpc8610hpcd_diu.c

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* Authors: York Sun <yorksun@freescale.com>

* Timur Tabi <timur@freescale.com>

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* FSL DIU Framebuffer driver

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```
./board/freescale/mpc8641hpcn/ddr.c
```

```
ddr.c
```

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```
mpc8641hpcn.c
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```
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```

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```
./board/freescale/mx28evk/iomux.c
iomux.c
```

```
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```
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```

```
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```
./board/freescale/mx31ads/mx31ads.c
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```
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```
mx31pdk.c
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```

```
./board/freescale/mx35pdk/mx35pdk.c
```

```
mx35pdk.c
```

```
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```

```
./board/freescale/mx35pdk/mx35pdk.h
```

```
mx35pdk.h
```

```
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./board/freescale/mx51evk/mx51evk.c
```

```
mx51evk.c
```

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```
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mx51evk_video.c
```

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```
./board/freescale/mx53loco/mx53loco.c
```

```
mx53loco.c
```

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./board/freescale/mx6qarm2/mx6qarm2.c

mx6qarm2.c

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./board/freescale/mx6qsabreauto/mx6qsabreauto.c

mx6qsabreauto.c

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mx6qsabrelite.c
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mx6qsabresd.c
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```
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```
./board/freescale/p1010rdb/p1010rdb.c
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p1010rdb.c

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./board/freescale/p1010rdb/tlb.c

tlb.c

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./board/freescale/p1022ds/ddr.c

ddr.c

```
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```
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law.c
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```
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pci.c

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*/
```

```
./board/freescale/p1_p2_rdb_pc/p1_p2_rdb_pc.c
p1_p2_rdb_pc.c
```

```
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```

```
./board/freescale/p1_p2_rdb_pc/spl_minimal.c
spl_minimal.c
```

```
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```

```
*
*/

./board/freescale/p1_p2_rdb_pc/tlb.c
tlb.c
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 */
```

```
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ddr.c
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 */
```

```
./board/freescale/p2020come/law.c
```

```
law.c
```

```
/*
```

```
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```

```
*
```

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```
*/
```

```
./board/freescale/p2020come/p2020come.c
```

```
p2020come.c
```

```
/*
```

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```
*/
```

```
./board/freescale/p2020come/tlb.c
```

tlb.c

```
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 */
```

./board/freescale/p2020ds/ddr.c

ddr.c

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```

./board/freescale/p2020ds/law.c

law.c

```
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*/
```

```
./board/freescale/p2020ds/p2020ds.c
```

```
p2020ds.c
```

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*/
```

```
./board/freescale/p2020ds/tlb.c
```

```
tlb.c
```

```
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*/  
  
./board/freescale/p2041rdb/cpld.c  
cpld.c  
/**  
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* Software Foundation; either version 2 of the License, or (at your option)  
* any later version.  
*  
* This file provides support for the board-specific CPLD used on some Freescale  
* reference boards.  
*  
* The following macros need to be defined:  
*  
* CPLD_BASE - The virtual address of the base of the CPLD register map  
*  
*/  
  
./board/freescale/p2041rdb/cpld.h  
cpld.h  
/**  
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* any later version.  
*  
* This file provides support for the ngPIXIS, a board-specific FPGA used on  
* some Freescale reference boards.  
*/  
  
./board/freescale/p2041rdb/ddr.c  
ddr.c  
/*  
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```

```
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*/

./board/freescale/p2041rdb/eth.c
eth.c
/*
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```

```
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p2041rdb.c
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```
./board/freescale/t4qds/ddr.c
```

```
ddr.c
```

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```

```
./board/freescale/t4qds/eth.c
```

```
eth.c
```

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 */
```

```
./board/freescale/t4qds/law.c
```

```
law.c
```

```
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```
./board/freescale/t4qds/pci.c
```

```
pci.c
```

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```

```
./board/freescale/t4qds/t4240qds_qixis.h
```

```
t4240qds_qixis.h
```

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```
./board/freescale/t4qds/t4qds.c
```

```
t4qds.c
```

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```
./board/freescale/t4qds/t4qds.h
```

```
t4qds.h
```

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```

```
./board/freescale/t4qds/tlb.c
```

```
tlb.c
```

```
/*
```

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```

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*
```

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```
*/
```

```
./board/friendlyarm/mini2440/mini2440.c
```

```
mini2440.c
```

```
/*
```

```
* (C) Copyright 2002
```

```
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
```

```
* Marius Groeger <mgroeger@sysgo.de>
```

```
*
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* Michel Pollet <buserror@gmail.com>
```

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*
```

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* Gabriel Huau <contact@huau-gabriel.fr>
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*/
```

```
./board/friendlyarm/mini2440/mini2440.h  
mini2440.h  
/* PLL Parameters */
```

```
./board/funkwerk/vovpn-gw/flash.c  
flash.c  
/*  
* (C) Copyright 2004  
* Elmeg Communications Systems GmbH, Juergen Selent (j.selent@elmeg.de)  
*  
* Support for the Elmeg VoVPN Gateway Module  
* -----  
* This is a single bank flashdriver for INTEL 28F320J3, 28F640J3  
* and 28F128J3A flashes working in 8 Bit mode.  
*  
* Most of this code is taken from existing u-boot source code.  
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*/
```

```
./board/funkwerk/vovpn-gw/m88e6060.c  
m88e6060.c
```

```
/*  
 * (C) Copyright 2004  
 * Elmeg Communications Systems GmbH, Juergen Selent (j.selent@elmeg.de)  
 *  
 * Support for the Elmeg VoVPN Gateway Module  
 * -----  
 * Initialize Marvell M88E6060 Switch  
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 */
```

```
./board/funkwerk/vovpn-gw/m88e6060.h  
m88e6060.h
```

```
/*  
 * (C) Copyright 2004  
 * Elmeg Communications Systems GmbH, Juergen Selent (j.selent@elmeg.de)  
 *  
 * Support for the Elmeg VoVPN Gateway Module  
 * -----  
 * Initialize Marvell M88E6060 Switch  
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*/

./board/funkwerk/vovpn-gw/vovpn-gw.c

vovpn-gw.c

/*

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* Elmeg Communications Systems GmbH, Juergen Selent (j.selent@elmeg.de)

*

* Support for the Elmeg VoVPN Gateway Module

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*/

./board/g2000/g2000.c

g2000.c

/*

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* Stefan Roese, esd gmbh germany, stefan.roese@esd-electronics.com

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```
*/  
  
./board/g2000/strataflash.c  
strataflash.c  
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*/
```

```
./board/gaisler/grsim/grsim.c  
grsim.c  
/* GRSIM/TSIM board  
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```

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*

*/

./board/gaisler/grsim_leon2/grsim_leon2.c

grsim_leon2.c

/* GRSIM/TSIM board

*

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*

*/

./board/gaisler/gr_cpci_ax2000/gr_cpci_ax2000.c

gr_cpci_ax2000.c

/*

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./board/gaisler/gr_ep2s60/gr_ep2s60.c
gr_ep2s60.c

```
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 */
```

./board/gaisler/gr_xc3s_1500/gr_xc3s_1500.c
gr_xc3s_1500.c

```
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 */
```

./board/galaxy5200/galaxy5200.c
galaxy5200.c

```
/*
```

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./board/gdsys/405ep/405ep.c
405ep.c

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*/
```

```
./board/gdsys/405ep/405ep.h
405ep.h
/* functions to be provided by board implementation */
```

```
./board/gdsys/405ep/dlvision-10g.c
dlvision-10g.c
```

```
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```

```
./board/gdsys/405ep/io.c
io.c
```

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*/
```

```
./board/gdsys/405ep/iocon.c
```

```
iocon.c
```

```
/*
```

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```

```
./board/gdsys/405ep/neo.c
```

```
neo.c
```

```
/*
```

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*/
```

```
./board/gdsys/405ex/405ex.c
405ex.c
No license information found.
```

```
./board/gdsys/405ex/405ex.h
405ex.h
/* functions to be provided by board implementation */
```

```
./board/gdsys/405ex/chip_config.c
chip_config.c
/*
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```

```
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io64.c
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*/
```

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```

```
./board/gdsys/common/miiphybb.c
```

```
miiphybb.c
```

```
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```

```
./board/gdsys/common/osd.c
```

```
osd.c
```

```
/*
```

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```
./board/gdsys/common/osd.h
```

```
osd.h
```

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```

```
./board/gdsys/dlvision/dlvision.c
```

```
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```

```
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```

```
./board/gdsys/gdppc440etx/gdppc440etx.c
gdppc440etx.c
```

```
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```
./board/gdsys/intip/chip_config.c  
chip_config.c
```

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```
./board/gdsys/intip/intip.c  
intip.c
```

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./board/gen860t/beeper.c
beeper.c

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./board/gen860t/beeper.h
beeper.h

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flash.c

```
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```

./board/gen860t/fpga.c
fpga.c

```
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```
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```

```
fpga.h
```

```
/*
```

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```

```
./board/gen860t/gen860t.c
```

```
gen860t.c
```

```
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```

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*/
```

```
./board/gen860t/ioport.c
ioport.c
```

```
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```

```
./board/gen860t/ioport.h
ioport.h
```

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```

```
./board/genesi/mx51_efikamx/efikamx-usb.c
efikamx-usb.c
```

```
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*/
```

```
./board/genesi/mx51_efikamx/efikamx.c
efikamx.c
```

```
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```

```
./board/genietv/flash.c
flash.c
```

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*/
```

```
./board/genietv/genietv.c
genietv.c
```

```
/*
* genietv/genietv.c
*
* The GENIETV is using the following physical memorymap (copied from
* the FADS configuration):
*
* ff020000 -> ff02ffff : pcmcia
* ff010000 -> ff01ffff : BCSR      connected to CS1, setup by 8xxROM
* ff000000 -> ff00ffff : IMAP     internal in the cpu
* 02800000 -> 0287ffff : flash    connected to CS0
* 00000000 -> nnnnnnnn : sdram     setup by U-Boot
*/
```

```

*
* CS pins are connected as follows:
*
* CS0 -512Kb boot flash
* CS1 - SDRAM #1
* CS2 - SDRAM #2
* CS3 - Flash #1
* CS4 - Flash #2
* CS5 - LON (if present)
* CS6 - PCMCIA #1
* CS7 - PCMCIA #2
*
* Ports are configured as follows:
*
* PA7 - SDRAM banks enable
*/

./board/genietv/genietv.h
genietv.h
/*
* The GENIETV is using the following physical memorymap (copied from
* the FADS configuration):
*
* ff020000 -> ff02ffff : pcmcia
* ff010000 -> ff01ffff : BCSR      connected to CS1, setup by 8xxROM
* ff000000 -> ff00ffff : IMAP      internal in the cpu
* 02800000 -> 0287ffff : flash     connected to CS0
* 00000000 -> nnnnnnnn : sdram     setup by U-Boot
*
* CS pins are connected as follows:
*
* CS0 -512Kb boot flash
* CS1 - SDRAM #1
* CS2 - SDRAM #2
* CS3 - Flash #1
* CS4 - Flash #2
* CS5 - LON (if present)
* CS6 - PCMCIA #1
* CS7 - PCMCIA #2
*
* Ports are configured as follows:
*
* PA7 - SDRAM banks enable
*/

./board/gw8260/flash.c
flash.c
/*
* (C) Copyright 2000
* Marius Groeger <mgroeger@sysgo.de>

```

```
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*/
```

```
./board/gw8260/gw8260.c
gw8260.c
```

```
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*/
```

```
./board/h2200/h2200.c
```

```
h2200.c
```

```
/*
 * iPAQ h2200 board configuration
 *
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```

```
./board/hale/tt01/tt01.c
```

```
tt01.c
```

```
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```

```
./board/hermes/flash.c
```

```
flash.c
```

```
/*
```

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```

```
./board/hermes/hermes.c
```

```
hermes.c
```

```
/*
```

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```

```
./board/hidden_dragon/flash.c
```

```
flash.c
```

```
/*
```

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*
```

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```

```
./board/hidden_dragon/hidden_dragon.c
```

```
hidden_dragon.c
```

```
/*
```

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```
./board/hidden_dragon/speed.h
speed.h
```

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```

```
./board/highbank/highbank.c
highbank.c
```

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```

```
./board/htkw/mcx/mcx.c
```

```
mcx.c
```

```
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 *
 * Based on ti/evm/evm.c
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```
./board/htkw/mcx/mcx.h
```

```
mcx.h
```

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```
./board/hymod/bsp.c
```

```
bsp.c
```

```
/*
```

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*
* hacked for Hymod FPGA support by Murray.Jensen@csiro.au, 29-Jan-01
*/
```

```
./board/hymod/eeeprom.c
```

```
eeeprom.c
```

```
/*
```

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```
./board/hymod/env.c
```

```
env.c
```

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```

```
./board/hymod/fetch.c
```

```
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```

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```
./board/hymod/flash.c
```

```
flash.c
```

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```

```
./board/hymod/flash.h
```

```
flash.h
```

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```
./board/hymod/hymod.c
```

```
hymod.c
```

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```

```
./board/hymod/hymod.h
```

```
hymod.h
```

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```
./board/hymod/input.c
```

```
input.c
```

```
/*
```

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```
* Murray Jensen, CSIRO-MIT, <Murray.Jensen@csiro.au>
```

```
/*
```

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```
*/
```

```
./board/ibf-dsp561/ibf-dsp561.c
```

```
ibf-dsp561.c
```

```
/*
```

```
* U-boot - main board file
```

```
/*
```

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```
/*
```

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```

```
*/
```

```
./board/icecube/flash.c
```

```
flash.c
```

```
/*
```

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```

```
./board/icecube/icecube.c
```

```
icecube.c
```

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```
./board/icecube/mt46v16m16-75.h
```

mt46v16m16-75.h

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./board/icecube/mt46v32m16.h

mt46v32m16.h

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./board/icecube/mt48lc16m16a2-75.h

mt481c16m16a2-75.h

```
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./board/icu862/flash.c

flash.c

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./board/icu862/icu862.c

icu862.c

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./board/icu862/pcmcia.c

pcmcia.c

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./board/idmr/flash.c

flash.c

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./board/idmr/idmr.c
idmr.c

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./board/ids8247/ids8247.c
ids8247.c

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./board/ifm/o2dnt2/o2dnt2.c
o2dnt2.c

```
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./board/imx31_phycore/imx31_phycore.c
imx31_phycore.c

```
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```
./board/in-circuit/grasshopper/grasshopper.c
grasshopper.c
```

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```
./board/incaip/flash.c
flash.c
```

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./board/incaip/incaip.c
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```
*/
```

```
./board/inka4x0/hyb25d512160bf-5.h
```

```
hyb25d512160bf-5.h
```

```
/*
```

```
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```
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```

```
./board/inka4x0/inka4x0.c
inka4x0.c
```

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```
./board/inka4x0/inkadiag.c
inkadiag.c
```

```
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./board/inka4x0/k4h511638c.h
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./board/inka4x0/mt46v16m16-75.h
mt46v16m16-75.h

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```
./board/inka4x0/mt46v32m16-75.h
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```
mt46v32m16-75.h
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```
./board/inka4x0/mt48lc16m16a2-75.h
```

```
mt48lc16m16a2-75.h
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```
./board/intercontrol/digsy_mtc/cmd_disp.c
```

```
cmd_disp.c
```

```
/*
```

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*
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*/
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```
./board/intercontrol/digsy_mtc/cmd_mtc.c
```

```
cmd_mtc.c
```

```
/*
```

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```
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```
./board/intercontrol/digsy_mtc/cmd_mtc.h
```

```
cmd_mtc.h
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 * frank.bodammer@gcd-solutions.de
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 */
```

```
./board/intercontrol/digsy_mtc/eprom.h
eprom.h
/*
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 * Written by: Grzegorz Bernacki <gjb@semihalf.com>
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*  
*/
```

```
./board/intercontrol/digsy_mtc/is42s16800a-7t.h  
is42s16800a-7t.h
```

```
/*  
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* MA 02111-1307 USA  
*/
```

```
./board/intercontrol/digsy_mtc/is45s16800a2.h  
is45s16800a2.h
```

```
/*  
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* Heiko Schocher, DENX Software Engineering, hs@denx.de.  
*  
* based on:  
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*/
```

```
./board/iomega/iconnect/iconnect.c
```

```
iconnect.c
```

```
/*
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* Wojciech Dubowik <wojciech.dubowik@neratec.com>
* Luka Perkov <luka@openwrt.org>
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```

```
./board/iomega/iconnect/iconnect.h
```

```
iconnect.h
```

```
/*
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*/
```

```
./board/ip04/ip04.c
```

```
ip04.c
```

```
/*
 * U-boot - main board file
 *
 * Copyright (c) 2007 David Rowe,
 *           (c) 2006 Ivan Danov
 *
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 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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 */
```

```
./board/ip860/flash.c
```

```
flash.c
```

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 */
```

./board/ip860/ip860.c

ip860.c

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*/

./board/ipek01/ipek01.c

ipek01.c

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*/
```

```
./board/iphase4539/flash.c
flash.c
```

```
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*
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```
./board/iphase4539/iphase4539.c
iphase4539.c
```

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```

```
./board/isee/igep0020/igep0020.c
igep0020.c
```

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```

```
./board/isee/igep0020/igep0020.h
igep0020.h
```

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```
./board/isee/igep0030/igep0030.c
igep0030.c
```

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```
./board/isee/igep0030/igep0030.h
igep0030.h
```

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```

```
./board/ispan/ispan.c
```

```
ispan.c
```

```
/*
* Copyright (C) 2004 Arabella Software Ltd.
* Yuli Barcohen <yuli@arabellasw.com>
*
* Support for Interphase iSPAN Communications Controllers
* (453x and others). Tested on 4532.
*
* Derived from iSPAN 4539 port (iphase4539) by
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*/
```

```
./board/ivm/flash.c
```

```
flash.c
```

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```

```
./board/ivm/ivm.c
```

```
ivm.c
```

```
/*
```

```
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```
./board/jornada/jornada.c
```

```
jornada.c
```

```
/*
```

```
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```

```
./board/jse/flash.c
flash.c
```

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```

```
./board/jse/host_bridge.c
host_bridge.c
```

```
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./board/jse/jse.c

jse.c

```
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./board/jse/jse_priv.h

jse_priv.h

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```
./board/jse/sdram.c
sdram.c
```

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```

```
./board/jupiter/jupiter.c
jupiter.c
```

```
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```

```
./board/karo/tk71/tk71.c
```

```
tk71.c
```

```
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```

```
./board/karo/tx25/tx25.c
```

```
tx25.c
```

```
/*
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*
* Based on imx27lite.c:
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```

```
./board/keymile/common/common.c
```

```
common.c
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./board/keymile/common/common.h
```

```
common.h
```

```
/*
* (C) Copyright 2008
```

```
* Heiko Schocher, DENX Software Engineering, hs@denx.de.  
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* the License, or (at your option) any later version.  
*/
```

```
./board/keymile/common/ivm.c
```

```
ivm.c
```

```
/*  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./board/keymile/km82xx/km82xx.c
```

```
km82xx.c
```

```
/*  
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*/

./board/keymile/km83xx/km83xx.c

km83xx.c

/*
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* Dave Liu <daveliu@freescale.com>
*
* Copyright (C) 2007 Logic Product Development, Inc.
* Peter Barada <peterb@logicpd.com>
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* Anton Vorontsov <avorontsov@ru.mvista.com>
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*/

./board/keymile/km83xx/km83xx_i2c.c

km83xx_i2c.c

/*
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```
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./board/keymile/km_arm/fpga_config.c
```

```
fpga_config.c
```

```
/*  
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* Valentin Lontgchamp, Keymile AG, valentin.longchamp@keymile.com  
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* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,  
* MA 02110-1301 USA  
*/
```

```
./board/keymile/km_arm/km_arm.c
```

```
km_arm.c
```

```
/*  
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* Prafulla Wadaskar <prafulla@marvell.com>  
*  
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*  
*/
```

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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,  
* MA 02110-1301 USA  
*/
```

```
./board/kmc/kzm9g/kzm9g.c
```

```
kzm9g.c
```

```
/*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./board/korat/korat.c
```

```
korat.c
```

```
/*  
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*  
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*/
```

```
./board/kup/common/flash.c
flash.c
```

```
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* MA 02111-1307 USA
*/
```

```
./board/kup/common/kup.c
kup.c
```

```
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*/
```

```
./board/kup/common/kup.h
```

```
kup.h
```

```
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* MA 02111-1307 USA
*/
```

```
./board/kup/common/load_serenum_ethaddr.c
```

```
load_serenum_ethaddr.c
```

```
/*
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* MA 02111-1307 USA
*/
```

```
./board/kup/common/pcmcia.c
pcmcia.c
No license information found.
```

```
./board/kup/kup4k/kup4k.c
kup4k.c
/*
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* Klaus Heydeck, Kieback & Peter GmbH & Co KG, heydeck@kieback-peter.de
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* MA 02111-1307 USA
*/
```

```
./board/kup/kup4x/kup4x.c
kup4x.c
/*
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```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./board/LaCie/common/common.c
```

```
common.c
```

```
/*
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 */
```

```
./board/LaCie/common/common.h
```

```
common.h
```

```
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 */
```

```
./board/LaCie/adminiv2/adminiv2.c
```

```
adminiv2.c
```

```
/*
 * Copyright (C) 2010 Albert ARIBAUD <albert.u.boot@aribaud.net>
 *
 * (C) Copyright 2009
 * Marvell Semiconductor <www.marvell.com>
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```
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
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*/
```

```
./board/LaCie/net2big_v2/net2big_v2.c
net2big_v2.c
```

```
/*
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*
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* Marvell Semiconductor <www.marvell.com>
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
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```

```
./board/LaCie/net2big_v2/net2big_v2.h
net2big_v2.h
```

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```
./board/LaCie/netspace_v2/netspace_v2.c
netspace_v2.c
```

```
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```

```
./board/LaCie/netspace_v2/netspace_v2.h
netspace_v2.h
```

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/*
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```

```
./board/LaCie/wireless_space/wireless_space.c
wireless_space.c
```

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```

```
./board/LEOX/elpt860/elpt860.c
elpt860.c
```

```
/*
**=====
**
** Copyright (C) 2000, 2001, 2002, 2003
** The LEOX team <team@leox.org>, http://www.leox.org
**
** LEOX.org is about the development of free hardware and software resources
```

```
** for system on chip.
**
** Description: U-Boot port on the LEOX's ELPT860 CPU board
** ~~~~~
**
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**
**=====
*/

./board/LEOX/elpt860/flash.c
flash.c
/*
**=====
**
** Copyright (C) 2000, 2001, 2002, 2003
** The LEOX team <team@leox.org>, http://www.leox.org
**
** LEOX.org is about the development of free hardware and software resources
** for system on chip.
**
** Description: U-Boot port on the LEOX's ELPT860 CPU board
** ~~~~~
**
**=====
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**
**=====
*/
```

```
./board/linkstation/avr.c
```

```
avr.c
```

```
/*
 * avr.c
 *
 * AVR functions
 *
 * Copyright (C) 2006 Mihai Georgian <u-boot@linuxnotincluded.org.uk>
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 * MA 02111-1307 USA
 */
```

```
./board/linkstation/hwctl.c
```

```
hwctl.c
```

```
/*
 * hwctl.c
 *
 * LinkStation HW Control Driver
 *
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 */
```

```
*/  
  
./board/linkstation/ide.c  
ide.c  
/*  
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*  
*/  
/* ide.c - ide support functions */  
  
./board/linkstation/linkstation.c  
linkstation.c  
/*  
* linkstation.c  
*  
* Misc LinkStation specific functions  
*  
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```

```
./board/logicpd/am3517evm/am3517evm.c  
am3517evm.c
```

```
/*  
* am3517evm.c - board file for TI's AM3517 family of devices.  
*  
* Author: Vaibhav Hiremath <hvaibhav@ti.com>  
*  
* Based on ti/evm/evm.c  
*  
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*/
```

```
./board/logicpd/am3517evm/am3517evm.h  
am3517evm.h
```

```
/*  
* am3517evm.h - Header file for the AM3517 EVM.  
*  
* Author: Vaibhav Hiremath <hvaibhav@ti.com>  
*  
* Based on ti/evm/evm.h  
*  
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```

```
./board/logicpd/imx27lite/imx27lite.c
```

```
imx27lite.c
```

```
/*
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```

```
./board/logicpd/imx31_litekit/imx31_litekit.c
```

```
imx31_litekit.c
```

```
/*
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```

```
./board/logicpd/omap3som/omap3logic.c
```

```
omap3logic.c
```

```
/*
```

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```
*
```

```
* Author :
* Peter Barada <peter.barada@logicpd.com>
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*
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* Richard Woodruff <r-woodruff2@ti.com>
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```
*/
```

```
./board/logicpd/omap3som/omap3logic.h
```

```
omap3logic.h
```

```
/*
```

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```

```
./board/logicpd/zoom1/zoom1.c
```

```
zoom1.c
```

```
/*
```

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```
*
```

```
* Author :
```

```
*     Nishanth Menon <nm@ti.com>
```

```
*
```

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* Derived from Beagle Board and 3430 SDP code by
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*     Shashi Ranjan <shashiranjnca05@gmail.com>
*     Richard Woodruff <r-woodruff2@ti.com>
*     Syed Mohammed Khasim <khasim@ti.com>
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```
*/  
  
./board/logicpd/zoom1/zoom1.h  
zoom1.h  
/*  
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* Nishanth Menon <nm@ti.com>  
*  
* Derived from: board/omap3/beagle/beagle.h  
* Dirk Behme <dirk.behme@gmail.com>  
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*/
```

```
./board/logicpd/zoom2/debug_board.c  
debug_board.c  
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*/
```

```
./board/logicpd/zoom2/led.c
led.c
```

```
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```

```
./board/logicpd/zoom2/zoom2.c
zoom2.c
```

```
/*
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 *
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 *   Sunil Kumar <sunilsaini05@gmail.com>
 *   Shashi Ranjan <shashiranjnmca05@gmail.com>
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```
./board/logicpd/zoom2/zoom2.h
zoom2.h
```

```
/*
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```
./board/logicpd/zoom2/zoom2_serial.c
zoom2_serial.c
```

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* This file was adapted from arch/powerpc/cpu/mpc5xxx/serial.c
*
*/
```

```
./board/logicpd/zoom2/zoom2_serial.h
zoom2_serial.h
/*
```

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*/
```

```
./board/lubbock/flash.c
flash.c
/*
```

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```

```
./board/lubbock/lubbock.c
```

```
lubbock.c
```

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```
./board/lwmon/flash.c
```

```
flash.c
```

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```

```
./board/lwmon/lwmon.c
```

```
lwmon.c
```

```
/******
```

```
*
M* Modul:          lwmon.c
M*
M* Content:        LWMON specific U-Boot commands.
*
```

```
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* Wolfgang Denk, wd@denx.de
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```

```
D* Design:         wd@denx.de
C* Coding:         wd@denx.de
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*****/

```
./board/lwmon/pcmcia.c
pcmcia.c
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```
./board/lwmon5/kbd.c
kbd.c
```

```
/*
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 */
```

```
./board/lwmon5/lwmon5.c
lwmon5.c
```

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```
./board/lwmon5/sdram.c  
sdram.c
```

```
/*  
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```
./board/manroland/hmi1001/hmi1001.c  
hmi1001.c
```

```
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```
./board/manroland/mucmc52/mucmc52.c
mucmc52.c
```

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```

```
./board/manroland/uc100/pcmcia.c
pcmcia.c
```

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```
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uc100.c
```

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 * MA 02111-1307 USA  
 */
```

```
./board/manroland/uc101/uc101.c  
uc101.c
```

```
/*  
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 *  
 * (C) Copyright 2003-2004  
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 *  
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 * Mark Jonas, Freescale Semiconductor, mark.jonas@motorola.com.  
 *  
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 * Martin Krause, TQ-Systems GmbH, martin.krause@tqs.de  
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*/
```

```
./board/Marvell/aspelite/aspelite.c
```

```
aspelite.c
```

```
/*
```

```
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* Marvell Semiconductor <www.marvell.com>  
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>  
* Contributor: Mahavir Jain <mjain@marvell.com>  
*
```

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* MA 02110-1301 USA  
*/
```

```
./board/Marvell/common/ecctest.c
```

```
ecctest.c
```

```
/*
```

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```

```
./board/Marvell/common/flash.c
flash.c
```

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```

```
./board/Marvell/common/i2c.c
i2c.c
```

```
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*
* Hacked for the DB64360 board by Ingo.Assmus@keymile.com
* extra improvements by Brain Waite
*/
```

```
./board/Marvell/common/i2c.h
```

```
i2c.h
```

```
/*
```

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* Hacked for the DB64360 board by Ingo.Assmus@keymile.com
*/
```

```
./board/Marvell/common/intel_flash.c
```

```
intel_flash.c
```

```
/*
```

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*
* Hacked for the marvell db64360 eval board by
* Ingo Assmus <ingo.assmus@keymile.com>
*/
```

```
./board/Marvell/common/intel_flash.h
intel_flash.h
```

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* Hacked for the marvell db64360 eval board by
* Ingo Assmus <ingo.assmus@keymile.com>
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```

```
./board/Marvell/common/memory.c
memory.c
```

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 */

./board/Marvell/common/ns16550.c
ns16550.c
/*
 * COM1 NS16550 support
 * originally from linux source (arch/powerpc/boot/ns16550.c)
 * modified to use CONFIG_SYS_ISA_MEM and new defines
 *
 * further modified by Josh Huber <huber@mclx.com> to support
 * the DUART on the Galileo Eval board. (db64360)
 */

./board/Marvell/common/ns16550.h
ns16550.h
/*
 * NS16550 Serial Port
 * originally from linux source (arch/powerpc/boot/ns16550.h)
 * modified slightly to
 * have addresses as offsets from CONFIG_SYS_ISA_BASE
 * added a few more definitions
 * added prototypes for ns16550.c
 * reduced no of com ports to 2
 * modifications (c) Rob Taylor, Flying Pig Systems. 2000.
 *
 * further modified to support the DUART in the Galileo eval board
 * modifications (c) Josh Huber <huber@mclx.com>, Mission Critical Linux, Inc.
 */

./board/Marvell/common/serial.c
```

serial.c

```
/*
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 * Josh Huber <huber@mclx.com>, Mission Critical Linux, Inc.
 *
 * modified for marvell db64360 eval board by
 * Ingo Assmus <ingo.assmus@keymile.com>
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 */
```

./board/Marvell/common/serial.h

serial.h

```
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 * modified for marvell db64360 eval board by
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```
./board/Marvell/db64360/64360.h
64360.h
```

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```
./board/Marvell/db64360/db64360.c
db64360.c
```

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* modifications for the DB64360 eval board based by Ingo.Assmus@keymile.com
*/
```

```
./board/Marvell/db64360/eth.h
```

```
eth.h
```

```
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```

```
./board/Marvell/db64360/mpsc.c
```

```
mpsc.c
```

```
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```

```
./board/Marvell/db64360/mpsc.h
```

```
mpsc.h
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*/
```

```
./board/Marvell/db64360/mv_eth.c
```

```
mv_eth.c
```

```
/*
* (C) Copyright 2003
* Ingo Assmus <ingo.assmus@keymile.com>
*
* based on - Driver for MV64360X ethernet ports
* Copyright (C) 2002 rabeeh@galileo.co.il
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*/
```

```
./board/Marvell/db64360/mv_eth.h
```

```
mv_eth.h
```

```
/*
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*
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*/
```

```
./board/Marvell/db64360/mv_regs.h
```

```
mv_regs.h
```

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```

```
./board/Marvell/db64360/pci.c
```

```
pci.c
```

```
/*
```

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*/
```

```
/* PCI.c - PCI functions */
```

```
./board/Marvell/db64360/sdram_init.c
```

```
sdram_init.c
```

```
/*
```

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```

```
./board/Marvell/db64460/64460.h  
64460.h
```

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*/
```

```
./board/Marvell/db64460/db64460.c  
db64460.c
```

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* modifications for the DB64460 eval board based by Ingo.Assmus@keymile.com  
*/
```

```
./board/Marvell/db64460/eth.h
```

```
eth.h
```

```
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```

```
./board/Marvell/db64460/mpsc.c
```

```
mpsc.c
```

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*/
```

```
./board/Marvell/db64460/mpsc.h
```

```
mpsc.h
```

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* MA 02111-1307 USA
*/
```

```
./board/Marvell/db64460/mv_eth.c
```

```
mv_eth.c
```

```
/*
* (C) Copyright 2003
* Ingo Assmus <ingo.assmus@keymile.com>
```

```
*
* based on - Driver for MV64460X ethernet ports
* Copyright (C) 2002 rabeeh@galileo.co.il
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```

```
./board/Marvell/db64460/mv_eth.h
```

```
mv_eth.h
```

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* Ingo Assmus <ingo.assmus@keymile.com>
*
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*/
```

./board/Marvell/db64460/mv_regs.h

mv_regs.h

/*

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*

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*/

./board/Marvell/db64460/pci.c

pci.c

/*

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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.

*

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```
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
/* PCI.c - PCI functions */

./board/Marvell/db64460/sdram_init.c
sdram_init.c
/*
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* Josh Huber <huber@mclx.com>, Mission Critical Linux, Inc.
*
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*/

./board/Marvell/dkb/dkb.c
dkb.c
/*
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* Written-by: Lei Wen <leiw@marvell.com>
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```

```
./board/Marvell/dreamplug/dreamplug.c
dreamplug.c
```

```
/*
* (C) Copyright 2011
* Jason Cooper <u-boot@lakedaemon.net>
*
* Based on work by:
* Marvell Semiconductor <www.marvell.com>
* Written-by: Siddarth Gore <gores@marvell.com>
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*/
```

```
./board/Marvell/dreamplug/dreamplug.h
dreamplug.h
```

```
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* (C) Copyright 2011
* Jason Cooper <u-boot@lakedaemon.net>
*
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*/
```

```
./board/Marvell/gplugd/gplugd.c
gplugd.c
```

```
/*
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* eInfochips Ltd. <www.einfochips.com>
* Written-by: Ajay Bhargav <ajay.bhargav@einfochips.com>
*
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* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
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*/
```

```
./board/Marvell/guruplug/guruplug.c
guruplug.c
```

```
/*
```

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```

```
./board/Marvell/guruplug/guruplug.h
guruplug.h
```

```
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*/
```

```
./board/Marvell/include/core.h
```

```

core.h
/* Core.h - Basic core logic functions and definitions */
/* Copyright Galileo Technology. */
/*
*/

./board/Marvell/include/memory.h
memory.h
/* Memory.h - Memory mappings and remapping functions declarations */
/* Copyright - Galileo technology. */
/* includes */
/* defines */

./board/Marvell/include/mv_gen_reg.h
mv_gen_reg.h
/* mv_gen_reg.h - Internal registers definition file */
/* Copyright - Galileo technology. */
/*****
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* (MJKK), GALILEO TECHNOLOGY LTD. (GTL) AND GALILEO TECHNOLOGY, INC. (GTI).
*****/
* mv_gen_reg.h - Marvell 64360 and 64460 Internal registers definition file.
*
* DESCRIPTION:
*     None.
*
* DEPENDENCIES:
*     None.
*
*****/

./board/Marvell/include/pci.h
pci.h
/* PCI.h - PCI functions header file */
/* Copyright - Galileo technology. */
/* includes */
/* According to PCI REV 2.1 MAX agents allowed on the bus are -21- */

./board/Marvell/mv88f6281gtw_ge/mv88f6281gtw_ge.c
mv88f6281gtw_ge.c

```

```
/*
* Maintainer : Prafulla Wadaskar <prafulla@marvell.com>
*
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* Marvell Semiconductor <www.marvell.com>
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
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* MA 02110-1301 USA
*/
```

```
./board/Marvell/mv88f6281gtw_ge/mv88f6281gtw_ge.h
mv88f6281gtw_ge.h
```

```
/*
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* Marvell Semiconductor <www.marvell.com>
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```

*/

./board/Marvell/openrd/openrd.c

openrd.c

/*

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* Net Insight <www.netinsight.net>

* Written-by: Simon Kagstrom <simon.kagstrom@netinsight.net>

*

* Based on sheevaplug.c:

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* Written-by: Prafulla Wadaskar <prafulla@marvell.com>

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*/

./board/Marvell/openrd/openrd.h

openrd.h

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```

```
./board/Marvell/rd6281a/rd6281a.c
rd6281a.c
```

```
/*
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* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
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```

```
./board/Marvell/rd6281a/rd6281a.h
rd6281a.h
```

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*/
```

```
./board/Marvell/sheevaplug/sheevaplug.c
sheevaplug.c
```

```
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```

```
./board/Marvell/sheevaplug/sheevaplug.h
sheevaplug.h
```

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*/
```

```
./board/matrix_vision/common/mv_common.c
```

```
mv_common.c
```

```
/*  
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* MA 02111-1307 USA  
*/
```

```
./board/matrix_vision/common/mv_common.h
```

```
mv_common.h
```

```
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*/
```

```
./board/matrix_vision/mergerbox/fpga.c
```

```
fpga.c
```

```
/*
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* Rich Ireland, Enterasys Networks, rireland@enterasys.com.
* Keith Outwater, keith_outwater@mvis.com.
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* Andre Schwarz, Matrix Vision GmbH, andre.schwarz@matrix-vision.de
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```

```
./board/matrix_vision/mergerbox/fpga.h
```

```
fpga.h
```

```
/*
```

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```

```
./board/matrix_vision/mergerbox/mergerbox.c
mergerbox.c
```

```
/*
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* Andre Schwarz <andre.schwarz@matrix-vision.de>
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```

*/

./board/matrix_vision/mergerbox/pci.c
pci.c

/*

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*

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* Andre Schwarz <andre.schwarz@matrix-vision.de>

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*/

./board/matrix_vision/mergerbox/sm107.c
sm107.c

/*

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*/

./board/matrix_vision/mvbc_p/fpga.c
fpga.c

/*

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*/
```

```
./board/matrix_vision/mvbc_p/fpga.h
```

```
fpga.h
```

```
/*
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*
*/
```

```
./board/matrix_vision/mvbc_p/mvbc_p.c
```

```
mvbc_p.c
```

```
/*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
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* Mark Jonas, Freescale Semiconductor, mark.jonas@motorola.com.
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*/
```

```
./board/matrix_vision/mvbc_p/mvbc_p.h
mvbc_p.h
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```

```
./board/matrix_vision/mvblm7/fpga.c
fpga.c
/*
```

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```

```
./board/matrix_vision/mvblm7/fpga.h
```

```
fpga.h
```

```
/*
```

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*/
```

```
./board/matrix_vision/mvblm7/mvblm7.c
```

```
mvblm7.c
```

```
/*
```

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*/
```

```
./board/matrix_vision/mvblm7/mvblm7.h
mvblm7.h
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```

```
./board/matrix_vision/mvblm7/pci.c
pci.c
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```

```
./board/matrix_vision/mvblx/fpga.c
fpga.c
/*
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* Keith Outwater, keith_outwater@mvis.com.
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```

```
./board/matrix_vision/mvblx/fpga.h
```

```
fpga.h
```

```
/*
```

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*/
```

```
./board/matrix_vision/mvblx/mvblx.c
```

mvblx.c

```
/*
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 * Texas Instruments, <www.ti.com>
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 * Author :
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 */
```

./board/matrix_vision/mvblx/mvblx.h

mvblx.h

```
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```

```
./board/matrix_vision/mvblx/sys_eeprom.c
sys_eeprom.c
```

```
/*
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*/
```

```
./board/matrix_vision/mvsmr/fpga.c
fpga.c
```

```
/*
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```

```
./board/matrix_vision/mvsmr/fpga.h
```

```
fpga.h
```

```
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*/
```

```
./board/matrix_vision/mvsmr/mvsmr.c
```

```
mvsmr.c
```

```
/*  
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
```

```
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*/
```

```
./board/matrix_vision/mvsmr/mvsmr.h
mvsmr.h
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```

```
./board/mbx8xx/csr.h
csr.h
```

```
/*
* (C) Copyright 2000
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*
* Control and Status Register definitions for the MBX
*
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*/
```

```
./board/mbx8xx/dimm.h
dimm.h
```

```
/*
* Module name: %M%
* Description:
* Serial Presence Detect Definitions Module
* SCCS identification: %I%
* Branch: %B%
* Sequence: %S%
* Date newest applied delta was created (MM/DD/YY): %G%
* Time newest applied delta was created (HH:MM:SS): %U%
* SCCS file name %F%
* Fully qualified SCCS file name:
* %P%
* Copyright:
* (C) COPYRIGHT MOTOROLA, INC. 1996
* ALL RIGHTS RESERVED
* Notes:
* 1. All data was taken from an IBM application note titled
* "Serial Presence Detect Definitions".
* History:
* Date Who
*
* 10/24/96 Rob Baxter
* Initial release.
*
*/
```

```
./board/mbx8xx/flash.c
flash.c
```

```
/*
* (C) Copyright 2000
* Marius Groeger <mgroeger@sysgo.de>
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
*
* (C) Copyright 2000
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* Flash Routines for AM290[48]0B devices
*
*-----
```

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```

```
./board/mbx8xx/mbx8xx.c
```

```
mbx8xx.c
```

```
/*
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* Marius Groeger <mgroeger@sysgo.de>
*
* Board specific routines for the MBX
*
* - initialisation
* - interface to VPD data (mac address, clock speeds)
* - memory controller
* - serial io initialisation
* - ethernet io initialisation
*
* -----
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```

```
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pcmcia.c  
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```

```
./board/mbx8xx/vpd.c  
vpd.c  
/*  
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* Marius Groeger <mgroeger@sysgo.de>  
*  
* Code in faintly related to linux/arch/powerpc/8xx_io:  
* MPC8xx CPM I2C interface. Copyright (c) 1999 Dan Malek (dmalek@jilc.net).  
*  
* This file implements functions to read the MBX's Vital Product Data  
* (VPD). I can't use the more general i2c code in mpc8xx/... since I need  
* the VPD at a time where there is no RAM available yet. Hence the VPD is  
* read into a special area in the DPRAM (see config_MBX.h::CFG_DPRAMVPD).  
*  
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*/
```

```
./board/mbx8xx/vpd.h  
vpd.h  
/*  
* Module name: %M%  
* Description:  
* Vital Product Data (VPD) Header Module  
* SCCS identification: %I%
```

```
* Branch: %B%
* Sequence: %S%
* Date newest applied delta was created (MM/DD/YY): %G%
* Time newest applied delta was created (HH:MM:SS): %U%
* SCCS file name %F%
* Fully qualified SCCS file name:
* %P%
* Copyright:
* (C) COPYRIGHT MOTOROLA, INC. 1996
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* Notes:
* History:
* Date Who
*
* 10/24/96 Rob Baxter
* Initial release.
*
*/
```

./board/mcc200/auto_update.c

auto_update.c

```
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```

./board/mcc200/lcd.c

lcd.c

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```

```
./board/mcc200/mcc200.c
```

```
mcc200.c
```

```
/*
```

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```

```
./board/mcc200/mt46v16m16-75.h
```

```
mt46v16m16-75.h
```

```
/*
```

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```

```
./board/mcc200/mt48lc16m16a2-75.h
mt48lc16m16a2-75.h
```

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```

```
./board/mcc200/mt48lc16m32s2-75.h
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```

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./board/mcc200/mt48lc8m32b2-6-7.h
mt48lc8m32b2-6-7.h
/*
* Configuration Registers for the MT48LC8M32B2 SDRAM on the MPC5200 platform
*/
/* Settings for XLB = 132 MHz */

./board/micronas/vct/bcu.h
bcu.h
/*
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./board/micronas/vct/dcgu.c
dcgu.c
/*
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```

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./board/micronas/vct/ebi.c
ebi.c
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```
./board/micronas/vct/ebi.h
ebi.h
```

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```
./board/micronas/vct/ebi_nor_flash.c
ebi_nor_flash.c
```

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```

```
./board/micronas/vct/ebi_onenand.c
```

```
ebi_onenand.c
```

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```

```
./board/micronas/vct/ebi_smc911x.c
```

```
ebi_smc911x.c
```

```
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```

```
./board/micronas/vct/ehci.c
```

```
ehci.c
```

```
/*
```

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```
*/
```

```
./board/micronas/vct/gpio.c
```

```
gpio.c
```

```
/*
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```
*/
```

```
./board/micronas/vct/scc.c
```

```
scc.c
```

```
/*
```

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```
./board/micronas/vct/scc.h
```

```
scc.h
```

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```
./board/micronas/vct/smc_eeeprom.c
```

```
smc_eeeprom.c
```

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*/
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```
./board/micronas/vct/top.c
```

```
top.c
```

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```
*/
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```
./board/micronas/vct/vct.c
```

```
vct.c
```

```
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```

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```
./board/micronas/vct/vct.h
```

```
vct.h
```

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```

```
./board/micronas/vct/vctth/reg_dcgu.h
```

```
reg_dcgu.h
```

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```
./board/micronas/vct/vctth/reg_ebi.h
```

```
reg_ebi.h
```

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```
./board/micronas/vct/vctth/reg_fwsram.h
```

```
reg_fwsram.h
```

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```
./board/micronas/vct/vctth/reg_gpio.h
```

```
reg_gpio.h
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```
./board/micronas/vct/vctth/reg_scc.h
```

```
reg_scc.h
```

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```
./board/micronas/vct/vctth/reg_usbh.h
```

```
reg_usbh.h
```

```
/*
```

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```
./board/micronas/vct/vctth/reg_wdt.h
reg_wdt.h
```

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```
./board/micronas/vct/vctth2/reg_ebi.h
reg_ebi.h
```

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./board/micronas/vct/vctv/reg_dcgu.h
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```

```
./board/micronas/vct/vctv/reg_gpio.h
```

```
reg_gpio.h
```

```
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* MA 02111-1307 USA
*/
```

```
./board/micronas/vct/vctv/reg_wdt.h
```

```
reg_wdt.h
```

```
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*/
```

```
./board/mimc/mimc200/mimc200.c
```

```
mimc200.c
```

```
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 */
```

```
./board/miromico/hammerhead/hammerhead.c
hammerhead.c
```

```
/*
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 */
```

```
./board/mosaixtech/icon/chip_config.c
chip_config.c
```

```
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 */
```

```
./board/mosaixtech/icon/icon.c
icon.c
```

```
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 */
```

./board/motionpro/motionpro.c

motionpro.c

```
/*
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 *
 * modified for Promess PRO - by Andy Joseph, andy@promessdev.com
 * modified for Promess PRO-Motion - by Robert McCullough, rob@promessdev.com
 * modified by Chris M. Tumas 6/20/06 Change CAS latency to 2 from 3
 * Also changed the refresh for 100MHz operation
 *
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 * MA 02111-1307 USA
 */
```

./board/mousse/flash.c

flash.c

```
/*
 * MOUSSE/MPC8240 Board definitions.
 * Flash Routines for MOUSSE onboard AMD29LV106DB devices
 *
 * (C) Copyright 2000
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 * Sysgo Real-Time Solutions, GmbH <www.elinos.com>
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* MA 02111-1307 USA
*/
```

```
./board/mousse/flash.h
flash.h
/* PIO operations max */
/* 10 Seconds default */
/* Flash device info structure */
```

```
./board/mousse/m48t59y.c
m48t59y.c
```

```
/*
* SGS M48-T59Y TOD/NVRAM Driver
*
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```

*/

./board/mousse/m48t59y.h
m48t59y.h

/*

* SGS M48-T59Y TOD/NVRAM Driver

*

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*/

./board/mousse/mousse.c
mousse.c

/*

* MOUSSE Board Support

*

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* MA 02111-1307 USA
*/
```

```
./board/mousse/mousse.h
```

```
mouse.h
```

```
/*
* MOUSSE/MPC8240 Board definitions.
* For more info, see http://www.vooaha.com/
*
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*/
```

```
./board/mousse/pci.c
```

```
pci.c
```

```
/*
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```

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*/
```

```
./board/mpc8308_p1m/mpc8308_p1m.c
mpc8308_p1m.c
```

```
/*
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*/
```

```
./board/mpc8308_p1m/sdram.c
sdram.c
```

```
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 * Copyright (C) 2010 Ilya Yanok, Emcraft Systems, yanok@emcraft.com
 *
 * This files is mostly identical to the original from
 * board/freescale/mpc8308rdb/sdram.c
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 */
```

```
./board/mpl/common/common_util.c
common_util.c
```

```
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 */
```

```
*/  
  
./board/mpl/common/common_util.h  
common_util.h  
/*  
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*  
*/
```

```
./board/mpl/common/isa.c  
isa.c  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
```

```
* MA 02111-1307 USA
*
*
* TODO: clean-up
*/
```

```
./board/mpl/common/isa.h
```

```
isa.h
```

```
/*
```

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*/
```

```
./board/mpl/common/kbd.c
```

```
kbd.c
```

```
/*
```

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*
*
* Source partly derived from:
* linux/drivers/char/pc_keyb.c
*
*/
```

```
./board/mpl/common/kbd.h
kbd.h
```

```
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*
*/
```

```
./board/mpl/common/pci.c
pci.c
```

```
/*-----+
```

```
./board/mpl/common/pci_parts.h
pci_parts.h
```

```
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*/
```

```
./board/mpl/common/piix4_pci.h
```

```
piix4_pci.h
```

```
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*/
```

```
./board/mpl/common/usb_uhci.c
```

```
usb_uhci.c
```

```
/*
* Part of this code has been derived from linux:
* Universal Host Controller Interface driver for USB (take II).
*
* (c) 1999-2001 Georg Acher, acher@in.tum.de (executive slave) (base guitar)
* Deti Fliegl, deti@fliegl.de (executive slave) (lead voice)
```

```
*           Thomas Sailer, sailer@ife.ee.ethz.ch (chief consultant) (cheer
leader)
*           Roman Weissgaerber, weissg@vienna.at (virt root hub) (studio
porter)
* (c) 2000   Yggdrasil Computing, Inc. (port of new PCI interface support
*           from usb-ohci.c by Adam Richter, adam@yggdrasil.com).
* (C) 2000   David Brownell, david-b@pacbell.net (usb-ohci.c)
*
* HW-initialization based on material of
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* (C) Copyright 1999 Linus Torvalds
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```

```
./board/mpl/common/usb_uhci.h
```

```
usb_uhci.h
```

```
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* MA 02111-1307 USA
*
* Note: Part of this code has been derived from linux
*
*/
```

```
./board/mpl/mip405/cmd_mip405.c
cmd_mip405.c
```

```
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*
* hacked for MIP405
*/
```

```
./board/mpl/mip405/mip405.c
mip405.c
```

```
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*
*
* TODO: clean-up
*/
```

```
./board/mpl/mip405/mip405.h
```

```
mip405.h
```

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*
*/
```

```
./board/mpl/pati/cmd_pati.c
```

cmd_pati.c

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 *
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 */
```

./board/mpl/pati/pati.c

pati.c

```
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```
*/
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pati.h
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*/
/*****
* MACROS and register definitions for PATI Registers
*****/
```

```
./board/mpl/pati/pci_eeprom.h
pci_eeprom.h
/*****
* Eeprom Support
*****/
/*****
* Definitions
*****/
```

```
./board/mpl/pati/plx9056.h
plx9056.h
/*
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*/
/* PLX9096 register definitions
*/
```

```
./board/mpi/pip405/cmd_pip405.c
cmd_pip405.c
```

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*
* hacked for PIP405
*/
```

```
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pip405.c
```

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*
*
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*/
```

```
./board/mpl/pip405/pip405.h
```

```
pip405.h
```

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```

```
./board/mpl/vcma9/cmd_vcma9.c
```

```
cmd_vcma9.c
```

```
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```

```
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* adapted for VCMA9
* David Mueller, ELSOFT AG, d.mueller@elsoft.ch
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```

```
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```

```
vcma9.c
```

```
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*/

./board/mpi/vcma9/vcma9.h
vcma9.h

```
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 */
```

./board/mpr2/mpr2.c
mpr2.c

```
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 * board/mpr2/mpr2.c
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*/

./board/ms7720se/ms7720se.c

ms7720se.c

/*

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* board/ms7720se/ms7720se.c

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./board/ms7722se/ms7722se.c

ms7722se.c

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* board/ms7722se/ms7722se.c

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./board/ms7750se/ms7750se.c

ms7750se.c

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./board/muas3001/muas3001.c

muas3001.c

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```

```
./board/munices/mt48lc16m16a2-75.h
```

```
mt48lc16m16a2-75.h
```

```
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```
./board/munices/munices.c
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```
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```

```
flash.c
```

```
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```

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```

```
./board/musenki/musenki.c
```

```
musenki.c
```

```
/*
```

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```

```
./board/mvblue/flash.c
```

```
flash.c
```

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*/
```

```
./board/mvblue/mvblue.c
```

```
mvblue.c
```

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/*
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* MATRIX Vision GmbH / June 2002-Nov 2003
* Andre Schwarz
*/
```

```
./board/mx1ads/mx1ads.c
```

```
mx1ads.c
```

```
/*
* board/mx1ads/mx1ads.c
*
*/
```

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```

```
./board/mx1ads/syncflash.c
syncflash.c
```

```
/*
* board/mx1ads/syncflash.c
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```
./board/netphone/flash.c
```

flash.c

```
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./board/netphone/netphone.c

netphone.c

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./board/netphone/phone_console.c

phone_console.c

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 */
```

./board/netta/codec.c

codec.c

```
/*
 * CODEC
 */
/*****/
```

./board/netta/dsp.c

dsp.c

```
/*
 * Intracom TI6711/TI6412 DSP
 */
```

./board/netta/flash.c

flash.c

```
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```

```
./board/netta/netta.c
```

```
netta.c
```

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```

```
./board/netta/pcmcia.c
```

```
pcmcia.c
```

```
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```
./board/netta2/flash.c
```

```
flash.c
```

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```
./board/netta2/netta2.c
netta2.c
```

```
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*/
```

```
./board/netvia/flash.c
flash.c
```

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```

```
./board/netvia/netvia.c
```

```
netvia.c
```

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* MA 02111-1307 USA  
*/
```

```
./board/nokia/rx51/rx51.c
```

```
rx51.c
```

```
/*  
* (C) Copyright 2012  
* Ивайло Димитров <freemangordon@abv.bg>  
*  
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```

```
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*
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* Author :
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*/
```

```
./board/nokia/rx51/rx51.h
```

```
rx51.h
```

```
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*/
```

```
./board/nokia/rx51/tag_omap.h
```

```
tag_omap.h
```

```
/*
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*/
```

```
./board/ns9750dev/flash.c
```

```
flash.c
```

```
/*
```

```
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*/
```

```
./board/ns9750dev/led.c
```

```
led.c
```

```
/*****
```

```
*
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*
* $Id: led.c,v 1.1 2004/02/16 10:37:20 mpietrek Exp $
* @Author: Markus Pietrek
* @Descr: Defines helper functions for toggeling LEDs
* @Usage:
* @References: [1]
*
```

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*
*****/

./board/ns9750dev/ns9750dev.c
ns9750dev.c

```
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 * Markus Pietrek <mpietrek@fsforth.de>  
 * derived from omap1610innovator.c  
 * @References: [1] NS9750 Hardware Reference/December 2003  
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```

./board/nvidia/common/board.c
board.c

```
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 */
```

```
./board/nvidia/common/emc.c
```

```
emc.c
```

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```

```
./board/nvidia/common/emc.h
```

```
emc.h
```

```
/*
```

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```
./board/nvidia/common/uart-spi-switch.c
```

```
uart-spi-switch.c
```

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```

```
./board/nvidia/harmony/harmony.c
```

```
harmony.c
```

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```
./board/nvidia/seaboard/seaboard.c
seaboard.c
```

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```
./board/nvidia/whistler/whistler.c
whistler.c
```

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```

```
./board/nx823/flash.c
flash.c
```

```
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```

```
./board/nx823/nx823.c
```

nx823.c

```
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 * MA 02111-1307 USA
 */
```

./board/omicon/calimain/calimain.c

calimain.c

```
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 *
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```

```
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*/
```

```
./board/openrisc/openrisc-generic/openrisc-generic.c  
openrisc-generic.c
```

```
/*  
 * Based on nios2-generic.c:  
 * (C) Copyright 2005, Psyent Corporation <www.psyent.com>  
 * Scott McNutt <smcnutt@psyent.com>  
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
 * MA 02111-1307 USA  
 */
```

```
./board/overo/overo.c  
overo.c
```

```
/*  
 * Maintainer : Steve Sakoman <steve@sakoman.com>  
 *  
 * Derived from Beagle Board, 3430 SDP, and OMAP3EVM code by  
 * Richard Woodruff <r-woodruff2@ti.com>  
 * Syed Mohammed Khasim <khasim@ti.com>  
 * Sunil Kumar <sunilsaini05@gmail.com>  
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*/
```

```
./board/overo/overo.h
```

```
overo.h
```

```
/*
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*/
```

```
./board/palmld/palmld.c
```

```
palmld.c
```

```
/*
* Palm LifeDrive Support
*
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*/
```

```
./board/palmtc/palmtc.c
```

```
palmtc.c
```

```
/*
```

```
* Palm Tungsten|C Support
```

```
*
```

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```

```
*
```

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```
*/
```

```
./board/pandora/pandora.c
```

```
pandora.c
```

```
/*
```

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```
* Grazvydas Ignotas <notasas@gmail.com>
```

```
*
```

```
* Derived from Beagle Board, 3430 SDP, and OMAP3EVM code by
```

```
* Richard Woodruff <r-woodruff2@ti.com>
```

```
* Syed Mohammed Khasim <khasim@ti.com>
```

```
* Sunil Kumar <sunilsaini05@gmail.com>
```

```
* Shashi Ranjan <shashiranjanmca05@gmail.com>
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```
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```

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./board/pb1x00/flash.c
```

```
flash.c
```

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```
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```

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```
./board/pcippc2/cpc710.h  
cpc710.h
```

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```
./board/pcippc2/cpc710_init_ram.c  
cpc710_init_ram.c
```

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```
./board/pcippc2/cpc710_pci.c  
cpc710_pci.c
```

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```
./board/pcippc2/cpc710_pci.h  
cpc710_pci.h  
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```

```
./board/pcippc2/flash.c  
flash.c  
/*
```

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```
./board/pcippc2/fpga_serial.c
```

```
fpga_serial.c
```

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```
./board/pcippc2/fpga_serial.h
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fpga_serial.h
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```
./board/pcippc2/hardware.h
```

```
hardware.h
```

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```
./board/pcippc2/i2c.c
```

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```

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```
./board/pcippc2/i2c.h
```

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```
./board/pcippc2/ns16550.h
```

```
ns16550.h
```

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```
./board/pcippc2/pcippc2.c
```

```
pcippc2.c
```

```
/*
```

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./board/pcippc2/pcippc2.h
```

```
pcippc2.h
```

```
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```
./board/pcippc2/pcippc2_fpga.c  
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```
./board/pcippc2/sconsole.c  
sconsole.c
```

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```
./board/pcs440ep/flash.c
```

```
flash.c
```

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```
./board/pcs440ep/pcs440ep.c
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```
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```

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```
./board/pdm360ng/pdm360ng.c  
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```

```
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```

```
./board/phytec/pcm030/mt46v32m16-75.h
```

mt46v32m16-75.h

```
/*
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 * Eric Schumann, Phytex Messtechnik
 * adapted for mt46v32m16-75 DDR-RAM
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./board/phytec/pcm030/pcm030.c

pcm030.c

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*/
```

```
./board/pm520/flash.c  
flash.c
```

```
/*  
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```

```
./board/pm520/mt46v16m16-75.h  
mt46v16m16-75.h
```

```
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```

```
./board/pm520/mt48lc16m16a2-75.h
```

```
mt48lc16m16a2-75.h
```

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```
./board/pm520/pm520.c
```

```
pm520.c
```

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```

```
./board/pm826/flash.c
```

```
flash.c
```

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```
./board/pm826/pm826.c
```

```
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```

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```

```
./board/pm828/flash.c
flash.c
```

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```

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```
./board/pn62/cmd_pn62.c
cmd_pn62.c
```

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```
./board/pn62/misc.c
misc.c
```

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```
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```

```
pn62.c
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```
./board/pn62/pn62.h
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pn62.h
```

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```

```
./board/ppmc7xx/flash.c
flash.c
```

```
/*
* flash.c
* -----
*
* Flash programming routines for the Wind River PPMC 74xx/7xx
* based on flash.c from the TQM8260 board.
*
* By Richard Danter (richard.danter@windriver.com)
* Copyright (C) 2005 Wind River Systems
*/
```

```
./board/ppmc7xx/pci.c
pci.c
```

```
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* Frank Gottschling <fgottschling@eltec.de>
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```

```
./board/ppmc7xx/ppmc7xx.c
```

```
ppmc7xx.c
```

```
/*
```

```
* ppmc7xx.c
```

```
* -----
```

```
*
```

```
* Main board-specific routines for Wind River PPMC 7xx/74xx board.
```

```
*
```

```
* By Richard Danter (richard.danter@windriver.com)
```

```
* Copyright (C) 2005 Wind River Systems
```

```
*/
```

```
./board/ppmc8260/ppmc8260.c
```

```
ppmc8260.c
```

```
/*
```

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*/
```

```
./board/ppmc8260/strataflash.c
```

```
strataflash.c
```

```
/*
```

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```

```
./board/pr1/pr1.c
```

```
pr1.c
```

```
/*
* U-boot - main board file
*
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*
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```
./board/prodrive/alpr/alpr.c
```

```
alpr.c
```

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```
./board/prodrive/alpr/fpga.c
fpga.c
```

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```

```
./board/prodrive/alpr/nand.c
nand.c
```

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```
./board/prodrive/common/flash.c
```

```
flash.c
```

```
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```

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```
./board/prodrive/common/fpga.c
```

```
fpga.c
```

```
/*
```

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```

```
./board/prodrive/p3mx/64460.h
64460.h
```

```
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```
./board/prodrive/p3mx/eth.h
eth.h
```

```
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```
./board/prodrive/p3mx/mpsc.c
```

```
mpsc.c
```

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```
./board/prodrive/p3mx/mpsc.h
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```
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```
./board/prodrive/p3mx/mv_eth.c
```

```
mv_eth.c
```

```
/*
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*
* based on - Driver for MV64460X ethernet ports
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```
./board/prodrive/p3mx/mv_eth.h
```

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```

```
./board/prodrive/p3mx/mv_regs.h
```

```
mv_regs.h
```

```
/*
* (C) Copyright 2003
* Ingo Assmus <ingo.assmus@keymile.com>
*
* based on - Driver for MV64460X ethernet ports
* Copyright (C) 2002 rabeeh@galileo.co.il
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```

```
./board/prodrive/p3mx/p3mx.c
```

```
p3mx.c
```

```
/*
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*
* Based on original work by
* Roel Loeffen, (C) Copyright 2006 Prodrive B.V.
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*
* modifications for the DB64360 eval board based by Ingo.Assmus@keymile.com
* modifications for the pci750 by reinhard.arlt@esd-electronics.com
* modifications for the P3M750 by roel.loeffen@prodrive.nl
*/
```

```
./board/prodrive/p3mx/p3mx.h
p3mx.h
```

```
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*
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*/
```

```
./board/prodrive/p3mx/pci.c
```

```
pci.c
```

```
/*
```

```
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
```

```
*
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```
*
```

```
*/
```

```
/* PCI.c - PCI functions */
```

```
./board/prodrive/p3mx/ppc_error_no.h
```

```
ppc_error_no.h
```

```
/*
```

```
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* Ingo Assmus <ingo.assmus@keymile.com>
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```

```
./board/prodrive/p3mx/sdram_init.c
sdram_init.c
```

```
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*/
```

```
./board/prodrive/p3mx/serial.c
serial.c
```

```
/*
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 *
 * modified for marvell db64360 eval board by
 * Ingo Assmus <ingo.assmus@keymile.com>
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```
./board/prodrive/p3mx/serial.h
serial.h
```

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```
./board/prodrive/p3p440/p3p440.c
p3p440.c
```

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```
./board/prodrive/p3p440/p3p440.h
p3p440.h
```

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```
./board/prodrive/pdnb3/flash.c
flash.c
```

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```
./board/prodrive/pdnb3/nand.c
nand.c
```

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./board/prodrive/pdnb3/pdnb3.c
pdnb3.c
```

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```
./board/psyent/common/AMDLV065D.c
AMDLV065D.c
```

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```
./board/psyent/pci5441/pci5441.c
pci5441.c
```

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```
./board/psyent/pk1c20/led.c
```

```
led.c
```

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```
./board/psyent/pk1c20/pk1c20.c
```

```
pk1c20.c
```

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```
./board/pxa255_idp/pxa_idp.c
```

```
pxa_idp.c
```

```
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```
./board/qemu-mips/qemu-mips.c
```

```
qemu-mips.c
```

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```
./board/qi/qi_lb60/qi_lb60.c
qi_lb60.c
```

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```
./board/quad100hd/nand.c
nand.c
```

```
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```
./board/quad100hd/quad100hd.c
quad100hd.c
```

```
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```

```
./board/quantum/fpga.c
fpga.c
```

```
/*
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* Stefan Roese, esd gmbh germany, stefan.roese@esd-electronics.com
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*/
/* The DEBUG define must be before common to enable debugging */

./board/quantum/fpga.h
fpga.h
/*
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* Keith Outwater, keith_outwater@mvis.com.
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```

```
./board/r360mpi/flash.c
```

```
flash.c
```

```
/*
```

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```
./board/r360mpi/pcmcia.c
```

```
pcmcia.c
```

```
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```

```
./board/r360mpi/r360mpi.c
```

```
r360mpi.c
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 */
```

```
./board/raidsonic/ib62x0/ib62x0.c
ib62x0.c
```

```
/*
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 * Luka Perkov <luka@openwrt.org>
 * Simon Baatz <gmbnomis@gmail.com>
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 */
```

```
./board/raidsonic/ib62x0/ib62x0.h
ib62x0.h
```

```
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 * Simon Baatz <gmbnomis@gmail.com>
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 */
```

```
./board/raspberrypi/rpi_b/rpi_b.c
rpi_b.c
```

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 */
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```
./board/rattler/rattler.c
rattler.c
```

```
/*
 * Copyright (C) 2004 Arabella Software Ltd.
 * Yuli Barcohen <yuli@arabellasw.com>
 *
 * Support for Analogue&Micro Rattler boards family.
 * Tested on Rattler8248.
 */
```

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*/
```

```
./board/rbc823/flash.c
```

```
flash.c
```

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*/
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```
./board/rbc823/kbd.c
```

```
kbd.c
```

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*/
```

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```
./board/rbc823/rbc823.c
```

```
rbc823.c
```

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*/
```

```
./board/renesas/ap325rxa/ap325rxa.c
```

```
ap325rxa.c
```

```
/*
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*
*/
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*/
```

```
./board/renesas/ap325rxa/cpld-ap325rxa.c
cpld-ap325rxa.c
```

```
/******
```

```
* Project:
*       CPLD SlaveSerial Configuration via embedded microprocessor.
```

```
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```

```
* Description:
```

```
*       This is the main source file that will allow a microprocessor
*       to configure Xilinx Virtex, Virtex-E, Virtex-EM, Virtex-II,
*       and Spartan-II devices via the SlaveSerial Configuration Mode.
*       This code is discussed in Xilinx Application Note, XAPP502.
```

```
* History:
```

```
*       3-October-2001  MN/MP - Created
*       20-August-2008  Renesas Solutions - Modified to SH7723
```

```
*****/
```

```
./board/renesas/ecovec/ecovec.c
ecovec.c
```

```
/*
```

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*
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*/
```

```
./board/renesas/MigoR/migo_r.c
migo_r.c
```

```
/*
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*
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*
* board/MigoR/migo_r.c
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*/
```

```
./board/renesas/r0p7734/r0p7734.c
r0p7734.c
```

```
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```

```
./board/renesas/r2dplus/r2dplus.c
```

```
r2dplus.c
```

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```

```
./board/renesas/r7780mp/r7780mp.c
```

```
r7780mp.c
```

```
/*  
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```

```
./board/renesas/r7780mp/r7780mp.h
r7780mp.h
```

```
/*
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*
* u-boot/board/r7780mp/r7780mp.h
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*/
```

```
./board/renesas/rsk7203/rsk7203.c
rsk7203.c
```

```
/*
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*/
```

```
./board/renesas/rsk7264/rsk7264.c
```

```
rsk7264.c
```

```
/*
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*
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*
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```

```
./board/renesas/rsk7269/rsk7269.c
```

```
rsk7269.c
```

```
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```

```
./board/renesas/sh7752evb/sh7752evb.c
```

```
sh7752evb.c
```

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```

```
./board/renesas/sh7752evb/spi-boot.c
spi-boot.c
```

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```
./board/renesas/sh7757lcr/sh7757lcr.c
sh7757lcr.c
```

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```
./board/renesas/sh7757lcr/spi-boot.c
spi-boot.c
```

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```
./board/renesas/sh7763rdp/sh7763rdp.c
sh7763rdp.c
```

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 * board/sh7763rdp/sh7763rdp.c
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```

```
./board/renesas/sh7785lcr/rtl8169.h
rtl8169.h
```

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```
./board/renesas/sh77851cr/rtl8169_mac.c  
rtl8169_mac.c
```

```
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```

```
./board/renesas/sh77851cr/selfcheck.c  
selfcheck.c
```

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```
./board/renesas/sh77851cr/sh77851cr.c  
sh77851cr.c
```

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*/
```

```
./board/ronetix/pm9261/led.c
led.c
```

```
/*
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* Lead Tech Design <www.leadtechdesign.com>
* Ilko Iliev <www.ronetix.at>
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*/
```

```
./board/ronetix/pm9261/partition.c
partition.c
```

```
/*
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```

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*/

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pm9261.c
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*/

./board/ronetix/pm9263/led.c
led.c
/*
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```

```
* Stelian Pop <stelian@popies.net>
* Lead Tech Design <www.leadtechdesign.com>
* Ilko Iliev <www.ronetix.at>
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```

```
./board/ronetix/pm9263/partition.c
partition.c
```

```
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* Ilko Iliev <www.ronetix.at>
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```

```
./board/ronetix/pm9263/pm9263.c
pm9263.c
/*
```

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*/
```

```
./board/ronetix/pm9g45/pm9g45.c
```

```
pm9g45.c
```

```
/*
```

```
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* Asen Dimov <dimov@ronetix.at>
* Ronetix GmbH <www.ronetix.at>
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```

```
./board/RPXClassic/eccx.c
```

```
eccx.c
```

```
/*
```

```
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* Pierre AUBERT p.aubert@staubli.com
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*/
```

```
/* Video support for the ECCX daughter board
```

```
*/
```

```
./board/RPXClassic/flash.c
```

```
flash.c
```

```
/*
```

```
* (C) Copyright 2001
* Stäubli Faverges - <www.staubli.com>
* Pierre AUBERT p.aubert@staubli.com
* U-Boot port on RPXClassic LF (CLLF_BW31) board
*
* RPXClassic uses Am29DL323B flash memory with 2 banks
*
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```

```
./board/RPXClassic/RPXClassic.c
```

```
RPXClassic.c
```

```
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* Pierre AUBERT p.aubert@staubli.com
* U-Boot port on RPXClassic LF (CLLF_BW31) board
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```

```
./board/RPXlite/flash.c
```

```
flash.c
```

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```
./board/RPXLite/RPXLite.c
```

```
RPXLite.c
```

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```

```
./board/RPXLite_dw/flash.c
```

```
flash.c
```

```
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```

```
./board/RPXlite_dw/RPXlite_dw.c
```

```
RPXlite_dw.c
```

```
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*/
```

```
./board/rpxsuper/flash.c
```

```
flash.c
```

```
/*  
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*
* Flash Routines for AMD 29F080B devices
* Added support for 64bit and AMD 29DL323B
*
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```

```
./board/rpxsuper/mii_phy.c
mii_phy.c
No license information found.
```

```
./board/rpxsuper/rpxsuper.c
rpxsuper.c
/*
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```

```
./board/rpxsuper/rpxsuper.h
rpxsuper.h
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```

```
./board/RRvision/flash.c
flash.c
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```
./board/RRvision/RRvision.c
RRvision.c
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```
./board/RRvision/video_ad7179.h
video_ad7179.h
```

```
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```

```
./board/rsdproto/flash.c
flash.c
```

```
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* Flash Routines for AM290[48]0B devices
*
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./board/rsdproto/rsdproto.c
rsdproto.c
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./board/sacsng/clkinit.c
```

clkinit.c

```
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 * Jon Diekema <diekema@cideas.com>
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 */
```

./board/sacsng/clkinit.h

clkinit.h

```
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 */
```

./board/sacsng/flash.c

flash.c

```
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 */
```

./board/sacsng/ioconfig.h

ioconfig.h

```
/*
 * I/O Port configuration table
 *
 * If conf is 1, then that port pin will be configured at boot time
 * according to the five values podr/pdir/ppar/psor/pdat for that entry
 */
```

./board/sacsng/sacsng.c

sacsng.c

```
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 * Gerald Van Baren <vanbaren@cideas.com>
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```

```
./board/samsung/common/multi_i2c.c
```

```
multi_i2c.c
```

```
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```

```
./board/samsung/goni/goni.c
```

```
goni.c
```

```
/*  
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```

```
./board/samsung/goni/onenand.c
onenand.c
```

```
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```
./board/samsung/origen/mmc_boot.c
mmc_boot.c
```

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*/
```

```
./board/samsung/origen/origen.c
```

```
origen.c
```

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*/
```

```
./board/samsung/origen/origen_setup.h
```

```
origen_setup.h
```

```
/*  
* Machine Specific Values for ORIGEN board based on S5PV310  
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```

```
./board/samsung/origen/tools/mkv310_image.c
```

```
mkv310_image.c
```

```
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*/
```

```
./board/samsung/smdk2410/smdk2410.c
```

```
smdk2410.c
```

```
/*  
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* Marius Groeger <mgroeger@sysgo.de>  
*  
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*/
```

```
./board/samsung/smdk5250/clock_init.c
clock_init.c
```

```
/*
* Clock setup for SMDK5250 board based on EXYNOS5
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* MA 02111-1307 USA
*/
```

```
./board/samsung/smdk5250/clock_init.h
clock_init.h
```

```
/*
* Clock initialization routines
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*
*/
```

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*/
```

```
./board/samsung/smdk5250/dmc_common.c
```

```
dmc_common.c
```

```
/*
* Mem setup common file for different types of DDR present on SMDK5250 boards.
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*/
```

```
./board/samsung/smdk5250/dmc_init_ddr3.c
```

```
dmc_init_ddr3.c
```

```
/*
* DDR3 mem setup file for SMDK5250 board based on EXYNOS5
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```

```
./board/samsung/smdk5250/setup.h  
setup.h
```

```
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```

```
./board/samsung/smdk5250/smdk5250.c  
smdk5250.c
```

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*/
```

```
./board/samsung/smdk5250/smdk5250_spl.c  
smdk5250_spl.c
```

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```

```
./board/samsung/smdk5250/spl_boot.c  
spl_boot.c
```

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```

```
./board/samsung/smdk5250/tzpc_init.c
tzpc_init.c
```

```
/*
* Lowlevel setup for SMDK5250 board based on S5PC520
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```

```
./board/samsung/smdk6400/smdk6400.c
smdk6400.c
```

```
/*
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* Marius Groeger <mgroeger@sysgo.de>
*
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*
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*/
```

```
./board/samsung/smdk6400/smdk6400_nand_spl.c
smdk6400_nand_spl.c
```

```
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```

```
./board/samsung/smdkc100/onenand.c
```

```
onenand.c
```

```
/*
```

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```

```
./board/samsung/smdkc100/smdkc100.c
```

```
smdkc100.c
```

```
/*
```

```
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* Kyungmin Park <kyungmin.park@samsung.com>
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```

```
./board/samsung/smdkv310/mmc_boot.c
```

```
mmc_boot.c
```

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```
./board/samsung/smdkv310/smdkv310.c
```

```
smdkv310.c
```

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```

```
./board/samsung/smdkv310/tools/mkv310_image.c
mkv310_image.c
```

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*/
```

```
./board/samsung/trats/setup.h
setup.h
```

```
/*
 * Machine Specific Values for TRATS board based on EXYNOS4210
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```

```
./board/samsung/trats/trats.c
```

```
trats.c
```

```
/*
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 * Kyungmin Park <kyungmin.park@samsung.com>
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```

```
./board/samsung/universal_c210/onenand.c
```

```
onenand.c
```

```
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```

```
./board/samsung/universal_c210/universal.c
universal.c
```

```
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 * Kyungmin Park <kyungmin.park@samsung.com>
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```
./board/sandbox/sandbox/sandbox.c
sandbox.c
```

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```

```
./board/sandburst/common/flash.c
flash.c
```

```
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*/
/*
* Ported from Ebony flash support
* Travis B. Sawyer
* Sandburst Corporation
*/
```

```
./board/sandburst/common/ppc440gx_i2c.c
ppc440gx_i2c.c
```

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```

```
./board/sandburst/common/ppc440gx_i2c.h
```

```
ppc440gx_i2c.h
```

```
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```

```
./board/sandburst/common/sb_common.c
```

```
sb_common.c
```

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```
./board/sandburst/common/sb_common.h
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```

```
./board/sandburst/karef/hal_ka_of_auto.h
hal_ka_of_auto.h
```

```
/* *****
* Common defs for reg spec for chip ka_of
* Auto-generated by trex2: DO NOT HAND-EDIT!!
* *****
*/
/* -----
* For block: 'ofem'
*/
```

```
./board/sandburst/karef/hal_ka_sc_auto.h
hal_ka_sc_auto.h
```

```
/* *****
```

```
* Common defs for reg spec for chip ka_sc
* Auto-generated by trex2: DO NOT HAND-EDIT!!
* *****
*/
/* -----
* For block: 'scan'
*/
```

```
./board/sandburst/karef/karef.c
karef.c
```

```
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```

```
./board/sandburst/karef/karef.h
karef.h
```

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```
./board/sandburst/karef/karef_version.h
karef_version.h
```

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```

```
./board/sandburst/metrobox/hal_xc_auto.h
hal_xc_auto.h
```

```
/* *****
* Common defs for reg spec for chip xc
* Auto-generated by trex2: DO NOT HAND-EDIT!!
* *****
*/
/* -----
* For block: 'xcvr_cntl'
*/
```

```
./board/sandburst/metrobox/metrobox.c
metrobox.c
```

```
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```
./board/sandburst/metrobox/metrobox.h
metrobox.h
```

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```
./board/sandburst/metrobox/metrobox_version.h
metrobox_version.h
```

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```

```
./board/sandpoint/flash.c
flash.c
```

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```
./board/sandpoint/sandpoint.c
sandpoint.c
```

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```
./board/sandpoint/speed.h
speed.h
```

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```
./board/sbc405/sbc405.c
sbc405.c
```

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```

```
./board/sbc405/strataflash.c
strataflash.c
```

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```

```
./board/sbc8349/pci.c
pci.c
```

```
/*
* pci.c -- WindRiver SBC8349 PCI board support.
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```

```
*
* Based on MPC8349 PCI support but w/o PIB related code.
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*/
```

```
./board/sbc8349/sbc8349.c
```

```
sbc8349.c
```

```
/*
* sbc8349.c -- WindRiver SBC8349 board support.
* Copyright (c) 2006-2007 Wind River Systems, Inc.
*
* Paul Gortmaker <paul.gortmaker@windriver.com>
* Based on board/mpc8349emds/mpc8349emds.c (and previous 834x releases.)
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./board/sbc8548/ddr.c
ddr.c

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law.c

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./board/sbc8548/sbc8548.c
sbc8548.c

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```
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```

```
tlb.c
```

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```

```
ddr.c
```

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```

```
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sbc8641d.c
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```

```
./board/sc3/sc3.c
```

```
sc3.c
```

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*
* $Log:$
*/
```

```
./board/sc3/sc3.h
```

sc3.h

```
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./board/sc3/sc3nand.c

sc3nand.c

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./board/scb9328/flash.c

flash.c

```
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 *
 * Written by Peter Figuli <peposh@etc.sk>, 2003.
 */
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./board/scb9328/intel.h

intel.h

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* Written by Marcel Telka <marcel@telka.sk>, 2002.
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* Documentation:
* [1] Intel Corporation, "3 Volt Intel Strata Flash Memory 28F128J3A, 28F640J3A,
*     28F320J3A (x8/x16)", April 2002, Order Number: 290667-011
* [2] Intel Corporation, "3 Volt Synchronous Intel Strata Flash Memory 28F640K3,
28F640K18,
*     28F128K3, 28F128K18, 28F256K3, 28F256K18 (x16)", June 2002, Order Number:
290737-005
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```

```
./board/scb9328/scb9328.c
scb9328.c
```

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*/
```

```
./board/schulercontrol/sc_sps_1/sc_sps_1.c
sc_sps_1.c
```

```
/*
* SchulerControl GmbH, SC_SPS_1 module
*
* Copyright (C) 2012 Marek Vasut <marex@denx.de>
```

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```

```
./board/schulercontrol/sc_sps_1/spl_boot.c
spl_boot.c
```

```
/*
* SchulerControl GmbH, SC_SPS_1 module setup
*
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```

```
./board/Seagate/dockstar/dockstar.c
dockstar.c
```

```
/*
 * Copyright (C) 2010 Eric C. Cooper <ecc@cmu.edu>
 *
 * Based on sheevaplug.c originally written by
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 */
```

```
./board/Seagate/dockstar/dockstar.h
dockstar.h
```

```
/*
 * Copyright (C) 2010 Eric C. Cooper <ecc@cmu.edu>
 *
 * Based on sheevaplug.h originally written by
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```

```
./board/sheldon/simpc8313/sdram.c
```

```
sdram.c
```

```
/*
```

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```
*
```

```
* Author: Ron Madrid <info@sheldoninst.com>
```

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*
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*/
```

```
./board/sheldon/simpc8313/simpc8313.c
```

```
simpc8313.c
```

```
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```

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```
./board/shmin/shmin.c
```

```
shmin.c
```

```
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*
* board/shmin/shmin.c
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*
* Copy board_flash_get_legacy() from board/freescale/m54455evb/m54455evb.c
*/
```

```
./board/sixnet/flash.c
```

```
flash.c
```

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```

```
./board/sixnet/fpgadata.c
fpgadata.c
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```

```
./board/sixnet/sixnet.c
sixnet.c
```

```
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```

```
./board/sixnet/sixnet.h
sixnet.h
/*
```

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```

```
./board/snmc/qs850/flash.c
flash.c
```

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./board/snmc/qs850/qs850.c

qs850.c

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./board/snmc/qs860t/flash.c

flash.c

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```
./board/snmc/qs860t/qs860t.c
qs860t.c
```

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```
./board/socrates/ddr.c
ddr.c
```

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./board/socrates/law.c

law.c

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./board/socrates/nand.c

nand.c

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```
./board/socrates/sdram.c
```

```
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```

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```
./board/socrates/socrates.c
```

```
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```

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```
./board/socrates/tlb.c
```

```
tlb.c
```

```
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```

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```
./board/socrates/upm_table.h
```

```
upm_table.h
```

```
/*
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```
./board/sorcery/sorcery.c
sorcery.c
```

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```
./board/spc1920/hpi.c
hpi.c
```

```
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```
./board/spc1920/hpi.h
```

```
hpi.h
```

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```

```
./board/spc1920/pld.h
```

```
pld.h
```

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```
./board/spc1920/spc1920.c  
spc1920.c
```

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 */
```

```
./board/spd8xx/flash.c  
flash.c
```

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```
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```

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```
./board/spear/common/spr_misc.c  
spr_misc.c
```

```
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```

```
./board/spear/spear300/spear300.c
```

```
spear300.c
```

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```

```
./board/spear/spear310/spear310.c
```

```
spear310.c
```

```
/*
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```

```
./board/spear/spear320/spear320.c
```

```
spear320.c
```

```
/*
```

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```

```
./board/spear/spear600/spear600.c
```

```
spear600.c
```

```
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```

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```

```
./board/spear/x600/fpga.c
```

```
fpga.c
```

```
/*
```

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```
*/
```

```
./board/spear/x600/fpga.h
```

```
fpga.h
```

```
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```

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```
./board/spear/x600/x600.c
```

```
x600.c
```

```
/*
```

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```

```
./board/st/nhk8815/nhk8815.c
```

```
nhk8815.c
```

```
/*
```

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```

```
./board/st-ericsson/snowball/db8500_pins.h
```

```
db8500_pins.h
```

```
/*  
* Copyright (C) ST-Ericsson SA 2010  
* Code ported from Nomadik GPIO driver in ST-Ericsson Linux kernel code.  
* The purpose is that GPIO config found in kernel should work by simply  
* copy-paste it to U-boot.  
*  
* Ported to U-boot by:  
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* Author: Rabin Vincent <rabin.vincent@stericsson.com>  
*/
```

```
./board/st-ericsson/snowball/snowball.c
```

```
snowball.c
```

```
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*/
```

```
./board/st-ericsson/u8500/gpio.c
```

```
gpio.c
```

```
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```
./board/st-ericsson/u8500/u8500_href.c
u8500_href.c
```

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```

```
./board/stx/stxgp3/ddr.c
ddr.c
```

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./board/stx/stxgp3/flash.c

flash.c

/*

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* Updated to support the Silicon Tx GP3 8560. We should only find

* two Intel 28F640 parts in 16-bit mode (i.e. 32-bit wide flash),

* but I left other code here in case people order custom boards.

*

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*

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* Add support the Sharp chips on the mpc8260ads.

* I started with board/ip860/flash.c and made changes I found in

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*

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./board/stx/stxgp3/law.c

law.c

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```

```
./board/stx/stxgp3/stxgp3.c
stxgp3.c
```

```
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```
./board/stx/stxgp3/tlb.c
tlb.c
```

```
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```
./board/stx/stxssa/ddr.c
ddr.c
```

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./board/stx/stxssa/law.c
law.c
```

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```

```
./board/stx/stxssa/stxssa.c
stxssa.c
```

```
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./board/stx/stxssa/tlb.c
tlb.c
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```
./board/stx/stxxtc/stxxtc.c
```

```
stxxtc.c
```

```
/*
```

```
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```
./board/svm_sc8xx/flash.c
```

flash.c

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./board/svm_sc8xx/svm_sc8xx.c

svm_sc8xx.c

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./board/syteco/jadecpu/jadecpu.c

jadecpu.c

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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./board/syteco/zmx25/zmx25.c
zmx25.c
```

```
/*
 * (c) 2011 Graf-Syteco, Matthias Weisser
 * <weisserm@arcor.de>
 *
 * Based on tx25.c:
 * (C) Copyright 2009 DENX Software Engineering
 * Author: John Rigby <jrigby@gmail.com>
 *
 * Based on imx27lite.c:
 * Copyright (C) 2008,2009 Eric Jarrige <jorasse@users.sourceforge.net>
 * Copyright (C) 2009 Ilya Yanok <yanok@emcraft.com>
 * And:
 * RedBoot tx25_misc.c Copyright (C) 2009 Red Hat
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*
*/
```

```
./board/t3corp/chip_config.c
chip_config.c
```

```
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* MA 02111-1307 USA
*
*/
```

```
./board/t3corp/t3corp.c
t3corp.c
```

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```

```
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./board/taskit/stamp9g20/led.c  
led.c
```

```
/*  
* Copyright (c) 2009 Wind River Systems, Inc.  
* Tom Rix <Tom.Rix@windriver.com>  
* (C) Copyright 2009  
* Eric Benard <eric@eukrea.com>  
*  
* (C) Copyright 2012  
* Markus Hubig <mhubig@imko.de>  
* IMKO GmbH <www.imko.de>  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./board/taskit/stamp9g20/stamp9g20.c  
stamp9g20.c
```

```
/*  
* (C) Copyright 2007-2008  
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* Lead Tech Design <www.leadtechdesign.com>  
*  
* Achim Ehrlich <aehrich@taskit.de>  
* taskit GmbH <www.taskit.de>  
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* Markus Hubig <mhubig@imko.de>  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./board/tcm-bf518/tcm-bf518.c
```

```
tcm-bf518.c
```

```
/*  
* U-boot - main board file  
*  
* Copyright (c) 2008-2009 Analog Devices Inc.  
*  
* Licensed under the GPL-2 or later.  
*/
```

```
./board/tcm-bf537/gpio_cfi_flash.c
```

```
gpio_cfi_flash.c
```

```
No license information found.
```

```
./board/tcm-bf537/tcm-bf537.c
```

```
tcm-bf537.c
```

```
/*  
* U-boot - main board file  
*  
* Copyright (c) 2005-2009 Analog Devices Inc.  
*  
* Licensed under the GPL-2 or later.  
*/
```

```
./board/technexion/twister/twister.c
```

```
twister.c
```

```
/*  
* Copyright (C) 2011  
* Stefano Babic, DENX Software Engineering, sbabic@denx.de.  
*  
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*  
*/
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*/
```

```
./board/technexion/twister/twister.h
twister.h
```

```
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*/
```

```
./board/teejet/mt_ventoux/mt_ventoux.c
mt_ventoux.c
```

```
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```

```
*/
```

```
./board/teejet/mt_ventoux/mt_ventoux.h
```

```
mt_ventoux.h
```

```
/*
```

```
* Copyright (C) 2011 Stefano Babic <sbabic@denx.de>
```

```
*
```

```
* Author: Hardy Weng <hardy.weng@technexion.com>
```

```
*
```

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```

```
*/
```

```
./board/ti/am335x/board.c
```

```
board.c
```

```
/*
```

```
* board.c
```

```
*
```

```
* Board functions for TI AM335X based boards
```

```
*
```

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```

```
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```

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```

```
*/
```

```
./board/ti/am335x/board.h
```

```
board.h
```

```
/*
```

```
* board.h
```

```
*
```

```
* TI AM335x boards information header
```

```
*
```

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```

```
*/
```

```
./board/ti/am335x/mux.c
```

```
mux.c
```

```
/*
```

```
* mux.c
```

```
*
```

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```

```
*/
```

```
./board/ti/am3517crane/am3517crane.c
```

```
am3517crane.c
```

```
/*
```

```
* am3517crane.c - board file for AM3517 CraneBoard
```

```
*
* Author: Srinath.R <srinath@mistralsolutions.com>
*
* Based on logicpd/am3517evm/am3517evm.c
*
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*/
```

```
./board/ti/am3517crane/am3517crane.h
am3517crane.h
```

```
/*
* am3517crane.h - Header file for the AM3517 CraneBoard.
*
* Author: Srinath R <srinath@mistralsolutions.com>
*
* Based on logicpd/am3517evm/am3517evm.h
*
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*/
```

```
./board/ti/beagle/beagle.c
beagle.c
```

```
/*
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 *
 * Author :
 *     Sunil Kumar <sunilsaini05@gmail.com>
 *     Shashi Ranjan <shashiranjanmca05@gmail.com>
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 * Derived from Beagle Board and 3430 SDP code by
 *     Richard Woodruff <r-woodruff2@ti.com>
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./board/ti/beagle/beagle.h
beagle.h
```

```
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*/
```

```
./board/ti/beagle/led.c
```

```
led.c
```

```
/*
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* Jason Kridner <jkridner@beagleboard.org>
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*/
```

```
./board/ti/evm/evm.c
```

```
evm.c
```

```
/*
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* Texas Instruments, <www.ti.com>
*
* Author :
*     Manikandan Pillai <mani.pillai@ti.com>
*
* Derived from Beagle Board and 3430 SDP code by
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*/
```

```
./board/ti/evm/evm.h
```

```
evm.h
```

```
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*/
```

```
./board/ti/omap1510inn/omap1510innovator.c
```

```
omap1510innovator.c
```

```
/*  
* (C) Copyright 2002  
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>  
* Marius Groeger <mgroeger@sysgo.de>  
*  
* (C) Copyright 2002  
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*  
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*/
```

```
./board/ti/omap2420h4/mem.c
```

```
mem.c
```

```
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* Richard Woodruff <r-woodruff2@ti.com>
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```

```
./board/ti/omap2420h4/omap2420h4.c
```

```
omap2420h4.c
```

```
/*
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* Richard Woodruff <r-woodruff2@ti.com>
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* MA 02111-1307 USA  
*/
```

```
./board/ti/omap2420h4/sys_info.c
```

```
sys_info.c
```

```
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*/
```

```
./board/ti/omap5912osk/omap5912osk.c
```

```
omap5912osk.c
```

```
/*  
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* Sysgo Real-Time Solutions, GmbH <www.elinos.com>  
* Marius Groeger <mgroeger@sysgo.de>  
*  
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* David Mueller, ELSOFT AG, <d.mueller@elsoft.ch>
```

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```

```
./board/ti/omap5_evm/evm.c
```

```
evm.c
```

```
/*
```

```
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```

```
./board/ti/omap5_evm/mux_data.h
```

```
mux_data.h
```

```
/*
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 * Texas Instruments Incorporated, <www.ti.com>
 *
 *      Sricharan R          <r.sricharan@ti.com>
 *
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*/
```

```
./board/ti/omap730p2/flash.c
```

```
flash.c
```

```
/*
 * (C) Copyright 2001
 * Kyle Harris, Nexus Technologies, Inc. kharris@nexus-tech.net
 *
 * (C) Copyright 2001
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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*/
```

```
./board/ti/omap730p2/omap730p2.c
omap730p2.c
```

```
/*
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* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*
* (C) Copyright 2002
* David Mueller, ELSOFT AG, <d.mueller@elsoft.ch>
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* MA 02111-1307 USA
*/
```

```
./board/ti/panda/panda.c
panda.c
/*
```

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* Steve Sakoman <steve@sakoman.com>
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* MA 02111-1307 USA
*/
```

```
./board/ti/panda/panda_mux_data.h
panda_mux_data.h
```

```
/*
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* Texas Instruments Incorporated, <www.ti.com>
*
* Balaji Krishnamoorthy <balajitk@ti.com>
* Aneesh V <aneesh@ti.com>
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```

```
./board/ti/sdp3430/sdp.c
```

```
sdp.c
```

```
/*
```

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* Texas Instruments Incorporated, <www.ti.com>
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```
* Richard Woodruff <r-woodruff2@ti.com>
```

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*
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```

```
*/
```

```
./board/ti/sdp3430/sdp.h
```

```
sdp.h
```

```
/*
```

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```

```
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*/
```

```
./board/ti/sdp4430/cmd_bat.c
cmd_bat.c
```

```
/*
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 * MA 02111-1307 USA
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```

```
./board/ti/sdp4430/sdp.c
sdp.c
```

```
/*
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 * Aneesh V <aneesh@ti.com>
 * Steve Sakoman <steve@sakoman.com>
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```
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*/
```

```
./board/ti/sdp4430/sdp4430_mux_data.h  
sdp4430_mux_data.h
```

```
/*  
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* Aneesh V <aneesh@ti.com>  
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* MA 02111-1307 USA  
*/
```

```
./board/ti/tnetv107xevm/sdb_board.c  
sdb_board.c
```

```
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* TNETV107X-EVM: Board initialization  
*  
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*/
```

```
./board/tim11/devkit3250/devkit3250.c
devkit3250.c
```

```
/*
 * Embest/Tim11 DevKit3250 board support
 *
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 * MA 02110-1301, USA.
*/
```

```
./board/tim11/devkit8000/devkit8000.c
devkit8000.c
```

```
/*
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 * Texas Instruments, <www.ti.com>
 *
 * Author :
 *   Sunil Kumar <sunilsaini05@gmail.com>
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*/
```

```
./board/timl1/devkit8000/devkit8000.h
devkit8000.h
```

```
/*
* (C) Copyright 2008
* Dirk Behme <dirk.behme@gmail.com>
*
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*/
```

```
./board/toradex/colibri_pxa270/colibri_pxa270.c
colibri_pxa270.c
```

```
/*
* Toradex Colibri PXA270 Support
*
* Copyright (C) 2010 Marek Vasut <marek.vasut@gmail.com>
*/
```

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*/
```

```
./board/toradex/colibri_t20-common/colibri_t20-common.c
colibri_t20-common.c
```

```
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*/
```

```
./board/toradex/colibri_t20-common/colibri_t20-common.h
colibri_t20-common.h
```

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```

```
./board/toradex/colibri_t20_iris/colibri_t20_iris.c  
colibri_t20_iris.c
```

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 *  
 */
```

```
./board/total5200/mt48lc16m16a2-75.h  
mt48lc16m16a2-75.h
```

```
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 * Mark Jonas, Freescale Semiconductor, mark.jonas@freescale.com.  
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 * MA 02111-1307 USA  
 */
```

```
./board/total5200/mt48lc32m16a2-75.h  
mt48lc32m16a2-75.h
```

```
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*/
```

```
./board/total15200/sdram.c
sdram.c
```

```
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*
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```
./board/total15200/sdram.h
sdram.h
```

```
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*/
```

```
./board/total5200/total5200.c
total5200.c
```

```
/*
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```

```
./board/tqc/tqm5200/cam5200_flash.c
cam5200_flash.c
```

```
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 */
```

```
./board/tqc/tqm5200/cmd_stk52xx.c
cmd_stk52xx.c
```

```
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 */
```

```
./board/tqc/tqm5200/cmd_tb5200.c
cmd_tb5200.c
```

```
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```
./board/tqc/tqm5200/mt48lc16m16a2-75.h
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```

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```

```
./board/tqc/tqm5200/tqm5200.c
tqm5200.c
```

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```
./board/tqc/tqm8260/tqm8260.c
```

```
tqm8260.c
```

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```
./board/tqc/tqm8272/nand.c  
nand.c
```

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```
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tqm8272.c
```

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```
./board/tqc/tqm8272/tqm8272.h  
tqm8272.h
```

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```
./board/tqc/tqm834x/pci.c  
pci.c
```

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```
./board/tqc/tqm834x/tqm834x.c
tqm834x.c
```

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```
./board/tqc/tqm8xx/load_serenum_ethaddr.c
load_serenum_ethaddr.c
```

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```
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```
./board/trizepsiv/conxs.c
conxs.c
```

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```
./board/trizepsiv/eprom.c
eprom.c
```

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```
./board/ttcontrol/vision2/vision2.c
vision2.c
```

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```
./board/utx8245/flash.c
```

```
flash.c
```

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```
./board/utx8245/utx8245.c
```

utx8245.c

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./board/v37/flash.c

flash.c

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./board/v37/v37.c

v37.c

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./board/v38b/ethaddr.c

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./board/v38b/v38b.c
v38b.c

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./board/ve8313/ve8313.c
ve8313.c

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```

```
./board/vpac270/onenand.c
```

```
onenand.c
```

```
/*
```

```
* Voipac PXA270 OneNAND SPL
```

```
*
```

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```
*/
```

```
./board/vpac270/vpac270.c
```

```
vpac270.c
```

```
/*
```

```
* Voipac PXA270 Support
```

```
*
```

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```
./board/w7o/cmd_vpd.c
```

```
cmd_vpd.c
```

```
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```

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```
./board/w7o/errors.h
```

```
errors.h
```

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/*
```

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```
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```

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```

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fsboot.c
```

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```
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post2.c
```

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```

```
./board/w7o/vpd.c
```

```
vpd.c
```

```
/*
```

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```
./board/w7o/vpd.h
```

```
vpd.h
```

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```

```
./board/w7o/w7o.c
```

```
w7o.c
```

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```
./board/w7o/w7o.h
```

```
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```
./board/w7o/watchdog.c
```

```
watchdog.c
```

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*/
```

```
./board/woodburn/woodburn.c
```

```
woodburn.c
```

```
/*
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*
* Based on flea3.c and mx35pdk.c
*
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```

```
./board/xaenix/flash.c
flash.c
```

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```

```
./board/xaenix/xaenix.c
xaenix.c
```

```
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```

```
./board/xes/common/actl_nand.c
```

```
actl_nand.c
```

```
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```
./board/xes/common/board.c  
board.c
```

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```

```
./board/xes/common/fsl_8xxx_clk.c  
fsl_8xxx_clk.c
```

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```
./board/xes/common/fsl_8xxx_misc.c  
fsl_8xxx_misc.c
```

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```
./board/xes/common/fsl_8xxx_misc.h
```

```
fsl_8xxx_misc.h
```

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```
./board/xes/common/fsl_8xxx_pci.c
```

```
fsl_8xxx_pci.c
```

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```

```
./board/xes/xpedite1000/xpedite1000.c
xpedite1000.c
```

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```
./board/xes/xpedite517x/ddr.c
ddr.c
```

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```

```
./board/xes/xpedite517x/law.c
```

```
law.c
```

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```

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```
./board/xes/xpedite517x/xpedite517x.c
```

```
xpedite517x.c
```

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```
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```

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```

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```
./board/xes/xpedite520x/tlb.c
```

```
tlb.c
```

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```

```
xpedite520x.c
```

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./board/xes/xpedite537x/ddr.c
ddr.c

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```
./board/xes/xpedite537x/tlb.c
```

```
tlb.c
```

```
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```
./board/xes/xpedite550x/ddr.c
```

```
ddr.c
```

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```

```
./board/xes/xpedite550x/law.c
```

```
law.c
```

```
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```

```
./board/xes/xpedite550x/tlb.c
```

```
tlb.c
```

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/*
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./board/xes/xpedite550x/xpedite550x.c
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xpedite550x.c
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*/
```

```
./board/xilinx/common/xbasic_types.c
xbasic_types.c
```

```
/******
```

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*
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```
./board/xilinx/common/xbasic_types.h
xbasic_types.h
```

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*****/

/******/

/**

*
* @file xbasic_types.h
*

* This file contains basic types for Xilinx software IP. These types do not
* follow the standard naming convention with respect to using the component
* name in front of each name because they are considered to be primitives.
*

* @note

* This file contains items which are architecture dependent.

```

*
* <pre>
* MODIFICATION HISTORY:
*
* Ver   Who   Date   Changes
* -----
* 1.00a rmm   12/14/01 First release
*       rmm   05/09/03 Added "xassert always" macros to rid ourselves of diab
*                   compiler warnings
* 1.00a rpm   11/07/03 Added XNullHandler function as a stub interrupt handler
* </pre>

```

*****/

```

./board/xilinx/common/xbuf_descriptor.h
xbuf_descriptor.h

```

/*****

```

*
* Author: Xilinx, Inc.
*

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* FILENAME:

* xbuf_descriptor.h

* DESCRIPTION:

* This file contains the interface for the XBufDescriptor component.
* The XBufDescriptor component is a passive component that only maps over
* a buffer descriptor data structure shared by the scatter gather DMA hardware
* and software. The component's primary purpose is to provide encapsulation of
* the buffer descriptor processing. See the source file xbuf_descriptor.c for
* details.

* NOTES:

* Most of the functions of this component are implemented as macros in order
* to optimize the processing. The names are not all uppercase such that they
* can be switched between macros and functions easily.

*****/

./board/xilinx/common/xdma_channel.c

xdma_channel.c

/******

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*
* FILENAME:
*
* xdma_channel.c
*
* DESCRIPTION:
*
* This file contains the DMA channel component. This component supports
* a distributed DMA design in which each device can have it's own dedicated
* DMA channel, as opposed to a centralized DMA design. This component
* performs processing for DMA on all devices.
*
* See xdma_channel.h for more information about this component.
*
* NOTES:
*
* None.
*
*****/

./board/xilinx/common/xdma_channel.h
xdma_channel.h
/*****
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* FILENAME:

*
* xdma_channel.h

* DESCRIPTION:

* This file contains the DMA channel component implementation. This component
* supports a distributed DMA design in which each device can have it's own
* dedicated DMA channel, as opposed to a centralized DMA design.
* A device which uses DMA typically contains two DMA channels, one for
* sending data and the other for receiving data.

* This component is designed to be used as a basic building block for
* designing a device driver. It provides registers accesses such that all
* DMA processing can be maintained easier, but the device driver designer
* must still understand all the details of the DMA channel.

* The DMA channel allows a CPU to minimize the CPU interaction required to move
* data between a memory and a device. The CPU requests the DMA channel to
* perform a DMA operation and typically continues performing other processing
* until the DMA operation completes. DMA could be considered a primitive form
* of multiprocessing such that caching and address translation can be an issue.

* Scatter Gather Operations

* The DMA channel may support scatter gather operations. A scatter gather
* operation automates the DMA channel such that multiple buffers can be
* sent or received with minimal software interaction with the hardware. Buffer
* descriptors, contained in the XBufDescriptor component, are used by the

* scatter gather operations of the DMA channel to describe the buffers to be processed.

* Scatter Gather List Operations

* A scatter gather list may be supported by each DMA channel. The scatter gather list allows buffer descriptors to be put into the list by a device driver which requires scatter gather. The hardware processes the buffer descriptors which are contained in the list and modifies the buffer descriptors to reflect the status of the DMA operations. The device driver is notified by interrupt that specific DMA events occur including scatter gather events. The device driver removes the completed buffer descriptors from the scatter gather list to evaluate the status of each DMA operation.

* The scatter gather list is created and buffer descriptors are inserted into the list. Buffer descriptors are never removed from the list after it's creation such that a put operation copies from a temporary buffer descriptor to a buffer descriptor in the list. Get operations don't copy from the list to a temporary, but return a pointer to the buffer descriptor in the list. A buffer descriptor in the list may be locked to prevent it from being overwritten by a put operation. This allows the device driver to get a descriptor from a scatter gather list and prevent it from being overwritten until the buffer associated with the buffer descriptor has been processed.

* Typical Scatter Gather Processing

* The following steps illustrate the typical processing to use the scatter gather features of a DMA channel.

- * 1. Create a scatter gather list for the DMA channel which puts empty buffer descriptors into the list.
- * 2. Create buffer descriptors which describe the buffers to be filled with receive data or the buffers which contain data to be sent.
- * 3. Put buffer descriptors into the DMA channel scatter list such that scatter

./board/xilinx/common/xdma_channel_i.h
xdma_channel_i.h

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* FILENAME:

* xdma_channel_i.h

* DESCRIPTION:

* This file contains data which is shared internal data for the DMA channel
* component. It is also shared with the buffer descriptor component which is
* very tightly coupled with the DMA channel component.

* NOTES:

* The last buffer descriptor constants must be located here to prevent a
* circular dependency between the DMA channel component and the buffer
* descriptor component.

*****/

./board/xilinx/common/xdma_channel_sg.c

xdma_channel_sg.c

/* \$Id: xdma_channel_sg.c,v 1.6 2003/02/03 19:50:33 moleres Exp \$ */

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* FILENAME:

* xdma_channel_sg.c
*

* DESCRIPTION:

* This file contains the implementation of the XDmaChannel component which is
* related to scatter gather operations.
*

* Scatter Gather Operations
*

* The DMA channel may support scatter gather operations. A scatter gather
* operation automates the DMA channel such that multiple buffers can be
* sent or received with minimal software interaction with the hardware. Buffer
* descriptors, contained in the XBufDescriptor component, are used by the
* scatter gather operations of the DMA channel to describe the buffers to be
* processed.
*

* Scatter Gather List Operations

*

* A scatter gather list may be supported by each DMA channel. The scatter
* gather list allows buffer descriptors to be put into the list by a device
* driver which requires scatter gather. The hardware processes the buffer
* descriptors which are contained in the list and modifies the buffer
* descriptors to reflect the status of the DMA operations. The device driver
* is notified by interrupt that specific DMA events occur including scatter
* gather events. The device driver removes the completed buffer descriptors
* from the scatter gather list to evaluate the status of each DMA operation.

*

* The scatter gather list is created and buffer descriptors are inserted into
* the list. Buffer descriptors are never removed from the list after it's
* creation such that a put operation copies from a temporary buffer descriptor
* to a buffer descriptor in the list. Get operations don't copy from the list
* to a temporary, but return a pointer to the buffer descriptor in the list.
* A buffer descriptor in the list may be locked to prevent it from being
* overwritten by a put operation. This allows the device driver to get a
* descriptor from a scatter gather list and prevent it from being overwritten
* until the buffer associated with the buffer descriptor has been processed.

*

* The get and put functions only operate on the list and are asynchronous from
* the hardware which may be using the list of descriptors. This is important
* because there are no checks in the get and put functions to ensure that the
* hardware has processed the descriptors. This must be handled by the driver
* using the DMA scatter gather channel through the use of the other functions.
* When a scatter gather operation is started, the start function does ensure
* that the descriptor to start has not already been processed by the hardware
* and is not the first of a series of descriptors that have not been committed
* yet.

*

* Descriptors are put into the list but not marked as ready to use by the
* hardware until a commit operation is done. This allows multiple descriptors
* which may contain a single packet of information for a protocol to be
* guaranteed not to cause any underflow conditions during transmission. The
* hardware design only allows descriptors to cause it to stop after a descriptor
* has been processed rather than before it is processed. A series of
* descriptors are put into the list followed by a commit operation, or each
* descriptor may be committed. A commit operation is performed by changing a
* single descriptor, the first of the series of puts, to indicate that the
* hardware may now use all descriptors after it. The last descriptor in the
* list is always set to cause the hardware to stop after it is processed.

*

* Typical Scatter Gather Processing

```
./board/xilinx/common/xio.h
```

```
xio.h
```

```
/*
```

```
* xio.h
```

```
*
```

```

* Defines XIo functions for Xilinx OCP in terms of Linux primitives
*
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*       source@mvista.com
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*/

./board/xilinx/common/xipif_v1_23_b.c
xipif_v1_23_b.c
/* $Id: xipif_v1_23_b.c,v 1.1 2002/03/18 23:24:52 linnj Exp $ */
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```

```
*****/  
/*****
```

*

* FILENAME:

*

* xipif.c

*

* DESCRIPTION:

*

* This file contains the implementation of the XIpIf component. The XIpIf component encapsulates the IPIF, which is the standard interface that IP must adhere to when connecting to a bus. The purpose of this component is to encapsulate the IPIF processing such that maintainability is increased. This component does not provide a lot of abstraction from the details of the IPIF as it is considered a building block for device drivers. A device driver designer must be familiar with the details of the IPIF hardware to use this component.

*

* The IPIF hardware provides a building block for all hardware devices such that each device does not need to reimplement these building blocks. The IPIF contains other building blocks, such as FIFOs and DMA channels, which are also common to many devices. These blocks are implemented as separate hardware blocks and instantiated within the IPIF. The primary hardware of the IPIF which is implemented by this software component is the interrupt architecture. Since there are many blocks of a device which may generate interrupts, all the interrupt processing is contained in the common part of the device, the IPIF. This interrupt processing is for the device level only and does not include any processing for the interrupt controller.

*

* A device is a mechanism such as an Ethernet MAC. The device is made up of several parts which include an IPIF and the IP. The IPIF contains most of the device infrastructure which is common to all devices, such as interrupt processing, DMA channels, and FIFOs. The infrastructure may also be referred to as IPIF internal blocks since they are part of the IPIF and are separate blocks that can be selected based upon the needs of the device. The IP of the device is the logic that is unique to the device and interfaces to the IPIF of the device.

*

* In general, there are two levels of registers within the IPIF. The first level, referred to as the device level, contains registers which are for the entire device. The second level, referred to as the IP level, contains registers which are specific to the IP of the device. The two levels of registers are designed to be hierarchical such that the device level is a more general register set above the more specific registers of the IP. The IP level of registers provides functionality which is typically common across all devices and allows IP designers to focus on the unique aspects of the IP.

*

* The interrupt registers of the IPIF are parameterizable such that the only the number of bits necessary for the device are implemented. The functions

* of this component do not attempt to validate that the passed in arguments are
* valid based upon the number of implemented bits. This is necessary to
* maintain the level of performance required for the common components. Bits
* of the registers are assigned starting at the least significant bit of the
* registers.

* Critical Sections

* It is the responsibility of the device driver designer to use critical
* sections as necessary when calling functions of the IPIF. This component
* does not use critical sections and it does access registers using
* read-modify-write operations. Calls to IPIF functions from a main thread
* and from an interrupt context could produce unpredictable behavior such that
* the caller must provide the appropriate critical sections.

* Mutual Exclusion

* The functions of the IPIF are not thread safe such that the caller of all
* functions is responsible for ensuring mutual exclusion for an IPIF. Mutual
* exclusion across multiple IPIF components is not necessary.

* NOTES:

* None.

* MODIFICATION HISTORY:

Ver	Who	Date	Changes
1.23b	jhl	02/27/01	Repartitioned to reduce size

./board/xilinx/common/xipif_v1_23_b.h

xipif_v1_23_b.h

/* \$Id: xipif_v1_23_b.h,v 1.1 2002/03/18 23:24:52 linnj Exp \$ */

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*****/
/*****

* FILENAME:

* xipif.h

* DESCRIPTION:

* The XIPIf component encapsulates the IPIF, which is the standard interface that IP must adhere to when connecting to a bus. The purpose of this component is to encapsulate the IPIF processing such that maintainability is increased. This component does not provide a lot of abstraction from the details of the IPIF as it is considered a building block for device drivers. A device driver designer must be familiar with the details of the IPIF hardware to use this component.

* The IPIF hardware provides a building block for all hardware devices such that each device does not need to reimplement these building blocks. The IPIF contains other building blocks, such as FIFOs and DMA channels, which are also common to many devices. These blocks are implemented as separate hardware blocks and instantiated within the IPIF. The primary hardware of the IPIF which is implemented by this software component is the interrupt architecture. Since there are many blocks of a device which may generate interrupts, all the interrupt processing is contained in the common part of the device, the IPIF. This interrupt processing is for the device level only and does not include any processing for the interrupt controller.

* A device is a mechanism such as an Ethernet MAC. The device is made up of several parts which include an IPIF and the IP. The IPIF contains most of the device infrastructure which is common to all devices, such as interrupt processing, DMA channels, and FIFOs. The infrastructure may also be referred to as IPIF internal blocks since they are part of the IPIF and are separate blocks that can be selected based upon the needs of the device. The IP of the device is the logic that is unique to the device and interfaces to the IPIF of the device.

* In general, there are two levels of registers within the IPIF. The first level, referred to as the device level, contains registers which are for the entire device. The second level, referred to as the IP level, contains registers which are specific to the IP of the device. The two levels of registers are designed to be hierarchical such that the device level is a more general register set above the more specific registers of the IP. The IP level of registers provides functionality which is typically common across all devices and allows IP designers to focus on the unique aspects of the IP.

* Critical Sections
*
* It is the responsibility of the device driver designer to use critical
* sections as necessary when calling functions of the IPIF. This component
* does not use critical sections and it does access registers using
* read-modify-write operations. Calls to IPIF functions from a main thread
* and from an interrupt context could produce unpredictable behavior such that
* the caller must provide the appropriate critical sections.

* Mutual Exclusion
*
* The functions of the IPIF are not thread safe such that the caller of all
* functions is responsible for ensuring mutual exclusion for an IPIF. Mutual
* exclusion across multiple IPIF components is not necessary.

* NOTES:

* None.

* MODIFICATION HISTORY:

Ver	Who	Date	Changes
1.23b	jhl	02/27/01	Repartitioned to minimize size

*****/

./board/xilinx/common/xpacket_fifo_v1_00_b.c
xpacket_fifo_v1_00_b.c

/*****

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*
*****/
/*
/*
/*
* @file xpacket_fifo_v1_00_b.c
*
* Contains functions for the XPacketFifoV100b component. See xpacket_fifo_v1_00_b.h
* for more information about the component.
*
* <pre>
* MODIFICATION HISTORY:
*
* Ver   Who   Date     Changes
* -----
* 1.00b rpm 03/26/02  First release
* </pre>
*
*****/

./board/xilinx/common/xpacket_fifo_v1_00_b.h
xpacket_fifo_v1_00_b.h
/*****
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*****/
/*****/
/*

* @file xpacket_fifo_v1_00_b.h

* This component is a common component because it's primary purpose is to
* prevent code duplication in drivers. A driver which must handle a packet
* FIFO uses this component rather than directly manipulating a packet FIFO.

* A FIFO is a device which has dual port memory such that one user may be
* inserting data into the FIFO while another is consuming data from the FIFO.
* A packet FIFO is designed for use with packet protocols such as Ethernet and
* ATM. It is typically only used with devices when DMA and/or Scatter Gather
* is used. It differs from a nonpacket FIFO in that it does not provide any
* interrupts for thresholds of the FIFO such that it is less useful without
* DMA.

* @note

* This component has the capability to generate an interrupt when an error
* condition occurs. It is the user's responsibility to provide the interrupt
* processing to handle the interrupt. This component provides the ability to
* determine if that interrupt is active, a deadlock condition, and the ability
* to reset the FIFO to clear the condition. In this condition, the device which
* is using the FIFO should also be reset to prevent other problems. This error

* condition could occur as a normal part of operation if the size of the FIFO
* is not setup correctly. See the hardware IP specification for more details.

* <pre>

* MODIFICATION HISTORY:

* Ver Who Date Changes

* -----
* 1.00b rpm 03/26/02 First release

* </pre>

*****/

./board/xilinx/common/xstatus.h

xstatus.h

/*****

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*      675 Mass Ave, Cambridge, MA 02139, USA.
*
*****/
/*****/
/**
*
* @file xstatus.h
*
* This file contains Xilinx software status codes.  Status codes have their
* own data type called XStatus.  These codes are used throughout the Xilinx
* device drivers.
*
*****/

./board/xilinx/common/xversion.c
xversion.c
/*****
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*****/
/*****

* This file contains the implementation of the XVersion component. This
* component represents a version ID. It is encapsulated within a component
* so that it's type and implementation can change without affecting users of
* it.

* The version is formatted as X.YYZ where X = 0 - 9, Y = 00 - 99, Z = a - z
* X is the major revision, YY is the minor revision, and Z is the
* compatability revision.

* Packed versions are also utilized for the configuration ROM such that
* memory is minimized. A packed version consumes only 16 bits and is
* formatted as follows.

* <pre>

Revision	Range	Bit Positions
Major Revision	0 - 9	Bits 15 - 12
Minor Revision	0 - 99	Bits 11 - 5
Compatability Revision	a - z	Bits 4 - 0

./board/xilinx/common/xversion.h

xversion.h

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*
*****/
/******
*
* This file contains the interface for the XVersion component. This
* component represents a version ID. It is encapsulated within a component
* so that it's type and implementation can change without affecting users of
* it.
*
* The version is formatted as X.YYZ where X = 0 - 9, Y = 00 - 99, Z = a - z
* X is the major revision, YY is the minor revision, and Z is the
* compatability revision.
*
* Packed versions are also utilized for the configuration ROM such that
* memory is minimized. A packed version consumes only 16 bits and is
* formatted as follows.
*
* <pre>
* Revision           Range           Bit Positions
*
* Major Revision     0 - 9           Bits 15 - 12
* Minor Revision     0 - 99          Bits 11 - 5
* Compatability Revision a - z       Bits 4 - 0
* </pre>
*
*****/

./board/xilinx/microblaze-generic/microblaze-generic.c
microblaze-generic.c
/*
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*
* Michal SIMEK <monstr@monstr.eu>
*
* See file CREDITS for list of people who contributed to this
* project.

```

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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./board/xilinx/microblaze-generic/xparameters.h
xparameters.h
```

```
/*
* (C) Copyright 2007 Michal Simek
*
* Michal SIMEK <monstr@monstr.eu>
*
* See file CREDITS for list of people who contributed to this
* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
* CAUTION: This file is a faked configuration !!!
*           There is no real target for the microblaze-generic
*           configuration. You have to replace this file with
*           the generated file from your Xilinx design flow.
*/
```

```
./board/xilinx/m1507/m1507.c
m1507.c
```

```
/*
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*/
```

```
./board/xilinx/ml507/xparameters.h
xparameters.h
```

```
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 * based on xparameters-ml507.h by Xilinx
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*/
```

```
./board/xilinx/ppc405-generic/xilinx_ppc405_generic.c
xilinx_ppc405_generic.c
```

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*/
```

```
./board/xilinx/ppc405-generic/xparameters.h
```

```
xparameters.h
```

```
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*/
```

```
./board/xilinx/ppc440-generic/xilinx_ppc440_generic.c
```

```
xilinx_ppc440_generic.c
```

```
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```
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xparameters.h
```

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*/
```

```
./board/xilinx/xilinx_iic/xiic_1.c
xiic_1.c
```

```
/* $Id: xiic_1.c,v 1.2 2002/12/05 19:32:40 meinelte Exp $ */
/*****
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*
*****/
```

```

/**
 *
 * @file xiic_1.c
 *
 * This file contains low-level driver functions that can be used to access the
 * device. The user should refer to the hardware device specification for more
 * details of the device operation.
 *
 * <pre>
 * MODIFICATION HISTORY:
 *
 * Ver    Who    Date      Changes
 * -----
 * 1.01b  jhl    5/13/02   First release
 * 1.01b  jhl    10/14/02  Corrected bug in the receive function, the setup of the
 *                          interrupt status mask was not being done in the loop such
 *                          that a read would sometimes fail on the last byte because
 *                          the transmit error which should have been ignored was
 *                          being used. This would leave an extra byte in the FIFO
 *                          and the bus throttled such that the next operation would
 *                          also fail. Also updated the receive function to not
 *                          disable the device after the last byte until after the
 *                          bus transitions to not busy which is more consistent
 *                          with the expected behavior.
 * 1.01c  ecm    12/05/02  new rev
 * </pre>
 *
 * *****/

./board/xilinx/xilinx_iic/xiic_1.h
xiic_1.h
/* $Id: xiic_1.h,v 1.2 2002/12/05 19:32:40 meinelte Exp $ */
/*****
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```

```

*
*****/
/*****/
/**
*
* @file xiic_1.h
*
* This header file contains identifiers and low-level driver functions (or
* macros) that can be used to access the device. High-level driver functions
* are defined in xiic.h.
*
* <pre>
* MODIFICATION HISTORY:
*
* Ver   Who   Date     Changes
* -----
* 1.00b jhl   05/07/02 First release
* 1.01c ecm   12/05/02 new rev
* </pre>
*
*****/

```

```
./board/xilinx/zynq/board.c
```

```
board.c
```

```

/*
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 *
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 * project.
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```

```
./board/zeus/update.c
```

```
update.c
```

```

/*
 * (C) Copyright 2007

```

```
* Stefan Roese, DENX Software Engineering, sr@denx.de.
*
* See file CREDITS for list of people who contributed to this
* project.
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*/
```

```
./board/zeus/zeus.c
```

```
zeus.c
```

```
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*/
```

```
./board/zipitz2/zipitz2.c
```

```
zipitz2.c
```

```
/*
* Copyright (C) 2009
```

```
* Marek Vasut <marek.vasut@gmail.com>
*
* Heavily based on pxa255_idp platform
*
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*/
```

```
./board/zpc1900/zpc1900.c
zpc1900.c
```

```
/*
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* Yuli Barcohen <yuli@arabellasw.com>
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*/
```

```
./common/bedbug.c
bedbug.c
/* $Id$ */
/* USE_SOURCE_CODE enables some symbolic debugging functions of this
```

```
./common/bootstage.c
bootstage.c
/*
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 * MA 02111-1307 USA
 */
```

```
./common/bouncebuf.c
bouncebuf.c
/*
 * Generic bounce buffer implementation
 *
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 *
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 * project.
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*/
```

```
./common/cmd_ambapp.c
```

```
cmd_ambapp.c
```

```
/*
```

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```
* Daniel Hellstrom, Gaisler Research, daniel@gaisler.com.
```

```
*
```

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```

```
*/
```

```
./common/cmd_binfo.c
```

```
cmd_binfo.c
```

```
/*
```

```
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```
*
```

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*/
```

```
./common/cmd_bedbug.c
cmd_bedbug.c
/*
* BedBug Functions
*/
```

```
./common/cmd_bmp.c
cmd_bmp.c
/*
* (C) Copyright 2002
* Detlev Zundel, DENX Software Engineering, dzu@denx.de.
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*/
```

```
./common/cmd_boot.c
cmd_boot.c
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*/
```

```
./common/cmd_bootldr.c
cmd_bootldr.c
```

```
/*
* U-boot - bootldr.c
*
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```

```
./common/cmd_bootm.c
cmd_bootm.c
```

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*/
```

./common/cmd_bootstage.c

cmd_bootstage.c

```
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 */
```

./common/cmd_cache.c

cmd_cache.c

```
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 */
```

./common/cmd_cbfs.c

cmd_cbfs.c

```
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 */
```

./common/cmd_console.c

cmd_console.c

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 * MA 02111-1307 USA
 */
```

./common/cmd_cplbininfo.c

cmd_cplbininfo.c

```
/*
 * cmd_cplbinfo.c - dump the instruction/data cplb tables
 *
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 */

./common/cmd_cramfs.c
cmd_cramfs.c
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 * MA 02111-1307 USA
 *
 * based on: cmd_jffs2.c
 *
 *      Add support for a CRAMFS located in RAM
 */

./common/cmd_dataflash_mmc_mux.c
cmd_dataflash_mmc_mux.c
/*
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```

```
./common/cmd_date.c
```

```
cmd_date.c
```

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```

```
./common/cmd_dcr.c
```

```
cmd_dcr.c
```

```
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*/
```

```
./common/cmd_df.c
```

```
cmd_df.c
```

```
/*
 * Command for accessing DataFlash.
 *
 * Copyright (C) 2008 Atmel Corporation
 */
```

```
./common/cmd_dfu.c
```

```
cmd_dfu.c
```

```
/*
 * cmd_dfu.c -- dfu command
 *
 * Copyright (C) 2012 Samsung Electronics
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 *          Lukasz Majewski <l.majewski@samsung.com>
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```
./common/cmd_diag.c
```

```
cmd_diag.c
```

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```
./common/cmd_disk.c
```

```
cmd_disk.c
```

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```
./common/cmd_display.c
```

```
cmd_display.c
```

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```
./common/cmd_dtt.c
```

```
cmd_dtt.c
```

```
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```

```
./common/cmd_echo.c
```

```
cmd_echo.c
```

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```

```
./common/cmd_eeprom.c
```

```
cmd_eeprom.c
```

```
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*/
```

```
./common/cmd_elf.c
```

```
cmd_elf.c
```

```
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./common/cmd_exit.c
cmd_exit.c
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*/

./common/cmd_ext2.c
cmd_ext2.c
/*
* (C) Copyright 2011 - 2012 Samsung Electronics
* EXT4 filesystem implementation in Uboot by
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* Manjunatha C Achar <a.manjunatha@samsung.com>
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*
* made from cmd_reiserfs by
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* MA 02111-1307 USA  
*  
*/
```

```
./common/cmd_ext4.c
```

```
cmd_ext4.c
```

```
/*  
* (C) Copyright 2011 - 2012 Samsung Electronics  
* EXT4 filesystem implementation in Uboot by  
* Uma Shankar <uma.shankar@samsung.com>  
* Manjunatha C Achar <a.manjunatha@samsung.com>  
*  
* Ext4fs support  
* made from existing cmd_ext2.c file of Uboot  
*  
* (C) Copyright 2004  
* esd gmbh <www.esd-electronics.com>  
* Reinhard Arlt <reinhard.arlt@esd-electronics.com>  
*  
* made from cmd_reiserfs by  
*  
* (C) Copyright 2003 - 2004  
* Sysgo Real-Time Solutions, AG <www.elinos.com>  
* Pavel Bartusek <pba@sysgo.com>  
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*
*/
```

```
./common/cmd_fat.c
```

```
cmd_fat.c
```

```
/*
```

```
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* Richard Jones, rjones@nexus-tech.net
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*/
```

```
./common/cmd_fdc.c
```

```
cmd_fdc.c
```

```
/*
```

```
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* Denis Peter, MPL AG, d.peter@mpl.ch.
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*
*/
/*
* Floppy Disk support
*/
```

```
./common/cmd_fdos.c
```

```
cmd_fdos.c
```

```
/*
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* Pierre AUBERT p.aubert@staubli.com
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* MA 02111-1307 USA
*/
```

```
./common/cmd_fdt.c
```

```
cmd_fdt.c
```

```
/*
* (C) Copyright 2007
* Gerald Van Baren, Custom IDEAS, vanbaren@cideas.com
* Based on code written by:
* Pantelis Antoniou <pantelis.antoniou@gmail.com> and
* Matthew McClintock <msm@freescale.com>
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*/
```

```
./common/cmd_fitupd.c
cmd_fitupd.c
```

```
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* Andreas Pretzsch, carpe noctem engineering, apr@cn-eng.de
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*/
```

```
./common/cmd_flash.c
cmd_flash.c
```

```
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* MA 02111-1307 USA
*/
```

./common/cmd_fpga.c

cmd_fpga.c

/*

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* Rich Ireland, Enterasys Networks, rireland@enterasys.com.

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*

*/

./common/cmd_fs.c

cmd_fs.c

/*

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*

* Inspired by cmd_ext_common.c, cmd_fat.c.

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*/

./common/cmd_gettime.c

cmd_gettime.c

/*

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```
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*/
```

```
./common/cmd_gpio.c
```

```
cmd_gpio.c
```

```
/*
* Control GPIO pins on the fly
*
* Copyright (c) 2008-2011 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/
```

```
./common/cmd_gpt.c
```

```
cmd_gpt.c
```

```
/*
* cmd_gpt.c -- GPT (GUID Partition Table) handling command
*
* Copyright (C) 2012 Samsung Electronics
* author: Lukasz Majewski <l.majewski@samsung.com>
* author: Piotr Wilczek <p.wilczek@samsung.com>
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*/
```

```
./common/cmd_hash.c
```

```
cmd_hash.c
```

```
/*
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*
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*
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*/
```

```
./common/cmd_help.c
```

```
cmd_help.c
```

```
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*/
```

```
./common/cmd_i2c.c
```

```
cmd_i2c.c
```

```
/*
```

```
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* Gerald Van Baren, Custom IDEAS, vanbaren@cideas.com.
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```
/*
```

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/*
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```

```
*/
```

```
./common/cmd_ide.c
```

```
cmd_ide.c
```

```
/*
```

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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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```

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/*
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*  
*/
```

```
./common/cmd_immap.c  
cmd_immap.c
```

```
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* MA 02111-1307 USA  
*/
```

```
./common/cmd_ini.c  
cmd_ini.c
```

```
/*  
* inih -- simple .INI file parser  
*  
* Copyright (c) 2009, Brush Technology  
* Copyright (c) 2012:  
*     Joe Hershberger, National Instruments, joe.hershberger@ni.com  
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*  
* The "inih" library is distributed under the following license, which is  
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*  
*/
```

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*
* Go to the project home page for more info:
* http://code.google.com/p/inih/
*/
```

```
./common/cmd_io.c
```

```
cmd_io.c
```

```
/*
```

```
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```

```
*
```

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```
*
```

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```

```
*/
```

./common/cmd_irq.c

cmd_irq.c

/*

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*/

./common/cmd_itest.c

cmd_itest.c

/*

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*/

./common/cmd_jffs2.c

```
cmd_jffs2.c
/*
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 *
 * (C) Copyright 2002
 * Robert Schwebel, Pengutronix, <r.schwebel@pengutronix.de>
 *
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 * Kai-Uwe Bloem, Auerswald GmbH & Co KG, <linux-development@auerswald.de>
 *
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 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
 * Added support for reading flash partition table from environment.
 * Parsing routines are based on driver/mtd/cmdline.c from the linux 2.4
 * kernel tree.
 *
 * $Id: cmdlinepart.c,v 1.17 2004/11/26 11:18:47 lavinen Exp $
 * Copyright 2002 SYSGO Real-Time Solutions GmbH
 *
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 * project.
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 * MA 02111-1307 USA
 */
```

```
./common/cmd_ldrinfo.c
```

```
cmd_ldrinfo.c
```

```
/*
 * U-boot - ldrinfo
 *
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 * project.
 */
```

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*/

./common/cmd_led.c

cmd_led.c

/*

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* Jason Kridner <jkridner@beagleboard.org>

*

* Based on cmd_led.c patch from:

* <http://www.mail-archive.com/u-boot@lists.denx.de/msg06873.html>

* (C) Copyright 2008

* Ulf Samuelsson <ulf.samuelsson@atmel.com>

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*/

./common/cmd_license.c

cmd_license.c

/*

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* Author: Harald Welte <laforge@openmoko.org>

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* MA 02111-1307 USA  
*/
```

```
./common/cmd_load.c
```

```
cmd_load.c
```

```
/*
```

```
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*/
```

```
./common/cmd_log.c
```

```
cmd_log.c
```

```
/*
```

```
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* Detlev Zundel, DENX Software Engineering, dzu@denx.de.  
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*  
* Comments:  
*  
* After relocating the code, the environment variable "loglevel" is  
* copied to console_loglevel. The functionality is similar to the  
* handling in the Linux kernel, i.e. messages logged with a priority  
* less than console_loglevel are also output to stdout.  
*  
* If you want messages with the default level (e.g. POST messages) to  
* appear on stdout also, make sure the environment variable  
* "loglevel" is set at boot time to a number higher than  
* default_message_loglevel below.  
*/
```

```
./common/cmd_mac.c
```

```
cmd_mac.c
```

```
/*  
* Copyright 2006 Freescale Semiconductor  
* York Sun (yorksun@freescale.com)  
*  
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* MA 02111-1307 USA  
*/
```

```
./common/cmd_md5sum.c
```

```
cmd_md5sum.c
```

```
/*
 * (C) Copyright 2011
 * Joe Hershberger, National Instruments, joe.hershberger@ni.com
 *
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 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./common/cmd_mdio.c
```

```
cmd_mdio.c
```

```
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 * Andy Fleming
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 * MA 02111-1307 USA
 */
```

./common/cmd_mem.c

cmd_mem.c

/*

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*/

./common/cmd_mfsl.c

cmd_mfsl.c

/*

* (C) Copyright 2007 Michal Simek

*

* Michal SIMEK <monstr@monstr.eu>

*

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```
*/  
  
./common/cmd_mii.c  
cmd_mii.c  
/*  
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*/
```

```
./common/cmd_misc.c  
cmd_misc.c  
/*  
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* MA 02111-1307 USA
```

```
*/  
  
./common/cmd_mmc.c  
cmd_mmc.c  
/*  
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* Kyle Harris, kharris@nexus-tech.net  
*  
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* MA 02111-1307 USA  
*/
```

```
./common/cmd_mmc_spi.c  
cmd_mmc_spi.c  
/*  
* Command for mmc_spi setup.  
*  
* Copyright (C) 2010 Thomas Chou <thomas@wytron.com.tw>  
* Licensed under the GPL-2 or later.  
*/
```

```
./common/cmd_mp.c  
cmd_mp.c  
/*  
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*/

./common/cmd_mtdparts.c

cmd_mtdparts.c

/*
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* (C) Copyright 2002
* Robert Schwebel, Pengutronix, <r.schwebel@pengutronix.de>
*
* (C) Copyright 2003
* Kai-Uwe Bloem, Auerswald GmbH & Co KG, <linux-development@auerswald.de>
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*
* Added support for reading flash partition table from environment.
* Parsing routines are based on driver/mtd/cmdline.c from the linux 2.4
* kernel tree.
*
* (C) Copyright 2008
* Harald Welte, OpenMoko, Inc., Harald Welte <laforge@openmoko.org>
*
* \$Id: cmdlinepart.c,v 1.17 2004/11/26 11:18:47 lavinen Exp \$
* Copyright 2002 SYSGO Real-Time Solutions GmbH
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```
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* MA 02111-1307 USA  
*/
```

```
./common/cmd_nand.c  
cmd_nand.c
```

```
/*  
* Driver for NAND support, Rick Bronson  
* borrowed heavily from:  
* (c) 1999 Machine Vision Holdings, Inc.  
* (c) 1999, 2000 David Woodhouse <dwmw2@infradead.org>  
*  
* Ported 'dynenv' to 'nand env.oob' command  
* (C) 2010 Nanometrics, Inc.  
* 'dynenv' -- Dynamic environment offset in NAND OOB  
* (C) Copyright 2006-2007 OpenMoko, Inc.  
* Added 16-bit nand support  
* (C) 2004 Texas Instruments  
*  
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*/
```

```
./common/cmd_net.c  
cmd_net.c
```

```
/*  
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```

```
*/  
  
./common/cmd_nvedit.c  
cmd_nvedit.c  
/*  
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* Andreas Heppel <aheppel@sysgo.de>  
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* MA 02111-1307 USA  
*/  
  
./common/cmd_onenand.c  
cmd_onenand.c  
/*  
* U-Boot command for OneNAND support  
*  
* Copyright (C) 2005-2008 Samsung Electronics  
* Kyungmin Park <kyungmin.park@samsung.com>  
*  
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*/  
  
./common/cmd_otp.c  
cmd_otp.c  
/*  
* cmd_otp.c - interface to Blackfin on-chip One-Time-Programmable memory  
*
```

```
* Copyright (c) 2007-2008 Analog Devices Inc.
*
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*/
/* There are 512 128-bit "pages" (0x000 through 0x1FF).
* The pages are accessible as 64-bit "halfpages" (an upper and lower half).
* The pages are not part of the memory map. There is an OTP controller which
* handles scanning in/out of bits. While access is done through OTP MMRs,
* the bootrom provides C-callable helper functions to handle the interaction.
*/
```

```
./common/cmd_part.c
```

```
cmd_part.c
```

```
/*
* Copyright (c) 2012, NVIDIA CORPORATION. All rights reserved.
*
* made from cmd_ext2, which was:
*
* (C) Copyright 2004
* esd gmbh <www.esd-electronics.com>
* Reinhard Arlt <reinhard.arlt@esd-electronics.com>
*
* made from cmd_reiserfs by
*
* (C) Copyright 2003 - 2004
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* Pavel Bartusek <pba@sysgo.com>
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*/
```

```
./common/cmd_pci.c
```

```
cmd_pci.c
```

```
/*
* (C) Copyright 2001 Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Andreas Heppel <aheppel@sysgo.de>
```

```
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*/
```

```
./common/cmd_pcmcia.c
cmd_pcmcia.c
```

```
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*
```

```
*****
*
```

```

* Lots of code copied from:
*
* m8xx_pcmcia.c - Linux PCMCIA socket driver for the mpc8xx series.
* (C) 1999-2000 Magnus Damm <damm@bitSMART.com>
*
* "The ExCA standard specifies that socket controllers should provide
* two IO and five memory windows per socket, which can be independently
* configured and positioned in the host address space and mapped to
* arbitrary segments of card address space. " - David A Hinds. 1999
*
* This controller does not meet the ExCA standard.
*
* m8xx pcmcia controller brief info:
* + 8 windows (attrib, mem, i/o)
* + up to two slots (SLOT_A and SLOT_B)
* + inputpins, outputpins, event and mask registers.
* - no offset register. sigh.
*
* Because of the lacking offset register we must map the whole card.
* We assign each memory window PCMCIA_MEM_WIN_SIZE address space.
* Make sure there is (PCMCIA_MEM_WIN_SIZE * PCMCIA_MEM_WIN_NO
* * PCMCIA_SOCKETS_NO) bytes at PCMCIA_MEM_WIN_BASE.
* The i/o windows are dynamically allocated at PCMCIA_IO_WIN_BASE.
* They are maximum 64KByte each...
*/

./common/cmd_portio.c
cmd_portio.c
/*
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* Marc Singer, elf@buici.com
*
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```

./common/cmd_pxe.c

cmd_pxe.c

/*

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*/

./common/cmd_read.c

cmd_read.c

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*/

./common/cmd_reginfo.c

cmd_reginfo.c

/*

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*/
```

```
./common/cmd_reiser.c
```

```
cmd_reiser.c
```

```
/*
```

```
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```

```
* Sysgo Real-Time Solutions, AG <www.elinos.com>
```

```
* Pavel Bartusek <pba@sysgo.com>
```

```
*
```

```
* See file CREDITS for list of people who contributed to this
* project.
```

```
*
```

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*
```

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```

```
*
```

```
*/
```

```
./common/cmd_sata.c
```

```
cmd_sata.c
```

```
/*
```

```
* Copyright (C) 2000-2005, DENX Software Engineering
```

```
* Wolfgang Denk <wd@denx.de>
```

```
* Copyright (C) Procsys. All rights reserved.
```

```
* Mushtaq Khan <mushtaq_k@procsys.com>
```

```
* <mushtaqk_921@yahoo.co.in>
```

```
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```
* Dave Liu <daveliu@freescale.com>
```

```
*
```

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```
*
```

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*/
```

```
./common/cmd_scsi.c
```

```
cmd_scsi.c
```

```
/*
```

```
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*  
*  
*  
*/
```

```
./common/cmd_setexpr.c
```

```
cmd_setexpr.c
```

```
/*
```

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*/
```

```
./common/cmd_sf.c
```

```
cmd_sf.c
```

```
/*
 * Command for accessing SPI flash.
 *
 * Copyright (C) 2008 Atmel Corporation
 * Licensed under the GPL-2 or later.
 */
```

```
./common/cmd_sha1sum.c
```

```
cmd_sha1sum.c
```

```
/*
 * (C) Copyright 2011
 * Joe Hershberger, National Instruments, joe.hershberger@ni.com
 *
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 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./common/cmd_sound.c

cmd_sound.c

/*

* Copyright (C) 2012 Samsung Electronics

* Rajeshwari Shinde <rajeshwari.s@samsung.com>

*

* See file CREDITS for list of people who contributed to this
* project.

*

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* MA 02111-1307 USA

*/

./common/cmd_source.c

cmd_source.c

/*

* (C) Copyright 2001

* Kyle Harris, kharris@nexus-tech.net

*

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* project.

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* MA 02111-1307 USA

*/

```
./common/cmd_spi.c
cmd_spi.c
/*
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 * project.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./common/cmd_spibootldr.c
cmd_spibootldr.c
/*
 * U-boot - spibootldr.c
 *
 * Copyright (c) 2005-2008 Analog Devices Inc.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
 * Licensed under the GPL-2 or later.
 */
```

```
./common/cmd_spl.c
cmd_spl.c
/*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./common/cmd_strings.c
cmd_strings.c
```

```
/*
 * cmd_strings.c - just like `strings` command
 *
 * Copyright (c) 2008 Analog Devices Inc.
 *
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 */
```

```
./common/cmd_terminal.c
cmd_terminal.c
```

```
/*
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 * Written by Harald Welte <laforge@openmoko.org>
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./common/cmd_test.c
cmd_test.c
```

```
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 */
```

```
./common/cmd_time.c
```

```
cmd_time.c
```

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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
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 */
```

```
./common/cmd_tpm.c
```

```
cmd_tpm.c
```

```
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* MA 02111-1307 USA
*/

./common/cmd_tsi148.c
cmd_tsi148.c
/*
* (C) Copyright 2009 Reinhard Arlt, reinhard.arlt@esd-electronics.com
*
* base on universe.h by
*
* (C) Copyright 2003 Stefan Roese, stefan.roese@esd-electronics.com
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./common/cmd_ubi.c
cmd_ubi.c
```

```
/*
 * Unsorted Block Image commands
 *
 * Copyright (C) 2008 Samsung Electronics
 * Kyungmin Park <kyungmin.park@samsung.com>
 *
 * Copyright 2008-2009 Stefan Roeser <sr@denx.de>, DENX Software Engineering
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 * published by the Free Software Foundation.
 */
```

```
./common/cmd_ubifs.c
```

```
cmd_ubifs.c
```

```
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./common/cmd_universe.c
```

```
cmd_universe.c
```

```
/*
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*/
```

```
./common/cmd_unzip.c
cmd_unzip.c
```

```
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* MA 02111-1307 USA
*/
```

```
./common/cmd_usb.c
cmd_usb.c
```

```
/*
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* Denis Peter, MPL AG Switzerland
*
* Most of this source has been derived from the Linux USB
* project.
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*
*/
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* MA 02111-1307 USA
*
*/
```

```
./common/cmd_version.c
cmd_version.c
```

```
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* MA 02111-1307 USA
*/
```

```
./common/cmd_ximg.c
cmd_ximg.c
```

```
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*
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* Kai-Uwe Bloem, Auerswald GmbH & Co KG, <linux-development@auerswald.de>
```

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* MA 02111-1307 USA
*/
```

```
./common/cmd_yaffs2.c
cmd_yaffs2.c
/* Yaffs commands.
* Modified by Charles Manning by adding ydevconfig command.
*
* Use ydevconfig to configure a mountpoint before use.
* For example:
* # Configure mountpt xxx using nand device 0 using blocks 100-500
* ydevconfig xxx 0 100 500
* # Mount it
* ymount xxx
* # yls, yrdm etc
* yls -l xxx
* yrdm xxx/boot-image 82000000
* ...
*/
```

```
./common/cmd_zfs.c
cmd_zfs.c
/*
*
* ZFS filesystem porting to Uboot by
* Jorgen Lundman <lundman at lundman.net>
*
* zfsfs support
* made from existing GRUB Sources by Sun, GNU and others.
*
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*
*/
```

```
./common/cmd_zip.c
```

```
cmd_zip.c
```

```
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* MA 02111-1307 USA
*/
```

```
./common/command.c
```

```
command.c
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./common/console.c
```

```
console.c
```

```
/*
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* Paolo Scaffardi, AIRVENT SAM s.p.a - RIMINI(ITALY), arsenio@tin.it
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./common/ddr_spd.c
```

```
ddr_spd.c
```

```
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*
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* modify it under the terms of the GNU General Public License
* Version 2 as published by the Free Software Foundation.
*/
/* used for ddr1 and ddr2 spd */
```

```
./common/dlmalloc.c
dlmalloc.c
/* ----- To make a malloc.h, start cutting here ----- */
/*
* VERSION 2.6.6 Sun Mar 5 19:10:03 2000 Doug Lea (dl at gee)
```

```
./common/edid.c
edid.c
/*
* Copyright (c) 2012 The Chromium OS Authors.
*
* (C) Copyright 2010
* Petr Stetiar <ynezz@true.cz>
*
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* project.
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* MA 02111-1307 USA
*
* Contains stolen code from ddcprobe project which is:
* Copyright (C) Nalin Dahyabhai <bigfun@pobox.com>
*
*/
```

```
./common/env_attr.c
env_attr.c
/*
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* MA 02111-1307 USA
*/
```

```
./common/env_callback.c
env_callback.c
```

```
/*
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*/
```

```
./common/env_common.c
env_common.c
```

```
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*
* (C) Copyright 2001 Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Andreas Heppel <aheppel@sysgo.de>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
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*/
```

```
./common/env_dataflash.c
env_dataflash.c
```

```
/*
* LowLevel function for DataFlash environment support
* Author : Gilles Gastaldi (Atmel)
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* MA 02111-1307 USA
*/
```

```
./common/env_eeprom.c
env_eeprom.c
```

```
/*
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*/
```

```
./common/env_embedded.c
```

```
env_embedded.c
```

```
/*
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* MA 02111-1307 USA
*/
```

```
./common/env_fat.c
```

```
env_fat.c
```

```
/*
* (c) Copyright 2011 by Tigris Elektronik GmbH
*
* Author:
* Maximilian Schwerin <mvs@tigris.de>
*
* See file CREDITS for list of people who contributed to this
* project.
```

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*/
```

```
./common/env_flags.c
```

```
env_flags.c
```

```
/*
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* Joe Hershberger, National Instruments, joe.hershberger@ni.com
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*/
```

```
./common/env_flash.c
```

```
env_flash.c
```

```
/*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* (C) Copyright 2001 Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Andreas Heppel <aheppel@sysgo.de>
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*/
```

```
./common/env_mmc.c
```

```
env_mmc.c
```

```
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*/
```

```
./common/env_nand.c
```

```
env_nand.c
```

```
/*
* (C) Copyright 2000-2010
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* (C) Copyright 2008
```

```
* Stuart Wood, Lab X Technologies <stuart.wood@labxtechnologies.com>
*
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* Jian Zhang, Texas Instruments, jzhang@ti.com.
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*/
```

```
./common/env_nowhere.c
```

```
env_nowhere.c
```

```
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```

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*/
```

```
./common/env_nvram.c
```

```
env_nvram.c
```

```
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*/
```

```
./common/env_onenand.c
```

```
env_onenand.c
```

```
/*  
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* Wolfgang Denk <wd@denx.de>  
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```

```
./common/env_remote.c
```

```
env_remote.c
```

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*/
```

```
./common/env_sf.c
```

```
env_sf.c
```

```
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*/
```

```
./common/exports.c
exports.c
No license information found.
```

```
./common/fdt_support.c
fdt_support.c
/*
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* Gerald Van Baren, Custom IDEAS, vanbaren@cideas.com
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*/
```

```
./common/flash.c
flash.c
/*
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*
*/
```

```
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*/
```

```
./common/hash.c
```

```
hash.c
```

```
/*
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* Joe Hershberger, National Instruments, joe.hershberger@ni.com
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* MA 02111-1307 USA
*/
```

```
./common/hush.c
```

```
hush.c
```

```
/*
* sh.c -- a prototype Bourne shell grammar parser
```

```

*      Intended to follow the original Thompson and Ritchie
*      "small and simple is beautiful" philosophy, which
*      incidentally is a good match to today's BusyBox.
*
* Copyright (C) 2000,2001 Larry Doolittle <larry@doolittle.boa.org>
*
* Credits:
*   The parser routines proper are all original material, first
*   written Dec 2000 and Jan 2001 by Larry Doolittle.
*   The execution engine, the builtins, and much of the underlying
*   support has been adapted from busybox-0.49pre's lash,
*   which is Copyright (C) 2000 by Lineo, Inc., and
*   written by Erik Andersen <andersen@lineo.com>, <andersee@debian.org>.
*   That, in turn, is based in part on ladsh.c, by Michael K. Johnson and
*   Erik W. Troan, which they placed in the public domain. I don't know
*   how much of the Johnson/Troan code has survived the repeated rewrites.
* Other credits:
*   b_addchr() derived from similar w_addchar function in glibc-2.2
*   setup_redirect(), redirect_opt_num(), and big chunks of main()
*   and many builtins derived from contributions by Erik Andersen
*   miscellaneous bugfixes from Matt Kraai
*
* There are two big (and related) architecture differences between
* this parser and the lash parser. One is that this version is
* actually designed from the ground up to understand nearly all
* of the Bourne grammar. The second, consequential change is that
* the parser and input reader have been turned inside out. Now,
* the parser is in control, and asks for input as needed. The old
* way had the input reader in control, and it asked for parsing to
* take place as needed. The new way makes it much easier to properly
* handle the recursion implicit in the various substitutions, especially
* across continuation lines.
*
* Bash grammar not implemented: (how many of these were in original sh?)
*   $@ (those sure look like weird quoting rules)
*   $_
*   ! negation operator for pipes
*   &> and >& redirection of stdout+stderr
*   Brace Expansion
*   Tilde Expansion
*   fancy forms of Parameter Expansion
*   aliases
*   Arithmetic Expansion
*   <(list) and >(list) Process Substitution
*   reserved words: case, esac, select, function
*   Here Documents ( << word )
*   Functions
* Major bugs:
*   job handling woefully incomplete and buggy
*   reserved word execution woefully incomplete and buggy

```

```
* to-do:
*   port selected bugfixes from post-0.49 busybox lash - done?
*   finish implementing reserved words: for, while, until, do, done
*   change { and } from special chars to reserved words
*   builtins: break, continue, eval, return, set, trap, ulimit
*   test magic exec
*   handle children going into background
*   clean up recognition of null pipes
*   check setting of global_argc and global_argv
*   control-C handling, probably with longjmp
*   follow IFS rules more precisely, including update semantics
*   figure out what to do with backslash-newline
*   explain why we use signal instead of sigaction
*   propagate syntax errors, die on resource errors?
*   continuation lines, both explicit and implicit - done?
*   memory leak finding and plugging - done?
*   more testing, especially quoting rules and redirection
*   document how quoting rules not precisely followed for variable assignments
*   maybe change map[] to use 2-bit entries
*   (eventually) remove all the printf's
*
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*/
```

```
./common/hwconfig.c
```

```
hwconfig.c
```

```
/*
* An interface for configuring a hardware via u-boot environment.
*
* Copyright (c) 2009 MontaVista Software, Inc.
* Copyright 2011 Freescale Semiconductor, Inc.
*
* Author: Anton Vorontsov <avorontsov@ru.mvista.com>
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* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2 of
* the License, or (at your option) any later version.
```

*/

./common/image.c

image.c

/*

* (C) Copyright 2008 Semihalf

*

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*/

./common/iomux.c

iomux.c

/*

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```
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*/
```

```
./common/kallsyms.c  
kallsyms.c
```

```
/*  
 * Helper functions for working with the builtin symbol table  
 *  
 * Copyright (c) 2008-2009 Analog Devices Inc.  
 * Licensed under the GPL-2 or later.  
 */  
/* We need the weak marking as this symbol is provided specially */  
/* Given an address, return a pointer to the symbol name and store  
 * the base address in caddr. So if the symbol map had an entry:  
 *  
 *       03fb9b7c_spi_cs_deactivate  
 * Then the following call:  
 *  
 *       unsigned long base;  
 *       const char *sym = symbol_lookup(0x03fb9b80, &base);  
 * Would end up setting the variables like so:  
 *       base = 0x03fb9b7c;  
 *       sym = "_spi_cs_deactivate";  
 */
```

```
./common/kgdb.c  
kgdb.c
```

```
/* taken from arch/powerpc/kernel/ppc-stub.c */  
/*****
```

```
./common/kgdb_stubs.c  
kgdb_stubs.c
```

```
/*  
 * U-boot - stub functions for common kgdb code,  
 *       can be overridden in board specific files  
 *  
 * Copyright 2009 Analog Devices Inc.  
 *  
 * Licensed under the GPL-2 or later.  
 */
```

```
./common/lcd.c  
lcd.c
```

```
/*  
 * Common LCD routines for supported CPUs  
 *  
 * (C) Copyright 2001-2002  
 * Wolfgang Denk, DENX Software Engineering -- wd@denx.de  
 *  
 * See file CREDITS for list of people who contributed to this  
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```

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*/
```

```
./common/lynxkdi.c
```

```
lynxkdi.c
```

```
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* purpose.
*/
```

```
./common/main.c
```

```
main.c
```

```
/*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* Add to readline cmdline-editing by
* (C) Copyright 2005
* JinHua Luo, Guangdong Linux Center, <luo.jinhua@gd-linux.com>
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* MA 02111-1307 USA
*/
```

```
./common/memsize.c
```

```
memsize.c
```

```
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*/
```

```
./common/menu.c
```

```
menu.c
```

```
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*/
```

```
./common/miiphyutil.c
```

```
miiphyutil.c
```

```
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*/
```

```
./common/modem.c
```

```
modem.c
```

```
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*/
```

```
./common/spl/spl.c
```

```
spl.c
```

```
/*
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* Aneesh V <aneesh@ti.com>
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```

```
./common/spl/spl_nand.c
```

```
spl_nand.c
```

```
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```

```
./common/spl/spl_net.c
```

```
spl_net.c
```

```
/*
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```
./common/spl/spl_nor.c
```

```
spl_nor.c
```

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```

```
./common/spl/spl_ymodem.c
```

```
spl_ymodem.c
```

```
/*
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 */
```

```
./common/stdio.c
```

```
stdio.c
```

```
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*/
```

```
./common/system_map.c
```

```
system_map.c
```

```
/*
* The builtin symbol table for use with kallsyms
*
* Copyright (c) 2008-2009 Analog Devices Inc.
* Licensed under the GPL-2 or later.
*/
```

```
./common/s_record.c
```

```
s_record.c
```

```
/*
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*/
```

```
./common/update.c
```

```
update.c
```

```
/*
* (C) Copyright 2008 Semihalf
*
* Written by: Rafal Czubak <rcz@semihalf.com>
```

```
*           Bartlomiej Sieka <tur@semihalf.com>
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*/
```

```
./common/usb.c
```

```
usb.c
```

```
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```
./common/usb_hub.c
```

```
usb_hub.c
```

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*/
```

./common/usb_kbd.c

usb_kbd.c

```
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```

./common/usb_storage.c

usb_storage.c

```
/*
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 * (c) 2000 David L. Brown, Jr. (usb-storage@davidb.org)
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*/

./common/xyzModem.c
xyzModem.c
/*
*=====
*
*      xyzModem.c
*
*      RedBoot stream handler for xyzModem protocol
*
*=====
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* -----
*####ECOSGPLCOPYRIGHTEND####
*=====
*#####DESCRIPTIONBEGIN####
*
* Author(s):      gthomas
* Contributors:   gthomas, tsmith, Yoshinori Sato
* Date:          2000-07-14
* Purpose:
* Description:
*
* This code is part of RedBoot (tm).
*
*#####DESCRIPTIONEND####
*=====
*/
```

./disk/part.c

part.c

```
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*/

./disk/part_amiga.c

part_amiga.c

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```

```
./disk/part_dos.c
```

```
part_dos.c
```

```
/*
```

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```
part_dos.h
```

```
/*
```

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```
./disk/part_efi.c
```

```
part_efi.c
```

```
/*
```

```
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```
./disk/part_iso.c
```

```
part_iso.c
```

```
/*
```

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```

```
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```

```
part_iso.h
```

```
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```
*/
```

```
./disk/part_mac.c
```

```
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```

```
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```

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```
./disk/part_mac.h
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```
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```
/*
```

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*/
```

```
./drivers/bios_emulator/atibios.c
```

```
atibios.c
```

```
/******
```

```
*/
```

```
* Video BOOT Graphics Card POST Module
```

```
*/
```

```
* =====
```

```
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* Jason Jin <Jason.jin@freescale.com>
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*
* =====
*
* Language:      ANSI C
* Environment:  Linux Kernel
* Developer:    Kendall Bennett
*
* Description:  Module to implement booting PCI/AGP controllers on the
*              bus. We use the x86 real mode emulator to run the BIOS on
*              graphics controllers to bring the cards up.
*
*              Note that at present this module does *not* support
*              multiple controllers.
*
*              The original name of this file is warmboot.c.
*              Jason ported this file to u-boot to run the ATI video card
*              BIOS in u-boot.
*****/

./drivers/bios_emulator/besys.c
besys.c
/*****
*
*              BIOS emulator and interface
*              to Realmode X86 Emulator Library
*
* =====
*
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* Jason Jin<Jason.jin@freescale.com>
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*
* =====
*
* Language:      ANSI C
* Environment:  Any
* Developer:    Kendall Bennett
*
* Description:  This file includes BIOS emulator I/O and memory access
*              functions.
*
*              Jason ported this file to u-boot to run the ATI video card
*              BIOS in u-boot. Removed some emulate functions such as the
*              timer port access. Made all the VGA port except reading 0x3c3
*              be emulated. Seems like reading 0x3c3 should return the high
*              16 bit of the io port.
*
*
* *****/
./drivers/bios_emulator/bios.c
bios.c
/*****
*
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*              to Realmode X86 Emulator Library
*
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* =====

* Language: ANSI C
* Environment: Any
* Developer: Kendall Bennett

* Description: Module implementing the BIOS specific functions.

* Jason ported this file to u-boot to run the ATI video card
* video BIOS.

*****/

./drivers/bios_emulator/biosemu.c

biosemu.c

/*****

* BIOS emulator and interface
* to Realmode X86 Emulator Library

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* Jason Jin <Jason.jin@freescale.com>

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* Language: ANSI C
* Environment: Any
* Developer: Kendall Bennett

* Description: Module implementing the system specific functions. This
* module is always compiled and linked in the OS dependent
* libraries, and never in a binary portable driver.

Jason ported this file to u-boot to run the ATI video card BIOS
in u-boot. Made all the video memory be emulated during the
BIOS running process which may affect the VGA function but the
frambuffer function can work after run the BIOS.

*****/

./drivers/bios_emulator/biosemui.h
biosemui.h

/*****

BIOS emulator and interface
to Realmode X86 Emulator Library

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Jason Jin <Jason.jin@freescale.com>

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* Language: ANSI C
* Environment: Any
* Developer: Kendall Bennett

* Description: Internal header file for the BIOS emulator library.

* Jason ported this file to u-boot, Added some architecture
* related Macro.

*****/

./drivers/bios_emulator/include/biosemu.h

biosemu.h

/******

* BIOS emulator and interface
* to Realmode X86 Emulator Library

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*

```
* =====
*
* Language:      ANSI C
* Environment:   Any
* Developer:     Kendall Bennett
*
* Description:   Header file for the real mode x86 BIOS emulator, which is
*               used to warmboot any number of VGA compatible PCI/AGP
*               controllers under any OS, on any processor family that
*               supports PCI. We also allow the user application to call
*               real mode BIOS functions and Int 10h functions (including
*               the VESA BIOS).
*
* *****/
```

```
./drivers/bios_emulator/include/x86emu/debug.h
debug.h
```

```
/******
```

```
*
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*
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*               Copyright (C) 1999 Egbert Eich
*
* =====
```

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*
* =====
```

```
*
* Language:      ANSI C
* Environment:   Any
* Developer:     Kendall Bennett
```

```
*
* Description: Header file for debug definitions.
*
*****/
```

```
./drivers/bios_emulator/include/x86emu/decode.h
decode.h
```

```
/******
```

```
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```

```
* =====
```

```
* Language:      ANSI C
* Environment:   Any
* Developer:     Kendall Bennett
*
```

```
* Description:   Header file for instruction decoding logic.
```

```
*****/
```

```
./drivers/bios_emulator/include/x86emu/ops.h
ops.h
```

```
/******
```

```
*
*
*           Realmode X86 Emulator Library
```


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* =====

* Language: Watcom C++ 10.6 or later
* Environment: Any
* Developer: Kendall Bennett

* Description: Inline assembler versions of the primitive operand
* functions for faster performance. At the moment this is
* x86 inline assembler, but these functions could be replaced
* with native inline assembler for each supported processor
* platform.

*****/

./drivers/bios_emulator/include/x86emu/prim_ops.h
prim_ops.h

/******

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*
* =====
*
* Language:      ANSI C
* Environment:   Any
* Developer:    Kendall Bennett
*
* Description:   Header file for primitive operation functions.
*
*****/

./drivers/bios_emulator/include/x86emu/regs.h
regs.h
/*****
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* =====
```

```
*
* Language:      ANSI C
* Environment:   Any
* Developer:    Kendall Bennett
*
* Description:   Header file for x86 register definitions.
*
*****/

./drivers/bios_emulator/include/x86emu/x86emui.h
x86emui.h
/*****
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*
* =====
*
* Language:      ANSI C
* Environment:   Any
* Developer:    Kendall Bennett
*
* Description:   Header file for system specific functions. These functions
*               are always compiled and linked in the OS depedent libraries,
*               and never in a binary portable driver.
*
*****/
```

```
./drivers/bios_emulator/include/x86emu.h
x86emu.h
```

```
/******
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*
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```
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```

```
*
```

```
* Language: ANSI C
* Environment: Any
* Developer: Kendall Bennett
```

```
*
```

```
* Description: Header file for public specific functions.
* Any application linking against us should only
* include this header
```

```
*
```

```
*****/
```

```
./drivers/bios_emulator/x86emu/debug.c
debug.c
```

```
/******
```

```
*
```

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*
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```
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```

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*
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=====

Language: ANSI C
Environment: Any
Developer: Kendall Bennett

Description: This file contains the code to handle debugging of the emulator.

*****/

./drivers/bios_emulator/x86emu/decode.c
decode.c

/*****

Realmode X86 Emulator Library

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* =====

* Language: ANSI C
* Environment: Any
* Developer: Kendall Bennett

* Description: This file includes subroutines which are related to
* instruction decoding and access of immediate data via IP. etc.

*****/

./drivers/bios_emulator/x86emu/ops.c
ops.c

/*****

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*

* =====

*

* Language: ANSI C
* Environment: Any
* Developer: Kendall Bennett

*

* Description: This file includes subroutines to implement the decoding
* and emulation of all the x86 processor instructions.

*

* There are approximately 250 subroutines in here, which correspond
* to the 256 byte-"opcodes" found on the 8086. The table which
* dispatches this is found in the files optab.[ch].

*

* Each opcode proc has a comment preceding it which gives it's table
* address. Several opcodes are missing (undefined) in the table.

*

* Each proc includes information for decoding (DECODE_PRINTF and
* DECODE_PRINTF2), debugging (TRACE_REGS, SINGLE_STEP), and misc
* functions (START_OF_INSTR, END_OF_INSTR).

*

* Many of the procedures are *VERY* similar in coding. This has
* allowed for a very large amount of code to be generated in a fairly
* short amount of time (i.e. cut, paste, and modify). The result is
* that much of the code below could have been folded into subroutines
* for a large reduction in size of this file. The downside would be
* that there would be a penalty in execution speed. The file could
* also have been *MUCH* larger by inlining certain functions which
* were called. This could have resulted even faster execution. The
* prime directive I used to decide whether to inline the code or to
* modularize it, was basically: 1) no unnecessary subroutine calls,
* 2) no routines more than about 200 lines in size, and 3) modularize
* any code that I might not get right the first time. The fetch_*
* subroutines fall into the latter category. The The decode_* fall
* into the second category. The coding of the "switch(mod){ }"
* in many of the subroutines below falls into the first category.
* Especially, the coding of {add,and,or,sub,...}_{byte,word}
* subroutines are an especially glaring case of the third guideline.
* Since so much of the code is cloned from other modules (compare
* opcode #00 to opcode #01), making the basic operations subroutine
* calls is especially important; otherwise mistakes in coding an
* "add" would represent a nightmare in maintenance.

*

*****/

```

./drivers/bios_emulator/x86emu/ops2.c
ops2.c
/*****
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*
* =====
*
* Language:      ANSI C
* Environment:   Any
* Developer:     Kendall Bennett
*
* Description:   This file includes subroutines to implement the decoding
*               and emulation of all the x86 extended two-byte processor
*               instructions.
*
*****/

./drivers/bios_emulator/x86emu/prim_ops.c
prim_ops.c
/*****
*

```

Realmode X86 Emulator Library

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=====

Language: ANSI C
Environment: Any
Developer: Kendall Bennett

Description: This file contains the code to implement the primitive machine operations used by the emulation code in ops.c

Carry Chain Calculation

This represents a somewhat expensive calculation which is apparently required to emulate the setting of the OF343364 and AF flag. The latter is not so important, but the former is. The overflow flag is the XOR of the top two bits of the carry chain for an addition (similar for subtraction). Since we do not want to simulate the addition in a bitwise manner, we try to calculate the carry chain given the two operands and the result.

So, given the following table, which represents the addition of two bits, we can derive a formula for the carry chain.

a	b	cin	r	cout
0	0	0	0	0

```

* 0  0  1  1  0
* 0  1  0  1  0
* 0  1  1  0  1
* 1  0  0  1  0
* 1  0  1  0  1
* 1  1  0  0  1
* 1  1  1  1  1
*

```

* Construction of table for cout:

```

* ab
* r \ 00  01  11  10
* |-----
* 0 |  0  1  1  1
* 1 |  0  0  1  0
*

```

* By inspection, one gets: $cc = ab + r'(a + b)$

* That represents alot of operations, but NO CHOICE....

* Borrow Chain Calculation.

* The following table represents the subtraction of two bits, from which we can derive a formula for the borrow chain.

```

* a  b  bin  r   bout
* 0  0  0    0   0
* 0  0  1    1   1
* 0  1  0    1   1
* 0  1  1    0   1
* 1  0  0    1   0
* 1  0  1    0   0
* 1  1  0    0   0
* 1  1  1    1   1
*

```

* Construction of table for cout:

```

* ab
* r \ 00  01  11  10
* |-----
* 0 |  0  1  0  0
* 1 |  1  1  1  0
*

```

* By inspection, one gets: $bc = a'b + r(a' + b)$

*****/

./drivers/bios_emulator/x86emu/sys.c

sys.c

/******

```

*
*           Realmode X86 Emulator Library
*
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*           Copyright (C) David Mosberger-Tang
*           Copyright (C) 1999 Egbert Eich
*
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* PERFORMANCE OF THIS SOFTWARE.
*
* =====
*
* Language:      ANSI C
* Environment:   Any
* Developer:    Kendall Bennett
*
* Description:   This file includes subroutines which are related to
*               programmed I/O and memory access. Included in this module
*               are default functions that do nothing. For real uses these
*               functions will have to be overridden by the user library.
*
* *****/

./drivers/block/ahci.c
ahci.c
/*
* Copyright (C) Freescale Semiconductor, Inc. 2006.
* Author: Jason Jin<Jason.jin@freescale.com>
*        Zhang Wei<wei.zhang@freescale.com>
*
* See file CREDITS for list of people who contributed to this
* project.
*

```

```
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
* with the reference on libata and ahci drvier in kernel
*
*/
```

```
./drivers/block/ata_piix.c
```

```
ata_piix.c
```

```
/*
* Copyright (C) Procsys. All rights reserved.
* Author: Mushtaq Khan <mushtaq_k@procsys.com>
*          <mushtaqk_921@yahoo.co.in>
*
* This program is free software; you can redistribute it and/or
* modify it under the terms of the GNU General Public License as
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* the License, or (at your option) any later version.
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* MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
* GNU General Public License for more details.
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
* with the reference to ata_piix driver in kernel 2.4.32
*/
```

```
./drivers/block/ata_piix.h
```

```
ata_piix.h
```

```
No license information found.
```

```
./drivers/block/dwc_ahsata.c
```

```
dwc_ahsata.c
```

```
/*
 * Copyright (C) 2010-2011 Freescale Semiconductor, Inc.
 * Terry Lv <r65388@freescale.com>
 *
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 * project.
 *
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 * GNU General Public License for more details.
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc.
 */
```

```
./drivers/block/dwc_ahsata.h
dwc_ahsata.h
```

```
/*
 * Copyright (C) 2010 Freescale Semiconductor, Inc.
 * Terry Lv <r65388@freescale.com>
 *
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 * the License, or (at your option) any later version.
 *
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 * MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
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 *
 * You should have received a copy of the GNU General Public License
 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./drivers/block/fsl_sata.c
fsl_sata.c
```

```
/*
 * Copyright (C) 2008,2010 Freescale Semiconductor, Inc.
 * Dave Liu <daveliu@freescale.com>
```

```
*
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* modify it under the terms of the GNU General Public License as
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/block/fsl_sata.h
```

```
fsl_sata.h
```

```
/*
* Copyright (C) 2007-2008 Freescale Semiconductor, Inc.
* Dave Liu <daveliu@freescale.com>
*
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* modify it under the terms of the GNU General Public License as
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* the License, or (at your option) any later version.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/block/ftide020.c
```

```
ftide020.c
```

```
/*
* Faraday FTIDE020 ATA Controller (AHB)
*
* (C) Copyright 2011 Andes Technology
* Greentime Hu <greentime@andestech.com>
* Macpaul Lin <macpaul@andestech.com>
* Kuo-Wei Chou <kwchou@andestech.com>
*
* See file CREDITS for list of people who contributed to this
```

```
* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
/* ftide020.c - ide support functions for the FTIDE020_S controller */

./drivers/block/ftide020.h
ftide020.h
/*
* Faraday FTIDE020_s ATA Controller (AHB)
*
* (C) Copyright 2011 Andes Technology
* Greentime Hu <greentime@andestech.com>
* Macpaul Lin <macpaul@andestech.com>
* Kuo-Wei Chou <kwchou@andestech.com>
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* See file CREDITS for list of people who contributed to this
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
```

./drivers/block/libata.c

libata.c

```
/*
 * Copyright (C) 2008 Freescale Semiconductor, Inc.
 *           Dave Liu <daveliu@freescale.com>
 *           port from the libata of linux kernel
 *
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 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./drivers/block/mvsata_ide.c

mvsata_ide.c

```
/*
 * Copyright (C) 2010 Albert ARIBAUD <albert.u.boot@aribaud.net>
 *
 * Written-by: Albert ARIBAUD <albert.u.boot@aribaud.net>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 * modify it under the terms of the GNU General Public License as
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 *
 * You should have received a copy of the GNU General Public License
 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
 * MA 02110-1301 USA
 */
```

```
./drivers/block/mxc_ata.c
```

```
mxc_ata.c
```

```
/*  
 * Freescale iMX51 ATA driver  
 *  
 * Copyright (C) 2010 Marek Vasut <marek.vasut@gmail.com>  
 *  
 * Based on code by:  
 *     Mahesh Mahadevan <mahesh.mahadevan@freescale.com>  
 *  
 * Based on code from original FSL ATA driver, which is  
 * part of eCos, the Embedded Configurable Operating System.  
 * Copyright (C) 1998, 1999, 2000, 2001, 2002 Red Hat, Inc.  
 *  
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 * You should have received a copy of the GNU General Public License  
 * along with this program; if not, write to the Free Software  
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
 * MA 02111-1307 USA  
 */
```

```
./drivers/block/pata_bfin.c
```

```
pata_bfin.c
```

```
/*  
 * Driver for Blackfin on-chip ATAPI controller.  
 *  
 * Enter bugs at http://blackfin.uclinux.org/  
 *  
 * Copyright (c) 2008 Analog Devices Inc.  
 *  
 * Licensed under the GPL-2 or later.  
 */
```

```
./drivers/block/pata_bfin.h
```

```
pata_bfin.h
```

```
/*  
 * Driver for Blackfin on-chip ATAPI controller.  
 *  
 * Enter bugs at http://blackfin.uclinux.org/  
 *  
 * Copyright (c) 2008 Analog Devices Inc.
```

```
*
* Licensed under the GPL-2 or later.
*/

./drivers/block/sata_dwc.c
sata_dwc.c
/*
* sata_dwc.c
*
* Synopsys DesignWare Cores (DWC) SATA host driver
*
* Author: Mark Miesfeld <mmiesfeld@amcc.com>
*
* Ported from 2.6.19.2 to 2.6.25/26 by Stefan Roese <sr@denx.de>
* Copyright 2008 DENX Software Engineering
*
* Based on versions provided by AMCC and Synopsys which are:
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* it and/or modify it under the terms of the GNU
* General Public License as published by the
* Free Software Foundation; either version 2 of the License,
* or (at your option) any later version.
*
*/
/*
* SATA support based on the chip canyonlands.
*
* 04-17-2009
*     The local version of this driver for the canyonlands board
*     does not use interrupts but polls the chip instead.
*/
```

```
./drivers/block/sata_dwc.h
sata_dwc.h
/*
* sata_dwc.h
*
* Synopsys DesignWare Cores (DWC) SATA host driver
*
* Author: Mark Miesfeld <mmiesfeld@amcc.com>
*
* Ported from 2.6.19.2 to 2.6.25/26 by Stefan Roese <sr@denx.de>
* Copyright 2008 DENX Software Engineering
*
* Based on versions provided by AMCC and Synopsys which are:
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* it and/or modify it under the terms of the GNU
* General Public License as published by the
* Free Software Foundation; either version 2 of the License,
* or (at your option) any later version.
*
*/
/*
* SATA support based on the chip canyonlands.
*
* 04-17-2009
*           The local version of this driver for the canyonlands board
*           does not use interrupts but polls the chip instead.
*/
```

```
./drivers/block/sata_sil.c
```

```
sata_sil.c
```

```
/*
* Copyright (C) 2011 Freescale Semiconductor, Inc.
* Author: Tang Yuantian <b29983@freescale.com>
*
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* modify it under the terms of the GNU General Public License as
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/block/sata_sil.h
```

```
sata_sil.h
```

```
/*
* Copyright (C) 2011 Freescale Semiconductor, Inc.
* Author: Tang Yuantian <b29983@freescale.com>
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/block/sata_sil3114.c
```

```
sata_sil3114.c
```

```
/*
* Copyright (C) Excito Elektronik i Skåne AB, All rights reserved.
* Author: Tor Krill <tor@excito.com>
*
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
* This is a driver for Silicon Image sil3114 sata chip modelled on
* the ata_piix driver
*/
```

```
./drivers/block/sata_sil3114.h
```

```
sata_sil3114.h
```

```
/*
* Copyright (C) Excito Elektronik i Skåne AB, All rights reserved.
* Author: Tor Krill <tor@excito.com>
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/block/sil680.c
sil680.c
```

```
/*
* (C) Copyright 2007
* Gary Jennejohn, DENX Software Engineering, garyj@denx.de.
*
* See file CREDITS for list of people who contributed to this
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
/* sil680.c - ide support functions for the Sil0680A controller */
```

```
./drivers/block/sym53c8xx.c
sym53c8xx.c
```

```
/*
* (C) Copyright 2001
* Denis Peter, MPL AG Switzerland, d.peter@mpl.ch.
*
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```

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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
* partly derived from
* linux/drivers/scsi/sym53c8xx.c
*
*/
```

```
./drivers/block/systemace.c
systemace.c
```

```
/*
* Copyright (c) 2004 Picture Elements, Inc.
* Stephen Williams (XXXXXXXXXXXXXXXXXX)
*
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* General Public License as published by the Free Software
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place - Suite 330, Boston, MA 02111-1307, USA
*/
```

```
./drivers/bootcount/bootcount.c
bootcount.c
```

```
/*
* (C) Copyright 2010-2012
* Stefan Roes, DENX Software Engineering, sr@denx.de.
*
* See file CREDITS for list of people who contributed to this
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/bootcount/bootcount_at91.c
bootcount_at91.c
```

```
/*
* See file CREDITS for list of people who contributed to this
* project.
*
* This program is free software; you can redistribute it and/or
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* MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
* GNU General Public License for more details.
*/
```

```
./drivers/bootcount/bootcount_blackfin.c
bootcount_blackfin.c
```

```
/*
* functions for handling bootcount support
*
* Copyright (c) 2010 Analog Devices Inc.
*
* Licensed under the 2-clause BSD.
*/
/* This version uses one 32bit storage and combines the magic/count */
/* We abuse the EVT0 MMR for bootcount storage by default */
```

```
./drivers/bootcount/bootcount_davinci.c
bootcount_davinci.c
```

```
/*
* (C) Copyright 2011
* Heiko Schocher, DENX Software Engineering, hs@denx.de.
*
* See file CREDITS for list of people who contributed to this
* project.
*
* This program is free software; you can redistribute it and/or
* modify it under the terms of the GNU General Public License as
```

```
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* MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
* GNU General Public License for more details.
*
*/
```

```
./drivers/bootcount/bootcount_ram.c
```

```
bootcount_ram.c
```

```
/*
* (C) Copyright 2010
* Heiko Schocher, DENX Software Engineering, hs@denx.de.
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* See file CREDITS for list of people who contributed to this
* project.
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*
*/
```

```
./drivers/dfu/dfu.c
```

```
dfu.c
```

```
/*
* dfu.c -- DFU back-end routines
*
* Copyright (C) 2012 Samsung Electronics
* author: Lukasz Majewski <l.majewski@samsung.com>
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*/
```

```
./drivers/dfu/dfu_mmc.c
```

```
dfu_mmc.c
```

```
/*
 * dfu.c -- DFU back-end routines
 *
 * Copyright (C) 2012 Samsung Electronics
 * author: Lukasz Majewski <l.majewski@samsung.com>
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*/
```

```
./drivers/dma/apbh_dma.c
```

```
apbh_dma.c
```

```
/*
 * Freescale i.MX28 APBH DMA driver
 *
 * Copyright (C) 2011 Marek Vasut <marek.vasut@gmail.com>
 * on behalf of DENX Software Engineering GmbH
 *
 * Based on code from LTIB:
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```

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*/

./drivers/dma/fsl_dma.c

fsl_dma.c

/*

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* (C) Copyright 2002, 2003 Motorola Inc.

* Xianghua Xiao (X.Xiao@motorola.com)

*

* (C) Copyright 2000

* Wolfgang Denk, DENX Software Engineering, wd@denx.de.

*

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*/

./drivers/dma/MCD_dmaApi.c

MCD_dmaApi.c

/*

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```

```
./drivers/dma/MCD_tasks.c
```

```
MCD_tasks.c
```

```
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```

```
./drivers/dma/MCD_tasksInit.c
```

```
MCD_tasksInit.c
```

```
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```

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*/
```

```
./drivers/dma/omap3_dma.c
```

```
omap3_dma.c
```

```
/* Copyright (C) 2011
```

```
* Corscience GmbH & Co. KG - Simon Schwarz <schwarz@corscience.de>
```

```
*/
```

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```
*/
```

```
./drivers/dma/pl330.c
```

```
pl330.c
```

```
/*
```

```
* Copyright (C) 2013 Altera Corporation <www.altera.com>
```

```
*/
```

```
* Copyright (C) 2010 Samsung Electronics Co Ltd.
```

```
* Jaswinder Singh <jassi.brar@samsung.com>
```

```
*/
```

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```

```
*/
```

```
./drivers/fpga/ACEX1K.c
```

```
ACEX1K.c
```

```
/*
 * (C) Copyright 2003
 * Steven Scholz, imc Measurement & Control, steven.scholz@imc-berlin.de
 *
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 * Rich Ireland, Enterasys Networks, rireland@enterasys.com.
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 */
```

```
./drivers/fpga/altera.c
```

```
altera.c
```

```
/*
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 *
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 *
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*
*/
```

```
./drivers/fpga/cyclon2.c
cyclon2.c
```

```
/*
* (C) Copyright 2006
* Heiko Schocher, hs@denx.de
* Based on ACE1XK.c
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```

```
./drivers/fpga/fpga.c
fpga.c
```

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*  
*/
```

```
./drivers/fpga/ivm_core.c  
ivm_core.c
```

```
/*  
* Porting to u-boot:  
*  
* (C) Copyright 2010  
* Stefano Babic, DENX Software Engineering, sbabic@denx.de.  
*  
* Lattice ispVME Embedded code to load Lattice's FPGA:  
*  
* Copyright 2009 Lattice Semiconductor Corp.  
*  
* ispVME Embedded allows programming of Lattice's suite of FPGA  
* devices on embedded systems through the JTAG port. The software  
* is distributed in source code form and is open to re - distribution  
* and modification where applicable.  
*  
* Revision History of ivm_core.c module:  
* 4/25/06 ht Change some variables from unsigned short or int  
* to long int to make the code compiler independent.  
* 5/24/06 ht Support using RESET (TRST) pin as a special purpose  
* control pin such as triggering the loading of known  
* state exit.  
* 3/6/07 ht added functions to support output to terminals  
*  
* 09/11/07 NN Type cast mismatch variables  
* Moved the sclock() function to hardware.c  
* 08/28/08 NN Added Calculate checksum support.  
* 4/1/09 Nguyen replaced the recursive function call codes on  
* the ispVMLCOUNT function  
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*  
*/
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*/
```

```
./drivers/fpga/lattice.c
```

```
lattice.c
```

```
/*  
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*  
* (C) Copyright 2002  
* Rich Ireland, Enterasys Networks, rireland@enterasys.com.  
*  
* ispVM functions adapted from Lattice's ispmVMEEmbedded code:  
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```

```
./drivers/fpga/spartan2.c
```

```
spartan2.c
```

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```
./drivers/fpga/spartan3.c
```

```
spartan3.c
```

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```

```
./drivers/fpga/stratixII.c
```

```
stratixII.c
```

```
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```

```
./drivers/fpga/virtex2.c
```

```
virtex2.c
```

```
/*
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```

```
./drivers/fpga/xilinx.c
```

```
xilinx.c
```

```
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 */

./drivers/gpio/altera_pio.c
altera_pio.c
/*
 * Driver for Altera's PIO ip core
 *
 * Copyright (C) 2011 Missing Link Electronics
 * Joachim Foerster <joachim@missinglinkelectronics.com>
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 *
 * To use this driver, in your board's config. header:
 * #define CONFIG_ALTERA_PIO
```

```

* #define CONFIG_SYS_ALTERA_PIO_NUM <number-of-pio-cores>
* #define CONFIG_SYS_ALTERA_PIO_GPIO_NUM <total-number-of-gpios>
* And in your board's early setup routine:
* altera_pio_init(<baseaddr>, <width>, 'i'|'o'|'t',
*               <reset-value>, <neg-mask>, "label");
* - 'i'|'o'|'t': PIO is input-only/output-only/tri-state
* - <reset-value>: for correct initial status display, output-only
* - <neg-mask> is meant to be used to in cases of active-low
*   GPIOs, such as LEDs and buttons (on/pressed == 0). Each bit
*   which is 1 in <neg-mask> inverts the corresponding GPIO's value
*   before set/after get. So: gpio_set_value(gpio, 1) => LED on .
*
* Do NOT define CONFIG_SYS_GPIO_BASE !
*
* Optionally, in your board's config. header:
* - To force a GPIO numbering scheme like in Linux ...
* #define CONFIG_GPIO_DOWNT0_NUMBERING
* ... starting with 255 (default)
* #define CONFIG_GPIO_DOWNT0_MAX 255
*/

```

./drivers/gpio/at91_gpio.c

at91_gpio.c

```

/*
* Memory Setup stuff - taken from blob memsetup.S
*
* Copyright (C) 2009 Jens Scharsig (js_at_ng@scharsoft.de)
*
* Copyright (C) 2005 HP Labs
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*/

```

./drivers/gpio/bcm2835_gpio.c

bcm2835_gpio.c

```
/*
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 * <vikram186@gmail.com>
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 */
```

./drivers/gpio/da8xx_gpio.c

da8xx_gpio.c

```
/*
 * GPIO driver for TI DaVinci DA8xx SOCs.
 *
 * (C) Copyright 2011 Guralp Systems Ltd.
 * Laurence Withers <lwithers@guralp.com>
 *
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 * MA 02111-1307 USA
 */
```

./drivers/gpio/db8500_gpio.c

db8500_gpio.c

```
/*
 * Code ported from Nomadik GPIO driver in ST-Ericsson Linux kernel code.
 * The purpose is that GPIO config found in kernel should work by simply
 * copy-paste it to U-boot.
 */
```

```
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*   Rewritten based on work by Prafulla WADASKAR <prafulla.wadaskar@st.com>
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```

```
./drivers/gpio/intel_ich6_gpio.c
```

```
intel_ich6_gpio.c
```

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*/
```

```
./drivers/gpio/kw_gpio.c
```

```
kw_gpio.c
```

```
/*
* arch/arm/plat-orion/gpio.c
*
* Marvell Orion SoC GPIO handling.
*
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```
./drivers/gpio/mpc83xx_gpio.c
mpc83xx_gpio.c
```

```
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* Freescale MPC83xx GPIO handling.
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*/
```

```
./drivers/gpio/mvgpio.c
mvgpio.c
```

```
/*
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* Written-by: Ajay Bhargav <ajay.bhargav@einfochips.com>
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*/
```

```
./drivers/gpio/mvgpio.h
mvgpio.h
```

```
/*
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*/
```

```
./drivers/gpio/mvmfp.c
mvmfp.c
```

```
/*
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* Marvell Semiconductor <www.marvell.com>
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>,
```

```
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*/
```

```
./drivers/gpio/mxc_gpio.c
```

```
mxc_gpio.c
```

```
/*
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* Guennadi Liakhovetski, DENX Software Engineering, <lg@denx.de>
*
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* Stefano Babic, DENX Software Engineering, <sbabic@denx.de>
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```

```
./drivers/gpio/mxs_gpio.c
```

```
mxs_gpio.c
```

```
/*
 * Freescale i.MX28 GPIO control code
 *
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 */

./drivers/gpio/omap_gpio.c
omap_gpio.c
/*
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 * Tom Rix <Tom.Rix@windriver.com>
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 * To fetch, use the kernel repository
 * git://git.kernel.org/pub/scm/linux/kernel/git/torvalds/linux-2.6.git
 * Use the v2.6.27 tag.
```

```
*
* Below is the original's header including its copyright
*
* linux/arch/arm/plat-omap/gpio.c
*
* Support functions for OMAP GPIO
*
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* Written by Juha Yrjölä <juha.yrjola@nokia.com>
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```

```
./drivers/gpio/pca953x.c
pca953x.c
```

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```

```
./drivers/gpio/pca9698.c
pca9698.c
```

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*/
```

```
./drivers/gpio/s3c2440_gpio.c
```

```
s3c2440_gpio.c
```

```
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```

```
./drivers/gpio/s5p_gpio.c
```

```
s5p_gpio.c
```

```
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*/
```

```
./drivers/gpio/sandbox.c
```

```
sandbox.c
```

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```

```
./drivers/gpio/sh_pfc.c
```

```
sh_pfc.c
```

```
/*
* Pinmuxed GPIO support for SuperH.
* Copy from linux kernel driver/sh/pfc.c
*
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*/
```

```
./drivers/gpio/spear_gpio.c
```

```
spear_gpio.c
```

```
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*/
```

```
./drivers/gpio/tegra_gpio.c
```

```
tegra_gpio.c
```

```
/*
* NVIDIA Tegra20 GPIO handling.
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* NVIDIA Corporation <www.nvidia.com>
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*/
```

```
./drivers/hwmon/adm1021.c
```

```
adm1021.c
```

```
/*
* (C) Copyright 2003
* Murray Jensen, CSIRO-MIT, Murray.Jensen@csiro.au
*
* based on dtt/lm75.c which is ...
```

```
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* Bill Hunter, Wave 7 Optics, williamhunter@mediaone.net
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```

```
./drivers/hwmon/adt7460.c
adt7460.c
```

```
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```

```
./drivers/hwmon/ds1621.c
ds1621.c
```

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*/
```

```
./drivers/hwmon/ds1722.c
ds1722.c
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```

```
./drivers/hwmon/ds1775.c
ds1775.c
```

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```

```
./drivers/hwmon/lm63.c
lm63.c
```

```
/*
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* based on lm75.c by Bill Hunter
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```

```
./drivers/hwmon/lm73.c
```

```
lm73.c
```

```
/*
```

```
* (C) Copyright 2007-2008
```

```
* Larry Johnson, lrj@acm.org
```

```
*
```

```
* based on dtt/lm75.c which is ...
```

```
*
```

```
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```

```
* Bill Hunter, Wave 7 Optics, williamhunter@mediaone.net
```

```
*
```

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*
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```
*/
```

```
./drivers/hwmon/lm75.c
```

```
lm75.c
```

```
/*
```

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*/
```

```
./drivers/hwmon/lm81.c
```

```
lm81.c
```

```
/*
```

```
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*
* based on dtt/lm75.c which is ...
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*/
```

./drivers/i2c/bfin-twi_i2c.c

bfin-twi_i2c.c

```
/*
 * i2c.c - driver for Blackfin on-chip TWI/I2C
 *
 * Copyright (c) 2006-2010 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */
```

./drivers/i2c/davinci_i2c.c

davinci_i2c.c

```
/*
 * TI DaVinci (TMS320DM644x) I2C driver.
 *
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 *
 * -----
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./drivers/i2c/designware_i2c.c

designware_i2c.c

```
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```

```
./drivers/i2c/designware_i2c.h
designware_i2c.h
```

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* MA 02111-1307 USA
*/
```

```
./drivers/i2c/fsl_i2c.c
fsl_i2c.c
```

```
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* MA 02111-1307 USA
*/
```

```
./drivers/i2c/mvtwsi.c
mvtwsi.c
```

```
/*
* Driver for the TWSI (i2c) controller found on the Marvell
* orion5x and kirkwood SoC families.
*
* Author: Albert Aribaud <albert.u.boot@aribaud.net>
* Copyright (c) 2010 Albert Aribaud.
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*/
```

```
./drivers/i2c/mv_i2c.c
mv_i2c.c
```

```
/*
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* Paolo Scaffardi, AIRVENT SAM s.p.a - RIMINI(ITALY), arsenio@tin.it
*
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* Marius Groeger <mgroeger@sysgo.de>
*
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* Robert Schwebel <r.schwebel@pengutronix.de>
*
* (C) Copyright 2011 Marvell Inc.
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* Lei Wen <leiwen@marvell.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
* Back ported to the 8xx platform (from the 8260 platform) by
* Murray.Jensen@cmst.csiro.au, 27-Jan-01.
*/
```

```
./drivers/i2c/mv_i2c.h
```

```
mv_i2c.h
```

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*/
```

```
./drivers/i2c/mxc_i2c.c
```

mx_c_i2c.c

```
/*
 * i2c driver for Freescale i.MX series
 *
 * (c) 2007 Pengutronix, Sascha Hauer <s.hauer@pengutronix.de>
 * (c) 2011 Marek Vasut <marek.vasut@gmail.com>
 *
 * Based on i2c-imx.c from linux kernel:
 * Copyright (C) 2005 Torsten Koschorrek <koschorrek at synertronixx.de>
 * Copyright (C) 2005 Matthias Blaschke <blaschke at synertronixx.de>
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 * MA 02111-1307 USA
 */
```

./drivers/i2c/mxs_i2c.c

mxs_i2c.c

```
/*
 * Freescale i.MX28 I2C Driver
 *
 * Copyright (C) 2011 Marek Vasut <marek.vasut@gmail.com>
 * on behalf of DENX Software Engineering GmbH
 *
 * Partly based on Linux kernel i2c-mxs.c driver:
 * Copyright (C) 2011 Wolfram Sang, Pengutronix e.K.
 *
 * Which was based on a (non-working) driver which was:
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*
*/
```

```
./drivers/i2c/omap1510_i2c.c
```

```
omap1510_i2c.c
```

```
/*
* Basic I2C functions
*
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*
* Author: Jian Zhang jzhang@ti.com, Texas Instruments
*
* Copyright (c) 2003 Wolfgang Denk, wd@denx.de
* Rewritten to fit into the current U-Boot framework
*
*/
```

```
./drivers/i2c/omap24xx_i2c.c
```

```
omap24xx_i2c.c
```

```
/*
* Basic I2C functions
*
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*
*/
```

```
* Author: Jian Zhang jzhang@ti.com, Texas Instruments
*
* Copyright (c) 2003 Wolfgang Denk, wd@denx.de
* Rewritten to fit into the current U-Boot framework
*
* Adapted for OMAP2420 I2C, r-woodruff2@ti.com
*
*/
```

```
./drivers/i2c/omap24xx_i2c.h
omap24xx_i2c.h
```

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 * MA 02111-1307 USA
 */
```

```
./drivers/i2c/pca9564_i2c.c
pca9564_i2c.c
```

```
/*
 * File:          drivers/i2c/pca9564.c
 * Based on:     drivers/i2c/s3c44b0_i2c.c
 * Author:
 *
 * Created:      2009-06-23
 * Description:  PCA9564 i2c bridge driver
 *
 * Modified:
 *              Copyright 2009 CJSC "NII STT", http://www.niistt.ru/
 *
 * Bugs:
 *
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*/
```

```
./drivers/i2c/ppc4xx_i2c.c
ppc4xx_i2c.c
```

```
/*
* (C) Copyright 2007-2009
* Stefan Roesse, DENX Software Engineering, sr@denx.de.
*
* based on work by Anne Sophie Harnois <anne-sophie.harnois@nextream.fr>
```

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* Bill Hunter, Wave 7 Optics, williamhunter@mediaone.net
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```

```
*/
```

```
./drivers/i2c/s3c24x0_i2c.c
s3c24x0_i2c.c
```

```
/*
* (C) Copyright 2002
* David Mueller, ELSOFT AG, d.mueller@elsoft.ch
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```

```
./drivers/i2c/s3c24x0_i2c.h
s3c24x0_i2c.h
```

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```

```
./drivers/i2c/s3c44b0_i2c.c
s3c44b0_i2c.c
```

```
/*
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* DAVE Srl
* http://www.dave-tech.it
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```

```
./drivers/i2c/sh_i2c.c
```

```
sh_i2c.c
```

```
/*
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*/
```

```
./drivers/i2c/sh_sh7734_i2c.c
```

```
sh_sh7734_i2c.c
```

```
/*
 * Copyright (C) 2012 Nobuhiro Iwamatsu <nobuhiro.iwamatsu.yj@renesas.com>
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```

```
./drivers/i2c/soft_i2c.c
soft_i2c.c
```

```
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*
* This has been changed substantially by Gerald Van Baren, Custom IDEAS,
* vanbaren@cideas.com. It was heavily influenced by LiMon, written by
* Neil Russell.
*/
```

```
./drivers/i2c/tegra_i2c.c
tegra_i2c.c
```

```
/*
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```

```
./drivers/i2c/tsi108_i2c.c
```

```
tsi108_i2c.c
```

```
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* Author: Alex Bounine
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```

```
./drivers/i2c/u8500_i2c.c
```

```
u8500_i2c.c
```

```
/*
```

```
* Copyright (C) ST-Ericsson SA 2010
*
* Basic U-Boot I2C interface for STn8500/DB8500
* Author: Michael Brandt <Michael.Brandt@stericsson.com> for ST-Ericsson
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*/
```

```
./drivers/i2c/u8500_i2c.h
u8500_i2c.h
```

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*/
```

```
./drivers/input/i8042.c
i8042.c
```

```
/*
* (C) Copyright 2002 ELTEC Elektronik AG
* Frank Gottschling <fgottschling@eltec.de>
*
*/
```

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*/
```

```
./drivers/input/input.c
```

```
input.c
```

```
/*
* Translate key codes into ASCII
*
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* (C) Copyright 2004 DENX Software Engineering, Wolfgang Denk, wd@denx.de
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*/
```

```
./drivers/input/keyboard.c
```

```
keyboard.c
```

```
/*
*****
*

```

```
* (C) Copyright 2004
* DENX Software Engineering
* Wolfgang Denk, wd@denx.de
*
* Keyboard driver
*
*****/
```

```
./drivers/input/key_matrix.c
key_matrix.c
```

```
/*
 * Manage Keyboard Matrices
 *
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 * MA 02111-1307 USA
 */
```

```
./drivers/input/pc_keyb.c
pc_keyb.c
```

```
/*
 *
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 * DENX Software Engineering
 * Wolfgang Denk, wd@denx.de
 *
 * PS/2 keyboard driver
 *
 * Originally from linux source (drivers/char/pc_keyb.c)
 *
*****/
```

```
./drivers/input/ps2mult.c
```

ps2mult.c

```
/*  
*  
* (C) Copyright 2004  
* DENX Software Engineering  
* Wolfgang Denk, wd@denx.de  
*  
* PS/2 multiplexer driver  
*  
* Originally from linux source (drivers/char/ps2mult.c)  
*  
* Uses simple serial driver (ps2ser.c) to access the multiplexer  
* Used by PS/2 keyboard driver (pc_keyb.c)  
*  
***/
```

./drivers/input/ps2ser.c

ps2ser.c

```
/*  
*  
* (C) Copyright 2004-2009  
* DENX Software Engineering  
* Wolfgang Denk, wd@denx.de  
*  
* Simple 16550A serial driver  
*  
* Originally from linux source (drivers/char/ps2ser.c)  
*  
* Used by the PS/2 multiplexer driver (ps2mult.c)  
*  
***/
```

./drivers/input/tegra-kbc.c

tegra-kbc.c

```
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* MA 02111-1307 USA
*/

./drivers/misc/ali512x.c
ali512x.c
/*
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* Daniel Engström, Omicron Ceti AB <daniel@omicron.se>.
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* MA 02111-1307 USA
*/

./drivers/misc/cbmem_console.c
cbmem_console.c
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* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston, MA, 02110-1301 USA
*/
```

```
./drivers/misc/ds4510.c
ds4510.c
```

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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./drivers/misc/gpio_led.c
gpio_led.c
```

```
/*
 * Status LED driver based on GPIO access conventions of Linux
 *
 * Copyright (C) 2010 Thomas Chou <thomas@wytron.com.tw>
 * Licensed under the GPL-2 or later.
 */
```

```
./drivers/misc/mc9sdz60.c
mc9sdz60.c
```

```
/*
 * (C) Copyright 2010 Stefano Babic <sbabic@denx.de>
 *
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```

```
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./drivers/misc/ns87308.c  
ns87308.c
```

```
/*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./drivers/misc/pdsp188x.c  
pdsp188x.c
```

```
/*  
* Copyright 2010 Sergey Poselenov, Emcraft Systems, <sposelenov@emcraft.com>  
* Copyright 2010 Ilya Yanok, Emcraft Systems, <yanok@emcraft.com>  
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* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,  
* MA 02110-1301, USA.  
*/
```

./drivers/misc/status_led.c

status_led.c

/*

* (C) Copyright 2000-2003

* Wolfgang Denk, DENX Software Engineering, wd@denx.de.

*

* See file CREDITS for list of people who contributed to this
* project.

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* MA 02111-1307 USA

*/

./drivers/misc/twl4030_led.c

twl4030_led.c

/*

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* Tom Rix <Tom.Rix at windriver.com>

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*

* twl4030_led_init is from cpu/omap3/common.c, power_init_r

*

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* Texas Instruments, <www.ti.com>

```
*
* Author :
*   Sunil Kumar <sunilsaini05 at gmail.com>
*   Shashi Ranjan <shashiranjnmc05 at gmail.com>
*
* Derived from Beagle Board and 3430 SDP code by
*   Richard Woodruff <r-woodruff2 at ti.com>
*   Syed Mohammed Khasim <khasim at ti.com>
*
*/

./drivers/mmc/altera_dw_mmc.c
altera_dw_mmc.c
/*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*
*/

./drivers/mmc/arm_pl180_mmci.c
arm_pl180_mmci.c
/*
* ARM PrimeCell MultiMedia Card Interface - PL180
*
* Copyright (C) ST-Ericsson SA 2010
*
* Author: Ulf Hansson <ulf.hansson@stericsson.com>
* Author: Martin Lundholm <martin.xa.lundholm@stericsson.com>
* Ported to drivers/mmc/ by: Matt Waddel <matt.waddel@linaro.org>
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* MA 02111-1307 USA
*/
```

```
./drivers/mmc/arm_pl180_mmci.h
arm_pl180_mmci.h
```

```
/*
 * ARM PrimeCell MultiMedia Card Interface - PL180
 *
 * Copyright (C) ST-Ericsson SA 2010
 *
 * Author: Ulf Hansson <ulf.hansson@stericsson.com>
 * Author: Martin Lundholm <martin.xa.lundholm@stericsson.com>
 * Ported to drivers/mmc/ by: Matt Waddel <matt.waddel@linaro.org>
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./drivers/mmc/bfin_sdh.c
bfin_sdh.c
```

```
/*
 * Driver for Blackfin on-chip SDH controller
 *
 * Copyright (c) 2008-2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
*/
```

```
./drivers/mmc/davinci_mmc.c
davinci_mmc.c
```

```
/*
 * Davinci MMC Controller Driver
```

```
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./drivers/mmc/dw_mmc.c
```

```
dw_mmc.c
```

```
/*
* Copyright (C) 2013 Altera Corporation <www.altera.com>
*
* (C) Copyright 2012 SAMSUNG Electronics
* Jaehoon Chung <jh80.chung@samsung.com>
* Rajeshwari Shinde <rajeshwari.s@samsung.com>
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*
*/
```

```
./drivers/mmc/exynos_dw_mmc.c
```

```
exynos_dw_mmc.c
```

```
/*
* (C) Copyright 2012 SAMSUNG Electronics
* Jaehoon Chung <jh80.chung@samsung.com>
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*/
```

```
./drivers/mmc/fsl_esdhc.c
```

```
fsl_esdhc.c
```

```
/*
* Copyright 2007, 2010-2011 Freescale Semiconductor, Inc
* Andy Fleming
*
* Based vaguely on the pxa mmc code:
* (C) Copyright 2003
* Kyle Harris, Nexus Technologies, Inc. kharris@nexus-tech.net
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*/
```

```
./drivers/mmc/ftsd010_esdhc.c
```

```
ftsd010_esdhc.c
```

```
/*
* Copyright (C) 2011 Andes Technology Corporation
* Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
*
*/
```

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* MA 02111-1307 USA
*/
```

```
./drivers/mmc/gen_atmel_mci.c
```

```
gen_atmel_mci.c
```

```
/*
* Copyright (C) 2010
* Rob Emanuele <rob@emanuele.us>
* Reinhard Meyer, EMK Elektronik <reinhard.meyer@emk-elektronik.de>
*
* Original Driver:
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/mmc/mmc.c
```

```
mmc.c
```

```

/*
 * Copyright 2008, Freescale Semiconductor, Inc
 * Andy Fleming
 *
 * Based vaguely on the Linux code
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */

./drivers/mmc/mmc_spi.c
mmc_spi.c
/*
 * generic mmc spi driver
 *
 * Copyright (C) 2010 Thomas Chou <thomas@wytron.com.tw>
 * Licensed under the GPL-2 or later.
 */

./drivers/mmc/mv_sdhci.c
mv_sdhci.c
No license information found.

./drivers/mmc/mxcmmc.c
mxcmmc.c
/*
 * This is a driver for the SDHC controller found in Freescale MX2/MX3
 * SoCs. It is basically the same hardware as found on MX1 (imxmmc.c).
 * Unlike the hardware found on MX1, this hardware just works and does
 * not need all the quirks found in imxmmc.c, hence the seperate driver.
 *
 * Copyright (C) 2009 Ilya Yanok, <yanok@emcraft.com>
 * Copyright (C) 2008 Sascha Hauer, Pengutronix <s.hauer@pengutronix.de>
 * Copyright (C) 2006 Pavel Pisa, PiKRON <ppisa@pikron.com>
 */

```

```
* derived from pxamci.c by Russell King
*
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* it under the terms of the GNU General Public License version 2 as
* published by the Free Software Foundation.
*
*/
```

```
./drivers/mmc/mxsmmc.c
```

```
mxsmmc.c
```

```
/*
 * Freescale i.MX28 SSP MMC driver
 *
 * Copyright (C) 2011 Marek Vasut <marek.vasut@gmail.com>
 * on behalf of DENX Software Engineering GmbH
 *
 * Based on code from LTIB:
 * (C) Copyright 2008-2010 Freescale Semiconductor, Inc.
 * Terry Lv
 *
 * Copyright 2007, Freescale Semiconductor, Inc
 * Andy Fleming
 *
 * Based vaguely on the pxa mmc code:
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 * Kyle Harris, Nexus Technologies, Inc. kharris@nexus-tech.net
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 * MA 02111-1307 USA
 */
```

```
./drivers/mmc/omap_hsmmc.c
```

```
omap_hsmmc.c
```

```
/*
 * (C) Copyright 2008
```

```
* Texas Instruments, <www.ti.com>
* Sukumar Ghorai <s-ghorai@ti.com>
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* MA 02111-1307 USA
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```

```
./drivers/mmc/pxa_mmc.h
```

```
pxa_mmc.h
```

```
/*
 * linux/drivers/mmc/mmc_pxa.h
 *
 * Author: Vladimir Shebordaev, Igor Oblakov
 * Copyright: MontaVista Software Inc.
 *
 * $Id: mmc_pxa.h,v 0.3.1.6 2002/09/25 19:25:48 ted Exp ted $
 *
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 * it under the terms of the GNU General Public License version 2 as
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 */
```

```
./drivers/mmc/pxa_mmc_gen.c
```

```
pxa_mmc_gen.c
```

```
/*
 * Copyright (C) 2010 Marek Vasut <marek.vasut@gmail.com>
 *
 * Loosely based on the old code and Linux's PXA MMC driver
 *
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* MA 02111-1307 USA
*/
```

```
./drivers/mmc/s5p_sdhci.c
```

```
s5p_sdhci.c
```

```
/*
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* Jaehoon Chung <jh80.chung@samsung.com>
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*/
```

```
./drivers/mmc/sdhci.c
```

```
sdhci.c
```

```
/*
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* Lei Wen <leiw@marvell.com>
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* MA 02111-1307 USA
*
* Back ported to the 8xx platform (from the 8260 platform) by
* Murray.Jensen@cmst.csiro.au, 27-Jan-01.
*/
```

```
./drivers/mmc/sh_mmcif.c
sh_mmcif.c
```

```
/*
 * MMCIF driver.
 *
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 *
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 */
```

```
./drivers/mmc/sh_mmcif.h
sh_mmcif.h
```

```
/*
 * MMCIF driver.
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 *
 */
```

```
./drivers/mmc/spl_mmc.c
spl_mmc.c
```

```
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 *
 * Aneesh V <aneesh@ti.com>
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* MA 02111-1307 USA  
*/
```

```
./drivers/mmc/tegra_mmc.c
```

```
tegra_mmc.c
```

```
/*  
* (C) Copyright 2009 SAMSUNG Electronics  
* Minkyu Kang <mk7.kang@samsung.com>  
* Jaehoon Chung <jh80.chung@samsung.com>  
* Portions Copyright 2011-2012 NVIDIA Corporation  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA  
*/
```

```
./drivers/mtd/at45.c
```

```
at45.c
```

```
/* Driver for ATMEL DataFlash support  
* Author : Hamid Ikdoumi (Atmel)  
*  
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*  
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```

```
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* MA 02111-1307 USA
*
*/
```

```
./drivers/mtd/cfi_flash.c
cfi_flash.c
```

```
/*
* (C) Copyright 2002-2004
* Brad Kemp, Seranoa Networks, Brad.Kemp@seranoa.com
*
* Copyright (C) 2003 Arabella Software Ltd.
* Yuli Barcohen <yuli@arabellasw.com>
*
* Copyright (C) 2004
* Ed Okerson
*
* Copyright (C) 2006
* Tolunay Orkun <listmember@orkun.us>
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
```

```
./drivers/mtd/cfi_mtd.c
cfi_mtd.c
```

```
/*
* (C) Copyright 2008 Semihalf
*
* Written by: Piotr Ziecik <kosmo@semihalf.com>
*
* See file CREDITS for list of people who contributed to this
* project.
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
```

```
./drivers/mtd/dataflash.c
```

```
dataflash.c
```

```
/* LowLevel function for ATMEL DataFlash support
```

```
* Author : Hamid Ikdoumi (Atmel)
```

```
*
```

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```

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```

```
*
```

```
*/
```

```
./drivers/mtd/ftsmc020.c
```

```
ftsmc020.c
```

```
/*
```

```
* (C) Copyright 2009 Faraday Technology
```

```
* Po-Yu Chuang <ratbert@faraday-tech.com>
```

```
*
```

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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./drivers/mtd/jedec_flash.c
```

```
jedec_flash.c
```

```
/*
* (C) Copyright 2007
* Michael Schwingen, <michael@schwingen.org>
*
* based in great part on jedec_probe.c from linux kernel:
* (C) 2000 Red Hat. GPL'd.
* Occasionally maintained by Thayne Harbaugh tharbaugh at lnx dot com
*
* See file CREDITS for list of people who contributed to this
* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/mtd/mtdconcat.c
```

```
mtdconcat.c
```

```
/*
* MTD device concatenation layer
*
* (C) 2002 Robert Kaiser <rkaiser@sysgo.de>
*
* NAND support by Christian Gan <cgan@iders.ca>
*/
```

```

* This code is GPL
*/

./drivers/mtd/mtdcore.c
mtdcore.c
/*
* Core registration and callback routines for MTD
* drivers and users.
*
* This program is free software; you can redistribute it and/or modify
* it under the terms of the GNU General Public License version 2 as
* published by the Free Software Foundation.
*/

./drivers/mtd/mtdpart.c
mtdpart.c
/*
* Simple MTD partitioning layer
*
* (C) 2000 Nicolas Pitre <nico@cam.org>
*
* This code is GPL
*
*      02-21-2002      Thomas Gleixner <gleixner@autronix.de>
*                      added support for read_oob, write_oob
*/

./drivers/mtd/mw_eeprom.c
mw_eeprom.c
/* Three-wire (MicroWire) serial eeprom driver (for 93C46 and compatibles) */
/*
* Serial EEPROM opcodes, including start bit
*/

./drivers/mtd/nand/am335x_spl_bch.c
am335x_spl_bch.c
/*
* (C) Copyright 2012
* Konstantin Kozhevnikov, Cogent Embedded
*
* based on nand_spl_simple code
*
* (C) Copyright 2006-2008
* Stefan Roese, DENX Software Engineering, sr@denx.de.
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* Foundation, Inc.  
*/
```

```
./drivers/mtd/nand/atmel_nand.c
```

```
atmel_nand.c
```

```
/*  
* (C) Copyright 2007-2008  
* Stelian Pop <stelian@popies.net>  
* Lead Tech Design <www.leadtechdesign.com>  
*  
* (C) Copyright 2006 ATMEL Rousset, Lacrosonniere Nicolas  
*  
* Add Programmable Multibit ECC support for various AT91 SoC  
* (C) Copyright 2012 ATMEL, Hong Xu  
*  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./drivers/mtd/nand/atmel_nand_ecc.h
```

```
atmel_nand_ecc.h
```

```
/*  
* Error Corrected Code Controller (ECC) - System peripherals registers.  
* Based on AT91SAM9260 datasheet revision B.  
*  
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* under the terms of the GNU General Public License as published by the  
* Free Software Foundation; either version 2 of the License, or (at your
```

```

* option) any later version.
*/

./drivers/mtd/nand/bfin_nand.c
bfin_nand.c
/*
 * Driver for Blackfin on-chip NAND controller.
 *
 * Enter bugs at http://blackfin.uclinux.org/
 *
 * Copyright (c) 2007-2008 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */
/* TODO:
 * - move bit defines into mach-common/bits/nand.h
 * - try and replace all IRQSTAT usage with STAT polling
 * - have software ecc mode use same algo as hw ecc ?
 */

./drivers/mtd/nand/davinci_nand.c
davinci_nand.c
/*
 * NAND driver for TI DaVinci based boards.
 *
 * Copyright (C) 2007 Sergey Kubushyn <ksi@koi8.net>
 *
 * Based on Linux DaVinci NAND driver by TI. Original copyright follows:
 */
/*
 *
 * linux/drivers/mtd/nand/nand_davinci.c
 *
 * NAND Flash Driver
 *
 * Copyright (C) 2006 Texas Instruments.
 *
 * -----
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```

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* -----
*
* Overview:
* This is a device driver for the NAND flash device found on the
* DaVinci board which utilizes the Samsung k9k2g08 part.
*

./drivers/mtd/nand/denali_nand.c
denali_nand.c

```
/*  
 * NAND Flash Controller Device Driver  
 * Copyright (C) 2013 Altera Corporation <www.altera.com>  
 * Copyright © 2009-2010, Intel Corporation and its suppliers.  
 *  
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 * this program; if not, write to the Free Software Foundation, Inc.,  
 * 51 Franklin St - Fifth Floor, Boston, MA 02110-1301 USA.  
 *  
 */
```

./drivers/mtd/nand/denali_nand.h
denali_nand.h

```
/*  
 * NAND Flash Controller Device Driver  
 * Copyright (C) 2013 Altera Corporation <www.altera.com>  
 * Copyright (c) 2009 - 2010, Intel Corporation and its suppliers.  
 *  
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 * this program; if not, write to the Free Software Foundation, Inc.,  
 * 51 Franklin St - Fifth Floor, Boston, MA 02110-1301 USA.
```

```

*
*/

./drivers/mtd/nand/diskonchip.c
diskonchip.c
/*
* drivers/mtd/nand/diskonchip.c
*
* (C) 2003 Red Hat, Inc.
* (C) 2004 Dan Brown <dan_brown@ieee.org>
* (C) 2004 Kalev Lember <kalev@smartlink.ee>
*
* Author: David Woodhouse <dwmw2@infradead.org>
* Additional Diskonchip 2000 and Millennium support by Dan Brown
<dan_brown@ieee.org>
* Diskonchip Millennium Plus support by Kalev Lember <kalev@smartlink.ee>
*
* Error correction code lifted from the old docecc code
* Author: Fabrice Bellard (fabrice.bellard@netgem.com)
* Copyright (C) 2000 Netgem S.A.
* converted to the generic Reed-Solomon library by Thomas Gleixner
<tglx@linutronix.de>
*
* Interface to generic NAND code for M-Systems DiskOnChip devices
*/

./drivers/mtd/nand/fsl_elbc_nand.c
fsl_elbc_nand.c
/* Freescale Enhanced Local Bus Controller FCM NAND driver
*
* Copyright (c) 2006-2008 Freescale Semiconductor
*
* Authors: Nick Spence <nick.spence@freescale.com>,
*          Scott Wood <scottwood@freescale.com>
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*/

```

```
./drivers/mtd/nand/fsl_elbc_spl.c
fsl_elbc_spl.c
/*
 * NAND boot for Freescale Enhanced Local Bus Controller, Flash Control Machine
 *
 * (C) Copyright 2006-2008
 * Stefan Roesse, DENX Software Engineering, sr@denx.de.
 *
 * Copyright (c) 2008 Freescale Semiconductor, Inc.
 * Author: Scott Wood <scottwood@freescale.com>
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./drivers/mtd/nand/fsl_ifc_nand.c
fsl_ifc_nand.c
/* Integrated Flash Controller NAND Machine Driver
 *
 * Copyright (c) 2012 Freescale Semiconductor, Inc
 *
 * Authors: Dipen Dudhat <Dipen.Dudhat@freescale.com>
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
 */
```

```
./drivers/mtd/nand/fsl_upm.c
```

```
fsl_upm.c
```

```
/*  
 * FSL UPM NAND driver  
 *  
 * Copyright (C) 2007 MontaVista Software, Inc.  
 * Anton Vorontsov <avorontsov@ru.mvista.com>  
 *  
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 * published by the Free Software Foundation; either version 2 of  
 * the License, or (at your option) any later version.  
 */
```

```
./drivers/mtd/nand/fsmc_nand.c
```

```
fsmc_nand.c
```

```
/*  
 * (C) Copyright 2010  
 * Vipin Kumar, ST Microelectronics, vipin.kumar@st.com.  
 *  
 * (C) Copyright 2012  
 * Amit Virdi, ST Microelectronics, amit.virdi@st.com.  
 *  
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 * along with this program; if not, write to the Free Software  
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
 * MA 02111-1307 USA  
 */
```

```
./drivers/mtd/nand/jz4740_nand.c
```

```
jz4740_nand.c
```

```
/*  
 * Platform independend driver for JZ4740.  
 *  
 * Copyright (c) 2007 Ingenic Semiconductor Inc.  
 * Author: <jlwei@ingenic.cn>  
 */
```

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* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2 of
* the License, or (at your option) any later version.
*/
```

```
./drivers/mtd/nand/kb9202_nand.c
kb9202_nand.c
```

```
/*
 * (C) Copyright 2006
 * KwikByte <kb9200_dev@kwikbyte.com>
 *
 * (C) Copyright 2009
 * Matthias Kaehlcke <matthias@kaehlcke.net>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./drivers/mtd/nand/kirkwood_nand.c
kirkwood_nand.c
```

```
/*
 * (C) Copyright 2009
 * Marvell Semiconductor <www.marvell.com>
 * Written-by: Prafulla Wadaskar <prafulla@marvell.com>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/mtd/nand/kmeter1_nand.c
```

```
kmeter1_nand.c
```

```
/*
* (C) Copyright 2009
* Heiko Schocher, DENX Software Engineering, hs@denx.de
*
* See file CREDITS for list of people who contributed to this
* project.
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/mtd/nand/mpc5121_nfc.c
```

```
mpc5121_nfc.c
```

```
/*
* Copyright 2004-2008 Freescale Semiconductor, Inc.
* Copyright 2009 Semihalf.
* (C) Copyright 2009 Stefan Roese <sr@denx.de>
*
* Based on original driver from Freescale Semiconductor
* written by John Rigby <jrigby@freescale.com> on basis
* of drivers/mtd/nand/mxc_nand.c. Reworked and extended
* Piotr Ziecik <kosmo@semihalf.com>.
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```

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* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
* MA 02110-1301, USA.
*/
```

```
./drivers/mtd/nand/mxc_nand.c
mxc_nand.c
```

```
/*
 * Copyright 2004-2007 Freescale Semiconductor, Inc.
 * Copyright 2008 Sascha Hauer, kernel@pengutronix.de
 * Copyright 2009 Ilya Yanok, <yanok@emcraft.com>
 *
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 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
 * MA 02110-1301, USA.
*/
```

```
./drivers/mtd/nand/mxs_nand.c
mxs_nand.c
```

```
/*
 * Freescale i.MX28 NAND flash driver
 *
 * Copyright (C) 2011 Marek Vasut <marek.vasut@gmail.com>
 * on behalf of DENX Software Engineering GmbH
 *
 * Based on code from LTIB:
 * Freescale GPMI NFC NAND Flash Driver
 *
 * Copyright (C) 2010 Freescale Semiconductor, Inc.
 * Copyright (C) 2008 Embedded Alley Solutions, Inc.
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* 51 Franklin Street, Fifth Floor, Boston, MA 02110-1301 USA.
*/
```

```
./drivers/mtd/nand/nand.c
```

```
nand.c
```

```
/*
```

```
* (C) Copyright 2005
* 2N Telekomunikace, a.s. <www.2n.cz>
* Ladislav Michl <michl@2n.cz>
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/mtd/nand/nand_base.c
```

```
nand_base.c
```

```
/*
```

```
* drivers/mtd/nand.c
*
* Overview:
* This is the generic MTD driver for NAND flash devices. It should be
* capable of working with almost all NAND chips currently available.
* Basic support for AG-AND chips is provided.
*
* Additional technical information is available on
* http://www.linux-mtd.infradead.org/doc/nand.html
```

```
*
* Copyright (C) 2000 Steven J. Hill (sjhill@realitydiluted.com)
*           2002-2006 Thomas Gleixner (tglx@linutronix.de)
*
* Credits:
*   David Woodhouse for adding multichip support
*
*   Aleph One Ltd. and Toby Churchill Ltd. for supporting the
*   rework for 2K page size chips
*
* TODO:
*   Enable cached programming for 2k page size chips
*   Check, if mtd->ecctype should be set to MTD_ECC_HW
*   if we have HW ecc support.
*   The AG-AND chips have nice features for speed improvement,
*   which are not supported yet. Read / program 4 pages in one go.
*   BBT table is not serialized, has to be fixed
*
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* published by the Free Software Foundation.
*
*/
```

```
./drivers/mtd/nand/nand_bbt.c
nand_bbt.c
```

```
/*
* drivers/mtd/nand_bbt.c
*
* Overview:
*   Bad block table support for the NAND driver
*
* Copyright (C) 2004 Thomas Gleixner (tglx@linutronix.de)
*
* This program is free software; you can redistribute it and/or modify
* it under the terms of the GNU General Public License version 2 as
* published by the Free Software Foundation.
*
* Description:
*
* When nand_scan_bbt is called, then it tries to find the bad block table
* depending on the options in the BBT descriptor(s). If no flash based BBT
* (NAND_USE_FLASH_BBT) is specified then the device is scanned for factory
* marked good / bad blocks. This information is used to create a memory BBT.
* Once a new bad block is discovered then the "factory" information is updated
* on the device.
* If a flash based BBT is specified then the function first tries to find the
* BBT on flash. If a BBT is found then the contents are read and the memory
* based BBT is created. If a mirrored BBT is selected then the mirror is
* searched too and the versions are compared. If the mirror has a greater
```

```
* version number than the mirror BBT is used to build the memory based BBT.
* If the tables are not versioned, then we "or" the bad block information.
* If one of the BBTs is out of date or does not exist it is (re)created.
* If no BBT exists at all then the device is scanned for factory marked
* good / bad blocks and the bad block tables are created.
*
* For manufacturer created BBTs like the one found on M-SYS DOC devices
* the BBT is searched and read but never created
*
* The auto generated bad block table is located in the last good blocks
* of the device. The table is mirrored, so it can be updated eventually.
* The table is marked in the OOB area with an ident pattern and a version
* number which indicates which of both tables is more up to date. If the NAND
* controller needs the complete OOB area for the ECC information then the
* option NAND_USE_FLASH_BBT_NO_OOB should be used: it moves the ident pattern
* and the version byte into the data area and the OOB area will remain
* untouched.
*
* The table uses 2 bits per block
* 11b:          block is good
* 00b:          block is factory marked bad
* 01b, 10b:    block is marked bad due to wear
*
* The memory bad block table uses the following scheme:
* 00b:          block is good
* 01b:          block is marked bad due to wear
* 10b:          block is reserved (to protect the bbt area)
* 11b:          block is factory marked bad
*
* Multichip devices like DOC store the bad block info per floor.
*
* Following assumptions are made:
* - bbt's start at a page boundary, if autolocated on a block boundary
* - the space necessary for a bbt in FLASH does not exceed a block boundary
*
*/
```

```
./drivers/mtd/nand/nand_bch.c
```

```
nand_bch.c
```

```
/*
* This file provides ECC correction for more than 1 bit per block of data,
* using binary BCH codes. It relies on the generic BCH library lib/bch.c.
*
* Copyright © 2011 Ivan Djelic <ivan.djelic@parrot.com>
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* later version.
*
*/
```

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* 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA.
*/
```

```
./drivers/mtd/nand/nand_ecc.c
```

```
nand_ecc.c
```

```
/*
```

```
* This file contains an ECC algorithm from Toshiba that detects and
* corrects 1 bit errors in a 256 byte block of data.
```

```
*
```

```
* drivers/mtd/nand/nand_ecc.c
```

```
*
```

```
* Copyright (C) 2000-2004 Steven J. Hill (sjhill@realitydiluted.com)
*                               Toshiba America Electronics Components, Inc.
```

```
*
```

```
* Copyright (C) 2006 Thomas Gleixner <tglx@linutronix.de>
```

```
*
```

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*
```

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```

```
*
```

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```

```
*/
```

```
./drivers/mtd/nand/nand_ids.c
```

nand_ids.c

```
/*
 * drivers/mtd/nandids.c
 *
 * Copyright (C) 2002 Thomas Gleixner (tglx@linutronix.de)
 *
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 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 */
```

./drivers/mtd/nand/nand_plat.c

nand_plat.c

```
/*
 * Genericish driver for memory mapped NAND devices
 *
 * Copyright (c) 2006-2009 Analog Devices Inc.
 * Licensed under the GPL-2 or later.
 */
/* Your board must implement the following macros:
 * NAND_PLAT_WRITE_CMD(chip, cmd)
 * NAND_PLAT_WRITE_ADR(chip, cmd)
 * NAND_PLAT_INIT()
 *
 * It may also implement the following:
 * NAND_PLAT_DEV_READY(chip)
 */
```

./drivers/mtd/nand/nand_spl_load.c

nand_spl_load.c

```
/*
 * Copyright (C) 2011
 * Heiko Schocher, DENX Software Engineering, hs@denx.de.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./drivers/mtd/nand/nand_spl_simple.c
nand_spl_simple.c
```

```
/*
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 * Stefan Roesse, DENX Software Engineering, sr@denx.de.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./drivers/mtd/nand/nand_util.c
nand_util.c
```

```
/*
 * drivers/mtd/nand/nand_util.c
 *
 * Copyright (C) 2006 by Weiss-Electronic GmbH.
 * All rights reserved.
 *
 * @author: Guido Classen <clagix@gmail.com>
 * @descr: NAND Flash support
 * @references: borrowed heavily from Linux mtd-utils code:
 * flash_eraseall.c by Arcom Control System Ltd
 * nandwrite.c by Steven J. Hill (sjhill@realitydiluted.com)
 * and Thomas Gleixner (tglx@linutronix.de)
 *
 * Copyright (C) 2008 Nokia Corporation: drop_ffs() function by
 * Artem Bityutskiy <dedekind1@gmail.com> from mtd-utils
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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```

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* License, or (at your option) any later version.
*/
```

```
./drivers/mtd/nand/ndfc.c
```

```
ndfc.c
```

```
/*
```

```
* Overview:
```

```
* Platform independent driver for NDFC (NanD Flash Controller)
* integrated into IBM/AMCC PPC4xx cores
*
```

```
* (C) Copyright 2006-2009
```

```
* Stefan Roes, DENX Software Engineering, sr@denx.de.
```

```
* Based on original work by
```

```
* Thomas Gleixner
```

```
* Copyright 2006 IBM
*
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* See file CREDITS for list of people who contributed to this
* project.
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* MA 02111-1307 USA
```

```
*/
```

```
./drivers/mtd/nand/nomadik.c
```

nomadik.c

```
/*
 * (C) Copyright 2007 STMicroelectronics, <www.st.com>
 * (C) Copyright 2009 Alessandro Rubini <rubini@unipv.it>
 *
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 * project.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./drivers/mtd/nand/omap_gpmmc.c

omap_gpmmc.c

```
/*
 * (C) Copyright 2004-2008 Texas Instruments, <www.ti.com>
 * Rohit Choraria <rohitkc@ti.com>
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 * project.
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./drivers/mtd/nand/s3c2410_nand.c

s3c2410_nand.c

```
/*
 * (C) Copyright 2006 OpenMoko, Inc.
 * Author: Harald Welte <laforge@openmoko.org>
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./drivers/mtd/nand/s3c64xx.c

s3c64xx.c

```
/*
 * (C) Copyright 2006 DENX Software Engineering
 *
 * Implementation for U-Boot 1.1.6 by Samsung
 *
 * (C) Copyright 2008
 * Guennadi Liakhovetki, DENX Software Engineering, <lg@denx.de>
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./drivers/mtd/nand/tegra_nand.c

tegra_nand.c

```
/*
 * Copyright (c) 2011 The Chromium OS Authors.
 * (C) Copyright 2011 NVIDIA Corporation <www.nvidia.com>
 * (C) Copyright 2006 Detlev Zundel, dzu@denx.de
 * (C) Copyright 2006 DENX Software Engineering
 *
 * See file CREDITS for list of people who contributed to this
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./drivers/mtd/nand/tegra_nand.h

tegra_nand.h

```
/*
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 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./drivers/mtd/onenand/onenand_base.c
onenand_base.c
```

```
/*
 * linux/drivers/mtd/onenand/onenand_base.c
 *
 * Copyright (C) 2005-2007 Samsung Electronics
 * Kyungmin Park <kyungmin.park@samsung.com>
 *
 * Credits:
 *   Adrian Hunter <ext-adrian.hunter@nokia.com>:
 *   auto-placement support, read-while load support, various fixes
 *   Copyright (C) Nokia Corporation, 2007
 *
 *   Rohit Hagargundgi <h.rohit at samsung.com>,
 *   Amul Kumar Saha <amul.saha@samsung.com>:
 *   Flex-OneNAND support
 *   Copyright (C) Samsung Electronics, 2009
 *
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 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 */
```

```
./drivers/mtd/onenand/onenand_bbt.c
onenand_bbt.c
```

```
/*
 * linux/drivers/mtd/onenand/onenand_bbt.c
 *
 * Bad Block Table support for the OneNAND driver
 *
 * Copyright(c) 2005-2008 Samsung Electronics
 * Kyungmin Park <kyungmin.park@samsung.com>
 *
 * TODO:
 *   Split BBT core and chip specific BBT.
 *
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 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 */
```

```
./drivers/mtd/onenand/onenand_spl.c
onenand_spl.c
```

```
/*
 * Copyright (C) 2011 Marek Vasut <marek.vasut@gmail.com>
 *
 * Based on code:
 *   Copyright (C) 2005-2009 Samsung Electronics
 *   Kyungmin Park <kyungmin.park@samsung.com>
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/mtd/onenand/onenand_uboot.c
onenand_uboot.c
```

```
/*
 * drivers/mtd/onenand/onenand_uboot.c
 *
 * Copyright (C) 2005-2008 Samsung Electronics
 * Kyungmin Park <kyungmin.park@samsung.com>
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 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 */
/*
 * OneNAND initialization at U-Boot
 */
```

```
./drivers/mtd/onenand/samsung.c
samsung.c
```

```
/*
 * S3C64XX/S5PC100 OneNAND driver at U-Boot
 *
 * Copyright (C) 2008-2009 Samsung Electronics
 * Kyungmin Park <kyungmin.park@samsung.com>
 *
 * Implementation:
 *     Emulate the pseudo BufferRAM
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 */
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/mtd/spi/atmel.c
```

```
atmel.c
```

```
/*
* Atmel SPI DataFlash support
*
* Copyright (C) 2008 Atmel Corporation
* Licensed under the GPL-2 or later.
*/
```

```
./drivers/mtd/spi/eeprom_m95xxx.c
```

```
eeprom_m95xxx.c
```

```
/*
* Copyright (C) 2009
* Albin Tonnerre, Free Electrons <albin.tonnerre@free-electrons.com>
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

./drivers/mtd/spi/eon.c

eon.c

```
/*
 * (C) Copyright 2010, ucRobotics Inc.
 * Author: Chong Huang <chuang@ucrobotics.com>
 * Licensed under the GPL-2 or later.
 */
```

./drivers/mtd/spi/macronix.c

macronix.c

```
/*
 * Copyright 2009(C) Marvell International Ltd. and its affiliates
 * Prafulla Wadaskar <prafulla@marvell.com>
 *
 * Based on drivers/mtd/spi/stmicro.c
 *
 * Copyright 2008, Network Appliance Inc.
 * Jason McMullan <mcmullan@netapp.com>
 *
 * Copyright (C) 2004-2007 Freescale Semiconductor, Inc.
 * TsiChung Liew (Tsi-Chung.Liew@freescale.com)
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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 * MA 02110-1301 USA
 */
```

./drivers/mtd/spi/ramtron.c

ramtron.c

```
/*
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 * Reinhard Meyer, EMK Elektronik, reinhard.meyer@emk-elektronik.de
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 */
```

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*/
```

```
./drivers/mtd/spi/spansion.c
```

```
spansion.c
```

```
/*
```

```
* Copyright (C) 2009 Freescale Semiconductor, Inc.
```

```
*
```

```
* Author: Mingkai Hu (Mingkai.hu@freescale.com)
```

```
* Based on stmicro.c by Wolfgang Denk (wd@denx.de),
```

```
* TsiChung Liew (Tsi-Chung.Liew@freescale.com),
```

```
* and Jason McMullan (mcmullan@netapp.com)
```

```
*
```

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* project.
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*
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```

```
*/
```

```
./drivers/mtd/spi/spi_flash.c
```

```
spi_flash.c
```

```
/*
```

```
* SPI flash interface
```

```
*
```

```
* Copyright (C) 2008 Atmel Corporation
* Copyright (C) 2010 Reinhard Meyer, EMK Elektronik
*
* Licensed under the GPL-2 or later.
*/

./drivers/mtd/spi/spi_flash_internal.h
spi_flash_internal.h
/*
* SPI flash internal definitions
*
* Copyright (C) 2008 Atmel Corporation
*/
/* Common parameters -- kind of high, but they should only occur when there
* is a problem (and well your system already is broken), so err on the side
* of caution in case we're dealing with slower SPI buses and/or processors.
*/

./drivers/mtd/spi/spi_spl_load.c
spi_spl_load.c
/*
* Copyright (C) 2011 OMICRON electronics GmbH
*
* based on drivers/mtd/nand/nand_spl_load.c
*
* Copyright (C) 2011
* Heiko Schocher, DENX Software Engineering, hs@denx.de.
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*/

./drivers/mtd/spi/sst.c
sst.c
/*
* Driver for SST serial flashes
*
* (C) Copyright 2000-2002
```

```
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
* Copyright 2008, Network Appliance Inc.  
* Jason McMullan <mcmullan@netapp.com>  
* Copyright (C) 2004-2007 Freescale Semiconductor, Inc.  
* TsiChung Liew (Tsi-Chung.Liew@freescale.com)  
* Copyright (c) 2008-2009 Analog Devices Inc.  
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*/
```

```
./drivers/mtd/spi/stmicro.c  
stmicro.c
```

```
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*/
```

```
./drivers/mtd/spi/winbond.c  
winbond.c
```

```
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*/
```

```
./drivers/mtd/st_smi.c
```

st_smi.c

```
/*
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 * Vipin Kumar, ST Microelectronics, vipin.kumar@st.com.
 *
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 */
```

./drivers/mtd/ubi/build.c

build.c

```
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 *
 * Author: Artem Bityutskiy (Битюцкий Артём),
 *         Frank Haverkamp
 */
```

./drivers/mtd/ubi/crc32.c

crc32.c

```

/*
 * Oct 15, 2000 Matt Domsch <Matt_Domsch@dell.com>
 * Nicer crc32 functions/docs submitted by linux@horizon.com. Thanks!
 * Code was from the public domain, copyright abandoned. Code was
 * subsequently included in the kernel, thus was re-licensed under the
 * GNU GPL v2.
 *
 * Oct 12, 2000 Matt Domsch <Matt_Domsch@dell.com>
 * Same crc32 function was used in 5 other places in the kernel.
 * I made one version, and deleted the others.
 * There are various incantations of crc32(). Some use a seed of 0 or ~0.
 * Some xor at the end with ~0. The generic crc32() function takes
 * seed as an argument, and doesn't xor at the end. Then individual
 * users can do whatever they need.
 * drivers/net/smc9194.c uses seed ~0, doesn't xor with ~0.
 * fs/jffs2 uses seed 0, doesn't xor with ~0.
 * fs/partitions/efi.c uses seed ~0, xor's with ~0.
 *
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 * Version 2. See the file COPYING for more details.
 */

./drivers/mtd/ubi/crc32defs.h
crc32defs.h
/*
 * There are multiple 16-bit CRC polynomials in common use, but this is
 * *the* standard CRC-32 polynomial, first popularized by Ethernet.
 *  $x^{32}+x^{26}+x^{23}+x^{22}+x^{16}+x^{12}+x^{11}+x^{10}+x^8+x^7+x^5+x^4+x^2+x^1+x^0$ 
 */
/* How many bits at a time to use. Requires a table of 4<<CRC_xx_BITS bytes. */
/* For less performance-sensitive, use 4 */

./drivers/mtd/ubi/crc32table.h
crc32table.h
/* this file is generated - do not edit */

./drivers/mtd/ubi/debug.c
debug.c
/*
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* Author: Artem Bityutskiy (Битюцкий Артём)
*/
```

```
./drivers/mtd/ubi/debug.h
```

```
debug.h
```

```
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*
* Author: Artem Bityutskiy (Битюцкий Артём)
*/
```

```
./drivers/mtd/ubi/eba.c
```

```
eba.c
```

```
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*
* Author: Artem Bityutskiy (Битюцкий Артём)
```

*/

./drivers/mtd/ubi/io.c

io.c

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*/

./drivers/mtd/ubi/kapi.c

kapi.c

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*/

./drivers/mtd/ubi/misc.c

misc.c

/*

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```

./drivers/mtd/ubi/scan.c

scan.c

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```

./drivers/mtd/ubi/scan.h

scan.h

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*
* Author: Artem Bityutskiy (Битюцкий Артём)
*/
```

```
./drivers/mtd/ubi/ubi-media.h
```

```
ubi-media.h
```

```
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*
* Authors: Artem Bityutskiy (Битюцкий Артём)
*          Thomas Gleixner
*          Frank Haverkamp
*          Oliver Lohmann
*          Andreas Arnez
*/
```

```
./drivers/mtd/ubi/ubi.h
```

```
ubi.h
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA  
*  
* Author: Artem Bityutskiy (Битюцкий Артём)  
*/
```

```
./drivers/mtd/ubi/upd.c  
upd.c
```

```
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*  
* Author: Artem Bityutskiy (Битюцкий Артём)  
*  
* Jan 2007: Alexander Schmidt, hacked per-volume update.  
*/
```

```
./drivers/mtd/ubi/vmt.c  
vmt.c
```

```
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```

```
./drivers/mtd/ubi/vtbl.c  
vtbl.c
```

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```

```
./drivers/mtd/ubi/wl.c  
wl.c
```

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```

```

*
* Authors: Artem Bityutskiy (Битюцкий Артём), Thomas Gleixner
*/

./drivers/net/4xx_enet.c
4xx_enet.c
/*-----+
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*-----*/
/*-----+
*
* File Name: enetemac.c
*
* Function: Device driver for the ethernet EMAC3 macro on the 405GP.
*
* Author: Mark Wisner
*
* Change Activity-
*
* Date Description of Change BY
* -----
* 05-May-99 Created MKW
* 27-Jun-99 Clean up JWB
* 16-Jul-99 Added MAL error recovery and better IP packet handling MKW
* 29-Jul-99 Added Full duplex support MKW
* 06-Aug-99 Changed names for Mal CR reg MKW
* 23-Aug-99 Turned off SYE when running at 10Mbs MKW
* 24-Aug-99 Marked descriptor empty after call_xlc MKW
* 07-Sep-99 Set MAL RX buffer size reg to ENET_MAX_MTU_ALIGNED / 16 MCG
* to avoid chaining maximum sized packets. Push starting
* RX descriptor address up to the next cache line boundary.

```

```

* 16-Jan-00 Added support for booting with IP of 0x0 MKW
* 15-Mar-00 Updated enetInit() to enable broadcast addresses in the
* EMAC0_RXM register. JWB
* 12-Mar-01 anne-sophie.harnois@nextream.fr
* - Variables are compatible with those already defined in
* include/net.h
* - Receive buffer descriptor ring is used to send buffers
* to the user
* - Info print about send/received/handled packet number if
* INFO_405_ENET is set
* 17-Apr-01 stefan.roese@esd-electronics.com
* - MAL reset in "eth_halt" included
* - Enet speed and duplex output now in one line
* 08-May-01 stefan.roese@esd-electronics.com
* - MAL error handling added (eth_init called again)
* 13-Nov-01 stefan.roese@esd-electronics.com
* - Set IST bit in EMAC0_MR1 reg upon 100MBit or full duplex
* 04-Jan-02 stefan.roese@esd-electronics.com
* - Wait for PHY auto negotiation to complete added
* 06-Feb-02 stefan.roese@esd-electronics.com
* - Bug fixed in waiting for auto negotiation to complete
* 26-Feb-02 stefan.roese@esd-electronics.com
* - rx and tx buffer descriptors now allocated (no fixed address
* used anymore)
* 17-Jun-02 stefan.roese@esd-electronics.com
* - MAL error debug printf 'M' removed (rx de interrupt may
* occur upon many incoming packets with only 4 rx buffers).
*-----*
* 17-Nov-03 travis.sawyer@sandburst.com
* - ported from 405gp_enet.c to utilized upto 4 EMAC ports
* in the 440GX. This port should work with the 440GP
* (2 EMACs) also
* 15-Aug-05 sr@denx.de
* - merged 405gp_enet.c and 440gx_enet.c to generic 4xx_enet.c

```

```
./drivers/net/8390.h
```

```
8390.h
```

```
/*
```

```
*/
```

```
/* Generic NS8390 register definitions. */
```

```
/* This file is part of Donald Becker's 8390 drivers, and is distributed
```

```
./drivers/net/altera_tse.c
```

```
altera_tse.c
```

```
/*
```

```
* Altera 10/100/1000 triple speed ethernet mac driver
```

```
*
```

```
* Copyright (C) 2008 Altera Corporation.
```

```
* Copyright (C) 2010 Thomas Chou <thomas@wytron.com.tw>
```

```
*
```

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* published by the Free Software Foundation.
*/
```

```
./drivers/net/altera_tse.h
```

```
altera_tse.h
```

```
/*
 * Altera 10/100/1000 triple speed ethernet mac
 *
 * Copyright (C) 2008 Altera Corporation.
 * Copyright (C) 2010 Thomas Chou <thomas@wytron.com.tw>
 *
 * This program is free software; you can redistribute it and/or modify
 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
*/
```

```
./drivers/net/armada100_fec.c
```

```
armada100_fec.c
```

```
/*
 * (C) Copyright 2011
 * eInfochips Ltd. <www.einfochips.com>
 * Written-by: Ajay Bhargav <ajay.bhargav@einfochips.com>
 *
 * (C) Copyright 2010
 * Marvell Semiconductor <www.marvell.com>
 * Contributor: Mahavir Jain <mjain@marvell.com>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
 * MA 02110-1301 USA
*/
```

```
./drivers/net/armada100_fec.h
```

```
armada100_fec.h
```

```
/*
 * (C) Copyright 2011
 * eInfochips Ltd. <www.einfochips.com>
 * Written-by: Ajay Bhargav <ajay.bhargav@einfochips.com>
 *
 * (C) Copyright 2010
 * Marvell Semiconductor <www.marvell.com>
 * Contributor: Mahavir Jain <mjain@marvell.com>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 * the License, or (at your option) any later version.
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 *
 * You should have received a copy of the GNU General Public License
 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
 * MA 02110-1301 USA
 */
```

```
./drivers/net/at91_emac.c
at91_emac.c
```

```
/*
 * Copyright (C) 2009 BuS Elektronik GmbH & Co. KG
 * Jens Scharsig (esw@bus-elektronik.de)
 *
 * (C) Copyright 2003
 * Author : Hamid Ikdoumi (Atmel)
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 * modify it under the terms of the GNU General Public License as
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 *
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```

```
* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/net/ax88180.c
```

```
ax88180.c
```

```
/*
 * ax88180: ASIX AX88180 Non-PCI Gigabit Ethernet u-boot driver
 *
 * This program is free software; you can distribute it and/or modify
 * it under the terms of the GNU General Public License (Version 2) as
 * published by the Free Software Foundation.
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 * You should have received a copy of the GNU General Public License
 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place - Suite 330, Boston MA 02111-1307,
 * USA.
*/
```

```
./drivers/net/ax88180.h
```

```
ax88180.h
```

```
/* ax88180.h: ASIX AX88180 Non-PCI Gigabit Ethernet u-boot driver */
/*
 *
 * This program is free software; you can distribute it and/or modify it
 * under the terms of the GNU General Public License (Version 2) as
 * published by the Free Software Foundation.
 *
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 * FITNESS FOR A PARTICULAR PURPOSE. See the GNU General Public License
 * for more details.
 *
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 * with this program; if not, write to the Free Software Foundation, Inc.,
 * 59 Temple Place - Suite 330, Boston MA 02111-1307, USA.
 *
*/
```

```
./drivers/net/ax88796.c
```

```
ax88796.c
```

```
/*
 * (c) 2007 Nobuhiro Iwamatsu <iwamatsu@nigauri.org>
 *
 * This program is free software; you can redistribute it and/or modify
 * it under the terms of the GNU General Public License as published by
```

```
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* (at your option) any later version.
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* GNU General Public License for more details.
*
* You should have received a copy of the GNU General Public License
* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*/
```

```
./drivers/net/ax88796.h
ax88796.h
```

```
/*
* AX88796L(NE2000) support
*
* (c) 2007 Nobuhiro Iwamatsu <iwamatsu@nigauri.org>
*
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* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2 of
* the License, or (at your option) any later version.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
```

```
./drivers/net/bfin_mac.c
bfin_mac.c
```

```
/*
* Driver for Blackfin On-Chip MAC device
*
* Copyright (c) 2005-2008 Analog Device, Inc.
*
* Licensed under the GPL-2 or later.
*/
```

```
./drivers/net/bfin_mac.h
bfin_mac.h
```

```
/*
```

```
* bfin_mac.h - some defines/structures for the Blackfin on-chip MAC.
*
* Copyright (c) 2005-2008 Analog Device, Inc.
*
* Licensed under the GPL-2 or later.
*/
```

```
./drivers/net/calxedaxgmac.c
calxedaxgmac.c
```

```
/*
* Copyright 2010-2011 Calxeda, Inc.
*
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* Software Foundation; either version 2 of the License, or (at your option)
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* more details.
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* this program. If not, see <http://www.gnu.org/licenses/>.
*/
```

```
./drivers/net/cpsw.c
cpsw.c
```

```
/*
* CPSW Ethernet Switch Driver
*
* Copyright (C) 2010 Texas Instruments Incorporated - http://www.ti.com/
*
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* kind, whether express or implied; without even the implied warranty
* of MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
* GNU General Public License for more details.
*/
```

```
./drivers/net/cs8900.c
cs8900.c
```

```
/*
* Cirrus Logic CS8900A Ethernet
*
* (C) 2009 Ben Warren , biggerbadderben@gmail.com
* Converted to use CONFIG_NET_MULTI API
```

```
*
* (C) 2003 Wolfgang Denk, wd@denx.de
*   Extension to synchronize ethaddr environment variable
*   against value in EEPROM
*
* (C) Copyright 2002
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*
* Copyright (C) 1999 Ben Williamson <benw@pobox.com>
*
* See file CREDITS for list of people who contributed to this
* project.
*
* This program is loaded into SRAM in bootstrap mode, where it waits
* for commands on UART1 to read and write memory, jump to code etc.
* A design goal for this program is to be entirely independent of the
* target board. Anything with a CL-PS7111 or EP7211 should be able to run
* this code in bootstrap mode. All the board specifics can be handled on
* the host.
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./drivers/net/cs8900.h
```

```
cs8900.h
```

```
/*
```

```
* Cirrus Logic CS8900A Ethernet
```

```
*
```

```
* (C) 2009 Ben Warren , biggerbadderben@gmail.com
```

```
*   Converted to use CONFIG_NET_MULTI API
```

```
*
```

```
* (C) Copyright 2002
```

```
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
```

```
* Marius Groeger <mgroeger@sysgo.de>
```

```
*
```

```
* Copyright (C) 1999 Ben Williamson <benw@pobox.com>
```

```
*
```

```
* See file CREDITS for list of people who contributed to this
* project.
*
* This program is loaded into SRAM in bootstrap mode, where it waits
* for commands on UART1 to read and write memory, jump to code etc.
* A design goal for this program is to be entirely independent of the
* target board. Anything with a CL-PS7111 or EP7211 should be able to run
* this code in bootstrap mode. All the board specifics can be handled on
* the host.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./drivers/net/davinci_emac.c
davinci_emac.c
```

```
/*
 * Ethernet driver for TI TMS320DM644x (DaVinci) chips.
 *
 * Copyright (C) 2007 Sergey Kubushyn <ksi@koi8.net>
 *
 * Parts shamelessly stolen from TI's dm644x_emac.c. Original copyright
 * follows:
 *
 * -----
 *
 * dm644x_emac.c
 *
 * TI DaVinci (DM644X) EMAC peripheral driver source for DV-EVM
 *
 * Copyright (C) 2005 Texas Instruments.
 *
 * -----
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 * the Free Software Foundation; either version 2 of the License, or
 * (at your option) any later version.
 *
 */
```

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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.  
* -----
```

```
./drivers/net/davinci_emac.h  
davinci_emac.h
```

```
/*  
* Copyright (C) 2011 Ilya Yanok, Emcraft Systems  
*  
* Based on: mach-davinci/emac_defs.h  
* Copyright (C) 2007 Sergey Kubushyn <ksi@koi8.net>  
*  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.  
*/
```

```
./drivers/net/dc2114x.c  
dc2114x.c
```

```
/*  
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* project.  
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```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/net/designware.c
```

```
designware.c
```

```
/*
 * (C) Copyright 2010
 * Vipin Kumar, ST Micoelectronics, vipin.kumar@st.com.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./drivers/net/designware.h
```

```
designware.h
```

```
/*
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 * Vipin Kumar, ST Micoelectronics, vipin.kumar@st.com.
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 * GNU General Public License for more details.
 *
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```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/net/dm9000x.c
dm9000x.c
/*
```

```
./drivers/net/dm9000x.h
dm9000x.h
/*
* dm9000 Ethernet
*/
/* although the registers are 16 bit, they are 32-bit aligned.
*/
```

```
./drivers/net/dnet.c
dnet.c
/*
* Dave Ethernet Controller driver
*
* Copyright (C) 2008 Dave S.r.l. <www.dave.eu>
*
* This program is free software; you can redistribute it and/or modify
* it under the terms of the GNU General Public License version 2 as
* published by the Free Software Foundation.
*/
```

```
./drivers/net/dnet.h
dnet.h
/*
* Dave Ethernet Controller driver
*
* Copyright (C) 2008 Dave S.r.l. <www.dave.eu>
*
* This program is free software; you can redistribute it and/or modify
* it under the terms of the GNU General Public License version 2 as
* published by the Free Software Foundation.
*/
```

```
./drivers/net/e1000.c
e1000.c
/*****
*****/
/*****
```

```
./drivers/net/e1000.h
e1000.h
/*****
```

```
./drivers/net/e1000_spi.c
```

```
e1000_spi.c
```

```
/*-----  
* SPI transfer  
*  
* This writes "bitlen" bits out the SPI MOSI port and simultaneously clocks  
* "bitlen" bits in the SPI MISO port. That's just the way SPI works.  
*  
* The source of the outgoing bits is the "dout" parameter and the  
* destination of the input bits is the "din" parameter. Note that "dout"  
* and "din" can point to the same memory location, in which case the  
* input data overwrites the output data (since both are buffered by  
* temporary variables, this is OK).  
*  
* This may be interrupted with Ctrl-C if "intr" is true, otherwise it will  
* never return an error.  
*/
```

```
./drivers/net/eeepro100.c
```

```
eeepro100.c
```

```
/*  
* (C) Copyright 2002  
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./drivers/net/enc28j60.c
```

```
enc28j60.c
```

```
/*  
* (C) Copyright 2010  
* Reinhard Meyer, EMK Elektronik, reinhard.meyer@emk-elektronik.de  
* Martin Krause, Martin.Krause@tqs.de
```

```
* reworked original enc28j60.c
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/net/enc28j60.h
enc28j60.h
```

```
/*
* (X) extracted from enc28j60.c
* Reinhard Meyer, EMK Elektronik, reinhard.meyer@emk-elektronik.de
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/net/ep93xx_eth.c
ep93xx_eth.c
```

```
/*
* Cirrus Logic EP93xx ethernet MAC / MII driver.
*
* Copyright (C) 2010, 2009
* Matthias Kaehlcke <matthias@kaehlcke.net>
*
* Copyright (C) 2004, 2005
* Cory T. Tusar, Videon Central, Inc., <ctusar@videon-central.com>
```

```
*
* Based on the original eth.[ch] Cirrus Logic EP93xx Rev D. Ethernet Driver,
* which is
*
* (C) Copyright 2002 2003
* Adam Bezanson, Network Audio Technologies, Inc.
* <bezanson@netaudiotech.com>
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* with this program; if not, write to the Free Software Foundation, Inc.,
* 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./drivers/net/ep93xx_eth.h
ep93xx_eth.h
```

```
/*
* Copyright (C) 2009 Matthias Kaehlcke <matthias@kaehlcke.net>
*
* Copyright (C) 2004, 2005
* Cory T. Tusar, Videon Central, Inc., <ctusar@videon-central.com>
*
* See file CREDITS for list of people who contributed to this
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
```

```

*
*/

./drivers/net/ethoc.c
ethoc.c
/*
* Opencore 10/100 ethernet mac driver
*
* Copyright (C) 2007-2008 Avionic Design Development GmbH
* Copyright (C) 2008-2009 Avionic Design GmbH
*   Thierry Reding <thierry.reding@avionic-design.de>
* Copyright (C) 2010 Thomas Chou <thomas@wytron.com.tw>
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* published by the Free Software Foundation.
*/

./drivers/net/fec_mxc.c
fec_mxc.c
/*
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* (C) Copyright 2008,2009 Eric Jarrige <eric.jarrige@armadeus.org>
* (C) Copyright 2008 Armadeus Systems nc
* (C) Copyright 2007 Pengutronix, Sascha Hauer <s.hauer@pengutronix.de>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./drivers/net/fec_mxc.h
fec_mxc.h
/*
* (C) Copyright 2009 Ilya Yanok, Emcraft Systems Ltd <yanok@emcraft.com>
* (C) Copyright 2008 Armadeus Systems, nc
* (C) Copyright 2008 Eric Jarrige <eric.jarrige@armadeus.org>
* (C) Copyright 2007 Pengutronix, Sascha Hauer <s.hauer@pengutronix.de>

```

```
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*
*/
```

```
./drivers/net/fm/dtsec.c
dtsec.c
```

```
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*/
```

```
./drivers/net/fm/eth.c
eth.c
```

```
/*
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```

```
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*/
```

```
./drivers/net/fm/fm.c
```

```
fm.c
```

```
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*/
```

```
./drivers/net/fm/fm.h
```

```
fm.h
```

```
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*/
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*/
```

```
./drivers/net/fm/init.c
```

```
init.c
```

```
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*/
```

```
./drivers/net/fm/memac.c
```

```
memac.c
```

```
/*  
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* Roy Zang <tie-fei.zang@freescale.com>  
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```

```
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*/
```

```
./drivers/net/fm/memac_phy.c  
memac_phy.c
```

```
/*  
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*   Roy Zang <tie-fei.zang@freescale.com>  
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* MA 02111-1307 USA  
* Some part is taken from tsec.c  
*/
```

```
./drivers/net/fm/p1023.c  
p1023.c
```

```
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*/
```

```
./drivers/net/fm/p4080.c
```

p4080.c

```
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 */
```

./drivers/net/fm/p5020.c

p5020.c

```
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 */
```

./drivers/net/fm/p5040.c

p5040.c

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*/
```

```
./drivers/net/fm/t4240.c
t4240.c
```

```
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```

```
./drivers/net/fm/tgec.c
tgec.c
```

```
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*/
```

```
./drivers/net/fm/tgec_phy.c
```

```
tgec_phy.c
```

```
/*
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
* Some part is taken from tsec.c
```

```
*/
```

```
./drivers/net/fsl_mcdmafec.c
```

```
fsl_mcdmafec.c
```

```
/*
```

```
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```

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*
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* TsiChung Liew (Tsi-Chung.Liew@freescale.com)
```

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```

```
./drivers/net/fsl_mdio.c
```

```
fsl_mdio.c
```

```
/*
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*/
```

```
./drivers/net/ftgmac100.c
```

```
ftgmac100.c
```

```
/*
* Faraday FTGMAC100 Ethernet
*
* (C) Copyright 2009 Faraday Technology
* Po-Yu Chuang <ratbert@faraday-tech.com>
*
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*/

./drivers/net/ftgmac100.h
ftgmac100.h
/*
* Faraday FTGMAC100 Ethernet
*
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./drivers/net/ftmac100.c
ftmac100.c
/*
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```

```
./drivers/net/ftmac100.h
```

```
ftmac100.h
```

```
/*
```

```
* Faraday FTMAC100 Ethernet
```

```
*
```

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```

```
*/
```

```
./drivers/net/greth.c
```

```
greth.c
```

```
/* Gaisler.com GRETH 10/100/1000 Ethernet MAC driver
```

```
*
```

```
* Driver use polling mode (no Interrupt)
```

```
*
```

```
* (C) Copyright 2007
```

```
* Daniel Hellstrom, Gaisler Research, daniel@gaisler.com
```

```
*
```

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```

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*
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*/
```

```
./drivers/net/greth.h
```

```
greth.h
```

```
/* Gaisler.com GRETH 10/100/1000 Ethernet MAC driver
```

```
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```

```
*/
```

```
./drivers/net/inca-ip_sw.c
```

```
inca-ip_sw.c
```

```
/*
* INCA-IP internal switch ethernet driver.
```

```
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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* MA 02111-1307 USA
*/
```

```
./drivers/net/ks8695eth.c
ks8695eth.c
```

```
/*
* ks8695eth.c -- KS8695 ethernet driver
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*/
```

```
./drivers/net/lan91c96.c
lan91c96.c
```

```
/*-----
* lan91c96.c
* This is a driver for SMSC's LAN91C96 single-chip Ethernet device, based
* on the SMC91111 driver from U-boot.
*
* (C) Copyright 2002
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Rolf Offermanns <rof@sysgo.de>
*
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*
* Information contained in this file was obtained from the LAN91C96
* manual from SMC. To get a copy, if you really want one, you can find
* information under www.smsc.com.
*
*
* "Features" of the SMC chip:
* 6144 byte packet memory. ( for the 91C96 )
* EEPROM for configuration
* AUI/TP selection ( mine has 10Base2/10BaseT select )
*
* Arguments:
*   io      = for the base address
*   irq     = for the IRQ
*
* author:
*   Erik Stahlman           ( erik@vt.edu )
*   Daris A Nevil          ( dnevil@snmc.com )
*
* Hardware multicast code from Peter Cammaert ( pc@denkart.be )
*
* Sources:
*   o  SMSC LAN91C96 databook (www.smsc.com)
*   o  smc91111.c (u-boot driver)
*   o  smc9194.c (linux kernel driver)
*   o  lan91c96.c (Intel Diagnostic Manager driver)
*
* History:
*   04/30/03 Mathijs Haarman      Modified smc91111.c (u-boot version)
*                                   for lan91c96
* -----
*/

./drivers/net/lan91c96.h
lan91c96.h
/*-----
* lan91c96.h
*
* (C) Copyright 2002
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
```

```
* Rolf Offermanns <rof@sysgo.de>
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*
* This file contains register information and access macros for
* the LAN91C96 single chip ethernet controller. It is a modified
* version of the smc9111.h file.
*
* Information contained in this file was obtained from the LAN91C96
* manual from SMC. To get a copy, if you really want one, you can find
* information under www.smsc.com.
*
* Authors
*   Erik Stahlman           ( erik@vt.edu )
*   Daris A Nevil          ( dnevil@snmc.com )
*
* History
* 04/30/03   Mathijs Haarman           Modified smc91111.h (u-boot version)
*                                     for lan91c96
* -----
*/
```

```
./drivers/net/macb.c
```

```
macb.c
```

```
/*
```

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```

```
*
```

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*
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*/
```

```
./drivers/net/macb.h
```

```
macb.h
```

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* MA 02111-1307 USA  
*/
```

```
./drivers/net/mcffec.c
```

```
mcffec.c
```

```
/*  
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*  
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* TsiChung Liew (Tsi-Chung.Liew@freescale.com)  
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* MA 02111-1307 USA
*/
```

```
./drivers/net/mcfmii.c
mcfmii.c
```

```
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* TsiChung Liew (Tsi-Chung.Liew@freescale.com)
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* MA 02111-1307 USA
*/
```

```
./drivers/net/mpc512x_fec.c
mpc512x_fec.c
```

```
/*
* (C) Copyright 2003-2010
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*
* Derived from the MPC8xx FEC driver.
* Adapted for MPC512x by Grzegorz Bernacki <gjb@semihalf.com>
*/
```

```
./drivers/net/mpc512x_fec.h
mpc512x_fec.h
```

```
/*
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*
*/
```

```

* Derived from the MPC8xx driver's header file.
*/
/* Receive & Transmit Buffer Descriptor definitions */

./drivers/net/mpc5xxx_fec.c
mpc5xxx_fec.c
/*
* (C) Copyright 2003-2010
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*
* This file is based on mpc4200fec.c,
* (C) Copyright Motorola, Inc., 2000
*/

./drivers/net/mpc5xxx_fec.h
mpc5xxx_fec.h
/*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* This file is based on mpc4200fec.h
* (C) Copyright Motorola, Inc., 2000
*
* odin ethernet header file
*/

./drivers/net/mvgbe.c
mvgbe.c
/*
* (C) Copyright 2009
* Marvell Semiconductor <www.marvell.com>
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
*
* (C) Copyright 2003
* Ingo Assmus <ingo.assmus@keymile.com>
*
* based on - Driver for MV64360X ethernet ports
* Copyright (C) 2002 rabeeh@galileo.co.il
*
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*/
```

```
./drivers/net/mvgbe.h  
mvgbe.h
```

```
/*  
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* Written-by: Prafulla Wadaskar <prafulla@marvell.com>  
*  
* based on - Driver for MV64360X ethernet ports  
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* MA 02110-1301 USA  
*/
```

```
./drivers/net/natsemi.c  
natsemi.c  
/*
```

```
./drivers/net/ne2000.c  
ne2000.c  
/*
```

```
./drivers/net/ne2000.h  
ne2000.h  
/*
```

```
./drivers/net/ne2000_base.c
```

ne2000_base.c

/*

./drivers/net/ne2000_base.h

ne2000_base.h

/*

./drivers/net/netconsole.c

netconsole.c

/*

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*/

./drivers/net/nicext.h

nicext.h

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*

* Name: nicext.h

*

* Description: Broadcom Network Interface Card Extension (NICE) is an
* extension to Linux NET device kernel mode drivers.
* NICE is designed to provide additional functionalities,
* such as receive packet intercept. To support Broadcom NICE,
* the network device driver can be modified by adding an
* device ioctl handler and by indicating receiving packets
* to the NICE receive handler. Broadcom NICE will only be

* enabled by a NICE-aware intermediate driver, such as
* Broadcom Advanced Server Program Driver (BASP). When NICE
* is not enabled, the modified network device drivers
* functions exactly as other non-NICE aware drivers.

* Author: Frankie Fan

* Created: September 17, 2000

*****/

./drivers/net/npe/include/IxAssert.h

IxAssert.h

/**

* @file IxAssert.h

* @date 21-MAR-2002 (replaced by OSAL)

* @brief This file contains assert and ensure macros used by the IXP400 software

* @par

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*/
```

```
./drivers/net/npe/include/IxAtmdAcc.h
```

```
IxAtmdAcc.h
```

```
/**
```

```
* @file    IxAtmdAcc.h
```

```
*
```

```
* @date    07-Nov-2001
```

```
*
```

```
* @brief  IxAtmdAcc Public API
```

```
*
```

```
* This file contains the public API of IxAtmdAcc, related to the
* data functions of the component
```

```
*
```

```
*
```

```
* @par
```

```
* IXP400 SW Release version 2.0
```

```
*
```

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*/
```

```
./drivers/net/npe/include/IxAtmdAccCtrl.h
IxAtmdAccCtrl.h
```

```
/**
 * @file IxAtmdAccCtrl.h
 *
 * @date 20-Mar-2002
 *
 * @brief IxAtmdAcc Public API
 *
 * This file contains the public API of IxAtmdAcc, related to the
 * control functions of the component.
 *
 * @par
 * IXP400 SW Release version 2.0
 *
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*/
```

```
./drivers/net/npe/include/IxAtmm.h
```

```
IxAtmm.h
```

```
/**
```

```
* @file IxAtmm.h
```

```
*
```

```
* @date 3-DEC-2001
```

```
*
```

```
* @brief Header file for the IXP400 ATM Manager component (IxAtmm)
```

```
*
```

```
*
```

```
* @par
```

```
* IXP400 SW Release version 2.0
```

```
*
```

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*/
```

```
./drivers/net/npe/include/IxAtmSch.h
```

```
IxAtmSch.h
```

```
/**
```

```
* @file IxAtmSch.h
```

```
*
```

```
* @date 23-NOV-2001
```

```
*
```

```
* @brief Header file for the IXP400 ATM Traffic Shaper
```

```
*
```

```
* This component demonstrates an ATM Traffic Shaper implementation. It
* will perform shaping on upto 12 ports and total of 44 VCs accross all ports,
* 32 are intended for AAL0/5 and 12 for OAM (1 per port).
* The supported traffic types are;1 rt-VBR VC where PCR = SCR.
* (Effectively CBR) and Up-to 44 VBR VCs.
```

```
*
```

```
* This component models the ATM ports and VCs and is capable of producing
* a schedule of ATM cells per port which can be supplied to IxAtmdAcc
* for execution on the data path.
```

```
*
```

```
* @par
```

```
* IXP400 SW Release version 2.0
```

```
*
```

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* @sa IxAtmm.h

*/

./drivers/net/npe/include/IxAtmTypes.h

IxAtmTypes.h

/**

* @file IxAtmTypes.h

*

* @date 24-MAR-2002

*

* @brief This file contains Atm types common to a number of Atm components.

*

* @par

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*

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*/
```

```
./drivers/net/npe/include/IxDmaAcc.h
```

```
IxDmaAcc.h
```

```
/**
```

```
* @file IxDmaAcc.h
```

```
*
```

```
* @date 15 October 2002
```

```
*
```

```
* @brief API of the IXP400 DMA Access Driver Component (IxDma)
```

```
*
```

```
*
```

```
* @par
```

```
* IXP400 SW Release version 2.0
```

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*/
```

```
./drivers/net/npe/include/IxEthAcc.h
```

```
IxEthAcc.h
```

```
/** @file IxEthAcc.h
```

```
*
* @brief this file contains the public API of @ref IxEthAcc component
*
* Design notes:
* The IX_OSAL_MBUF address is to be specified on bits [31-5] and must
* be cache aligned (bits[4-0] cleared)
*
*
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*/

./drivers/net/npe/include/IxEthAccDataPlane_p.h

IxEthAccDataPlane_p.h

/**

* @file IxEthAccDataPlane_p.h

* @author Intel Corporation

* @date 12-Feb-2002

* @brief Internal Header file for IXP425 Ethernet Access component.

* Design Notes:

* @par

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```

```
./drivers/net/npe/include/IxEthAccMac_p.h
```

```
IxEthAccMac_p.h
```

```
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```

```
./drivers/net/npe/include/IxEthAccMii_p.h
```

```
IxEthAccMii_p.h
```

```
/**
```

```
* @file IxEthAccMii_p.h
```

```
*
```

```
* @author Intel Corporation
```

```
* @date
```

```
*
```

```
* @brief MII Header file
```

```
*
```

```
* Design Notes:
```

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* @par
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```
* IXP400 SW Release version 2.0
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./drivers/net/npe/include/IxEthAccQueueAssign_p.h
IxEthAccQueueAssign_p.h
/**
 * @file IxEthAccQueueAssign_p.h
 *
 * @author Intel Corporation
 * @date 06-Mar-2002
 *
 * @brief Mapping from QMgr Q's to internal assignment
 *
 * Design Notes:
 *
 *
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./drivers/net/npe/include/IxEthAcc_p.h

IxEthAcc_p.h

/**

* @file IxEthAcc_p.h

* @author Intel Corporation

* @date 12-Feb-2002

* @brief Internal Header file for IXP425 Ethernet Access component.

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```

```
./drivers/net/npe/include/IxEthDB.h
```

```
IxEthDB.h
```

```
/** @file IxEthDB.h
```

```
*
* @brief this file contains the public API of @ref IxEthDB component
*
*
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*/

./drivers/net/npe/include/IxEthDBLocks_p.h
IxEthDBLocks_p.h
/**
* @file IxEthAccDBLocks_p.h
*
* @brief Definition of transaction lock stacks and lock utility macros
*
* @par
* IXP400 SW Release version 2.0
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*/
```

```
./drivers/net/npe/include/IxEthDBLog_p.h
IxEthDBLog_p.h
```

```
/**
 * @file IxEthDBLog_p.h
 *
 * @brief definitions of log macros and log configuration
 *
 * @par
 * IXP400 SW Release version 2.0
 *
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```

```
./drivers/net/npe/include/IxEthDBMessages_p.h
IxEthDBMessages_p.h
```

```
/**
 * @file IxEthDBMessages_p.h
 *
 * @brief Definitions of NPE messages
 *
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 *
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```

```
./drivers/net/npe/include/IxEthDBPortDefs.h
```

```
IxEthDBPortDefs.h
```

```
/**
```

```
* @file IxEthDBPortDefs.h
```

```
*
```

```
* @brief Public definition of the ports and port capabilities
```

```
*
```

```
* @par
```

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./drivers/net/npe/include/IxEthDBQoS.h
IxEthDBQoS.h
/**
* @file IxEthDBQoS.h
*
* @brief Public definitions for QoS traffic classes
*
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```

./drivers/net/npe/include/IxEthDB_p.h

IxEthDB_p.h

/**

* @file IxEthDB_p.h

*

* @brief Private MAC learning API

*

* @par

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./drivers/net/npe/include/IxEthMii.h

IxEthMii.h

/**

* @file IxEthMii.h

```
*
* @brief this file contains the public API of @ref IxEthMii component
*
* Design notes :
* The main intent of this API is to implement MII high level functionalities
* to support the codelets provided with the IXP400 software releases. It
* supercedes previous interfaces provided with @ref IxEthAcc component.
*
* This API has been tested with the PHYs provided with the
* IXP400 development platforms. It may not work for specific Ethernet PHYs
* used on specific boards.
*
* This source code detects and interface the LXT972, LXT973 and KS6995
* Ethernet PHYs.
*
* This source code should be considered as an example which may need
* to be adapted for different hardware implementations.
*
* It is strongly recommended to use public domain and GPL utilities
* like libmii, mii-diag for MII interface support.
*
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```

```
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IxEthMii_p.h
```

```
/**
 * @file IxEthMii_p.h
 *
 * @author Intel Corporation
 * @date
 *
 * @brief MII Header file
 *
 * Design Notes:
 *
 *
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 * IXP400 SW Release version 2.0
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```
./drivers/net/npe/include/IxEthNpe.h
```

```
IxEthNpe.h
```

```
/**
```

```
* @file IxEthNpe.h
```

```
*
```

```
* @par
```

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*/
```

```
./drivers/net/npe/include/IxFeatureCtrl.h
```

```
IxFeatureCtrl.h
```

```
/**
```

```
* @file IxFeatureCtrl.h
```

```
*
```

```
* @date 30-Jan-2003
```

```
* @brief This file contains the public API of the IXP400 Feature Control
* component.
```

```
*
```

```
*
```

```
* @par
```

```
* IXP400 SW Release version 2.0
```

```
*
```

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*/
/* -----

./drivers/net/npe/include/IxHssAcc.h
IxHssAcc.h
/**
* @file IxHssAcc.h
*
* @date 07-DEC-2001
*
* @brief This file contains the public API of the IXP400 HSS Access
* component
*
*
* @par
* IXP400 SW Release version 2.0
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*/
```

```
./drivers/net/npe/include/IxI2cDrv.h
IxI2cDrv.h
```

```
/**
 * @file IxI2cDrv.h
 *
 * @brief Header file for the IXP400 I2C Driver (IxI2cDrv)
 *
 * @version $Revision: 0.1 $
 *
 * @par
 * IXP400 SW Release version 2.0
 *
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```
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*/
```

```
./drivers/net/npe/include/IxNpeA.h
```

```
IxNpeA.h
```

```
/**
```

```
* @file    IxNpeA.h
```

```
*
```

```
* @date    22-Mar-2002
```

```
*
```

```
* @brief   Header file for the IXP400 ATM NPE API
```

```
*
```

```
*
```

```
* @par
```

```
* IXP400 SW Release version 2.0
```

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```
*
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*/

./drivers/net/npe/include/IxNpeDl.h
IxNpeDl.h
/**
* @file IxNpeDl.h
*
* @date 14 December 2001
* @brief This file contains the public API of the IXP400 NPE Downloader
*        component.
*
*
* @par
* IXP400 SW Release version 2.0
*
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*
```

```
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*/

./drivers/net/npe/include/IxNpeDlImageMgr_p.h
IxNpeDlImageMgr_p.h
/**
* @file IxNpeDlImageMgr_p.h
*
* @author Intel Corporation
* @date 14 December 2001
* @brief This file contains the private API for the ImageMgr module
*
*
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```
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*/
```

```
./drivers/net/npe/include/IxNpeDlMacros_p.h  
IxNpeDlMacros_p.h
```

```
/**
```

```
* @file IxNpeDlMacros_p.h
```

```
*
```

```
* @author Intel Corporation
```

```
* @date 21 January 2002
```

```
*
```

```
* @brief This file contains the macros for the IxNpeDl component.
```

```
*
```

```
*
```

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* @par
```

```
* IXP400 SW Release version 2.0
```

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*
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*/

./drivers/net/npe/include/IxNpeDlNpeMgrEcRegisters_p.h
IxNpeDlNpeMgrEcRegisters_p.h
/**
 * @file IxNpeDlNpeMgrEcRegisters_p.h
 *
 * @author Intel Corporation
 * @date 14 December 2001
 *
 * @par
 * IXP400 SW Release version 2.0
 *
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```

```
./drivers/net/npe/include/IxNpeDlNpeMgrUtils_p.h
IxNpeDlNpeMgrUtils_p.h
/**
 * @file IxNpeDlNpeMgrUtils_p.h
 *
 * @author Intel Corporation
 * @date 18 February 2002
 * @brief This file contains the private API for the NpeMgr module.
 *
 *
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 * IXP400 SW Release version 2.0
 *
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 */
```

```
./drivers/net/npe/include/IxNpeDlNpeMgr_p.h
```

IxNpeDlNpeMgr_p.h

```
/**
 * @file IxNpeDlNpeMgr_p.h
 *
 * @author Intel Corporation
 * @date 14 December 2001
 * @brief This file contains the private API for the NpeMgr module.
 *
 *
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```

./drivers/net/npe/include/IxNpeMh.h

IxNpeMh.h

```
/**
 * @file IxNpeMh.h
 *
 * @date 14 Dec 2001
 *
 * @brief This file contains the public API for the IXP400 NPE Message
 * Handler component.
 *
 *
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 * IXP400 SW Release version 2.0
 *
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```

```
./drivers/net/npe/include/IxNpeMhConfig_p.h
IxNpeMhConfig_p.h
```

```
/**
 * @file IxNpeMhConfig_p.h
 *
 * @author Intel Corporation
 * @date 18 Jan 2002
 *
 * @brief This file contains the private API for the Configuration module.
 *
 *
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 * IXP400 SW Release version 2.0
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```

```
./drivers/net/npe/include/IxNpeMhMacros_p.h
IxNpeMhMacros_p.h
```

```
/**
 * @file IxNpeMhMacros_p.h
 *
 * @author Intel Corporation
 * @date 21 Jan 2002
 *
 * @brief This file contains the macros for the IxNpeMh component.
 *
 *
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 * IXP400 SW Release version 2.0
 *
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```

```
./drivers/net/npe/include/IxNpeMhReceive_p.h
IxNpeMhReceive_p.h
```

```
/**
 * @file IxNpeMhReceive_p.h
 *
 * @author Intel Corporation
 * @date 18 Jan 2002
 *
 * @brief This file contains the private API for the Receive module.
 *
 *
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 * IXP400 SW Release version 2.0
 *
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```

```
./drivers/net/npe/include/IxNpeMhSend_p.h
IxNpeMhSend_p.h
```

```
/**
 * @file IxNpeMhSend_p.h
 *
 * @author Intel Corporation
 * @date 18 Jan 2002
 *
 * @brief This file contains the private API for the Send module.
 *
 *
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 *
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 */
```

```
./drivers/net/npe/include/IxNpeMhSolicitedCbMgr_p.h
IxNpeMhSolicitedCbMgr_p.h
```

```
/**
 * @file IxNpeMhSolicitedCbMgr_p.h
 *
 * @author Intel Corporation
 * @date 18 Jan 2002
 *
 * @brief This file contains the private API for the Solicited Callback
 * Manager module.
 *
 *
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 */
```

./drivers/net/npe/include/IxNpeMhUnsolicitedCbMgr_p.h

```
IxNpeMhUnsolicitedCbMgr_p.h
/**
 * @file IxNpeMhUnsolicitedCbMgr_p.h
 *
 * @author Intel Corporation
 * @date 18 Jan 2002
 *
 * @brief This file contains the private API for the Unsolicited Callback
 * Manager module.
 *
 *
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 *
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 */
```

```
./drivers/net/npe/include/IxNpeMicrocode.h
IxNpeMicrocode.h
/**
 * @date   April 18, 2005
 *
 * @brief  IXP400 NPE Microcode Image file
 *
 * This file was generated by the IxNpeDlImageGen tool.
 * It contains a NPE microcode image suitable for use
 * with the NPE Downloader (IxNpeDl) component in the
 * IXP400 Access Driver software library.
 *
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 *
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 */
```

```
./drivers/net/npe/include/IxOsal.h
IxOsal.h
```

```
/**
 * @file IxOsal.h
 *
 * @brief Top include file for OSAL
 *
 *
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 * IXP400 SW Release version 2.0
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 *
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 */
```

```
./drivers/net/npe/include/IxOsalAssert.h
IxOsalAssert.h
```

```
/*
 * @file      IxOsalAssert.h
 * @author    Intel Corporation
 * @date      25-08-2004
 *
 * @brief     description goes here
 *
 * @par
 * IXP400 SW Release version 2.0
 *
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 */

./drivers/net/npe/include/IxOsalBackward.h
IxOsalBackward.h
/**
 * This file is intended to provide backward
```

```

* compatibility for main osService/OSSL
* APIs.
*
* It shall be phased out gradually and users
* are strongly recommended to use IX_OSAL API.
*
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*/

./drivers/net/npe/include/IxOsalBackwardAssert.h
IxOsalBackwardAssert.h
/**
* This file is intended to provide backward
* compatibility for main osService/OSSL

```

```
* APIs.
*
* It shall be phased out gradually and users
* are strongly recommended to use IX_OSAL API.
*
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*/

./drivers/net/npe/include/IxOsalBackwardBufferMgt.h
IxOsalBackwardBufferMgt.h
/**
* This file is intended to provide backward
* compatibility for main osService/OSSL
* APIs.
```

```

*
* It shall be phased out gradually and users
* are strongly recommended to use IX_OSAL API.
*
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./drivers/net/npe/include/IxOsalBackwardCacheMMU.h
IxOsalBackwardCacheMMU.h
/**
* This file is intended to provide backward
* compatibility for main osService/OSSL
* APIs.
*

```

```

* It shall be phased out gradually and users
* are strongly recommended to use IX_OSAL API.
*
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```

```
./drivers/net/npe/include/IxOsalBackwardMemMap.h
```

```
IxOsalBackwardMemMap.h
```

```
/**
```

```

* This file is intended to provide backward
* compatibility for main osService/OSSL
* APIs.
*

```

```

* It shall be phased out gradually and users

```

```
* are strongly recommended to use IX_OSAL API.
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```

```
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IxOsalBackwardOsServices.h
```

```
/**
* This file is intended to provide backward
* compatibility for main osService/OSSL
* APIs.
*
* It shall be phased out gradually and users
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```

```
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```

```
./drivers/net/npe/include/IxOsalBackwardOssl.h
IxOsalBackwardOssl.h
```

```
/**
* This file is intended to provide backward
* compatibility for main osService/OSSL
* APIs.
*
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*
*/
```

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```

```
./drivers/net/npe/include/IxOsalBufferMgt.h
```

```
IxOsalBufferMgt.h
```

```
/**
```

```
* @file IxOsalBufferMgt.h
```

```
*
```

```
* @brief OSAL Buffer pool management and buffer management definitions.
```

```
*
```

```
* Design Notes:
```

```
*
```

```
* @par
```

```
* IXP400 SW Release version 2.0
```

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```

```
./drivers/net/npe/include/IxOsalBufferMgtDefault.h
IxOsalBufferMgtDefault.h
```

```
/**
 * @file IxOsalBufferMgtDefault.h
 *
 * @brief Default buffer pool management and buffer management
 * definitions.
 *
 * Design Notes:
 *
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*/
```

```
./drivers/net/npe/include/IxOsalConfig.h
```

```
IxOsalConfig.h
```

```
/**
```

```
* @file IxOsalConfig.h
```

```
*
```

```
* @brief OSAL Configuration header file
```

```
*
```

```
*
```

```
* @par
```

```
* IXP400 SW Release version 2.0
```

```
*
```

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```

```
./drivers/net/npe/include/IxOsalEndianess.h
```

```
IxOsalEndianess.h
```

```
/**
 * @file IxOsalEndianess.h (Obsolete file)
 *
 * @brief Header file for determining system endianness and OS
 *
 * @par
 * @version $Revision: 1.1
 *
 * @par
 * IXP400 SW Release version 2.0
 *
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./drivers/net/npe/include/IxOsalIoMem.h
IxOsalIoMem.h
/*
* @file      IxOsalIoMem.h
* @author    Intel Corporation
* @date      25-08-2004
*
* @brief     description goes here
*/
/**
* @par
* IXP400 SW Release version 2.0
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```

```
*
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*/
```

```
./drivers/net/npe/include/IxOsalMemAccess.h
IxOsalMemAccess.h
```

```
/**
 * @file IxOsalMemAccess.h
 *
 * @brief Header file for memory access
 *
 * @par
 * @version $Revision: 1.0 $
 *
 * @par
 * IXP400 SW Release version 2.0
 *
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*/
```

```
./drivers/net/npe/include/IxOsal0em.h
```

```
IxOsal0em.h
```

```
/**
```

```
* @file IxOsalIxp0em.h
```

```
*
```

```
* @brief this file contains platform-specific defines.
```

```
*
```

```
*
```

```
* @par
```

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```

```
*
```

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*/
```

```
./drivers/net/npe/include/IxOsal0s.h
IxOsal0s.h
No license information found.
```

```
./drivers/net/npe/include/IxOsal0sAssert.h
IxOsal0sAssert.h
No license information found.
```

```
./drivers/net/npe/include/IxOsal0sBufferMgt.h
IxOsal0sBufferMgt.h
```

```
/**
 * @file IxOsal0sBufferMgt.h
 *
 * @brief vxworks-specific buffer management module definitions.
 *
 * Design Notes:
 *
 * @par
 * IXP400 SW Release version 2.0
 *
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*/

./drivers/net/npe/include/IxOsal0sIxp400.h
IxOsal0sIxp400.h
/**
* @file IxOsal0sIxp400.h
*
* @brief OS and platform specific definitions
*
* Design Notes:
*
* @par
* IXP400 SW Release version 2.0
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```
*
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*/
```

```
./drivers/net/npe/include/IxOsal0sIxp400CustomizedMapping.h
IxOsal0sIxp400CustomizedMapping.h
```

```
/**
 * @file IxOsal0sIxp400CustomizedMapping.h
 *
 * @brief Set LE coherency modes for components.
 * The default setting is IX_OSAL_NO_MAPPING for LE.
 *
 * By default IX_OSAL_STATIC_MEMORY_MAP is defined for all the
components.
 * If any component uses a dynamic memory map it must define
 * IX_OSAL_DYNAMIC_MEMORY_MAP in its corresponding section.
```

```
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*
* @par
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*/
```

```
./drivers/net/npe/include/IxOsalOsTypes.h
IxOsalOsTypes.h
No license information found.
```

```
./drivers/net/npe/include/IxOsalOsUtilitySymbols.h
IxOsalOsUtilitySymbols.h
No license information found.
```

```
./drivers/net/npe/include/IxOsalTypes.h
IxOsalTypes.h
/**
* @file IxOsalTypes.h
*
* @brief Define OSAL basic data types.
*
* This file contains fundamental data types used by OSAL.
*
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```

```
./drivers/net/npe/include/IxOsalUtilitySymbols.h
IxOsalUtilitySymbols.h
```

```
/**
 * @file
 *
 * @brief OSAL Configuration header file
 *
 *
 * @par
 * IXP400 SW Release version 2.0
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*/

./drivers/net/npe/include/IxOsBuffMgt.h

IxOsBuffMgt.h

/**

* @file (Replaced by OSAL)

*

* @brief This file includes the OS dependant MBUF header files.

*

*

* @par

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```
./drivers/net/npe/include/IxOsBuffPoolMgt.h
IxOsBuffPoolMgt.h
```

```
/**
 * @file IxOsBuffPoolMgt.h (Replaced by OSAL)
 *
 * @date 9 Oct 2002
 *
 * @brief This file contains the mbuf pool implementation API
 *
 * @par
 * IXP400 SW Release version 2.0
 *
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*
* @par
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* This module contains the implementation of the OS Services buffer pool
* management service. This module provides routines for creating pools
* of buffers for exchange of network data, getting and returning buffers
* from and to the pool, and some other utility functions.
* <P>
* Currently, the pool has 2 underlying implementations - one for the vxWorks
* OS, and another which attempts to be OS-agnostic so that it can be used on
* other OS's such as Linux. The API is largely the same for all OS's,
* but there are some differences to be aware of. These are documented
* in the API descriptions below.
* <P>
* The most significant difference is this: when this module is used with
* the WindRiver VxWorks OS, it will create a pool of vxWorks "MBufs".
* These can be used directly with the vxWorks "netBufLib" OS Library.
* For other OS's, it will create a pool of generic buffers. These may need
* to be converted into other buffer types (sk_buff's in Linux, for example)
* before being used with any built-in OS routines available for
* manipulating network data buffers.
*
* @sa IxOsBuffMgt.h
*/

./drivers/net/npe/include/IxOsBufLib.h
IxOsBufLib.h
/**
* @file IxOsBufLib.h (Replaced by OSAL)
*
* @date 9 Oct 2002
*
* @brief This file contains the mbuf pool initialisation entry point

```

```
*
*
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*/

./drivers/net/npe/include/IxOsCacheMMU.h
IxOsCacheMMU.h
/**
* @file IxOsCacheMMU.h
*
* @brief this file contains the API of the @ref IxCacheMMU component
*
* <hr>
```

```

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*/

./drivers/net/npe/include/IxOsPrintf.h
IxOsPrintf.h
/**
* @file IxOsPrintf.h
*
* @brief this file contains the API of the @ref IxOsServices component
*
* @par
* IXP400 SW Release version 2.0
*

```

```
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```

```
./drivers/net/npe/include/IxOsServices.h
```

```
IxOsServices.h
```

```
/**
```

```
* @file (Replaced by OSAL)
```

```
*
```

```
* @brief this file contains the API of the @ref IxOsServices component
```

```
*
```

```
*
```

```
* @par
```

```
* IXP400 SW Release version 2.0
```

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./drivers/net/npe/include/IxOsServicesComponents.h
IxOsServicesComponents.h
/**
* @file IxOsServicesComponents.h (Replaced by OSAL)
*
* @brief Header file for memory access
*
* @par
*
* @par
* IXP400 SW Release version 2.0
*
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```

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*/
```

```
./drivers/net/npe/include/IxOsServicesEndianess.h
```

```
IxOsServicesEndianess.h
```

```
/**
```

```
* @file IxOsServicesEndianess.h (Replaced by OSAL)
*
* @brief Header file for determining system endianess and OS
*
* @par
*
* @par
* IXP400 SW Release version 2.0
*
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*/
```

```
./drivers/net/npe/include/IxOsServicesMemAccess.h
```

```
IxOsServicesMemAccess.h
```

```
/**
```

```
* @file IxOsServicesMemAccess.h (Replaced by OSAL)
```

```
*
```

```
* @brief Header file for memory access
```

```
*
```

```
* @par
```

```
*
```

```
* @par
```

```
* IXP400 SW Release version 2.0
```

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*
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*/

./drivers/net/npe/include/IxOsServicesMemMap.h

IxOsServicesMemMap.h

/**

* @file IxOsServicesMemMap.h (Replaced by OSAL)

*

* @brief Header file for memory access maps

*

* @par

*

* @par

* IXP400 SW Release version 2.0

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*/

./drivers/net/npe/include/IxParityENAcc.h
IxParityENAcc.h

```
/**  
 * @file IxParityENAcc.h  
 *  
 * @author Intel Corporation  
 * @date 24 Mar 2004  
 *  
 * @brief This file contains the public API for the IXP400 Parity Error  
 * Notifier access component.  
 *  
 * @par  
 * IXP400 SW Release version 2.0  
 *  
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*/

```

```

./drivers/net/npe/include/IxPerfProfAcc.h
IxPerfProfAcc.h

```

```

/**
* @file IxPerfProfAcc.h
*
* @brief Header file for the IXP400 Perf Prof component (IxPerfProfAcc)
*
* @par
* IXP400 SW Release version 2.0
*
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```

```
./drivers/net/npe/include/IxQMgr.h
```

```
IxQMgr.h
```

```
/**
```

```
* @file IxQMgr.h
```

```
*
```

```
* @date 30-Oct-2001
```

```
*
```

```
* @brief This file contains the public API of IxQMgr component.
```

```
*
```

```
* Some functions contained in this module are inline to achieve better
* data-path performance. For this to work, the function definitions are
* contained in this header file. The "normal" use of inline functions
* is to use the inline functions in the module in which they are
* defined. In this case these inline functions are used in external
* modules and therefore the use of "inline extern". What this means
* is as follows: if a function foo is declared as "inline extern" this
* definition is only used for inlining, in no case is the function
* compiled on its own. If the compiler cannot inline the function it
* becomes an external reference. Therefore in IxQMgrQAccess.c all
* inline functions are defined without the "inline extern" specifier
* and so define the external references. In all other source files
* including this header file, these functions are defined as "inline
* extern".
```

```
*
```

```
*
```

```
* @par
```

```
* IXP400 SW Release version 2.0
```

```
*
```

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*/
```

```
./drivers/net/npe/include/IxQMGrAqmIf_p.h
```

```
IxQMGrAqmIf_p.h
```

```
/**
```

```
* @file IxQMGrAqmIf_p.h
```

```
*
```

```
* @author Intel Corporation
```

```
* @date 30-Oct-2001
```

```
*
```

```
* @brief The IxQMGrAqmIf sub-component provides a number of inline
* functions for performing I/O on the AQM.
```

```
*
```

```
* Because some functions contained in this module are inline and are
* used in other modules (within the QMgr component) the definitions are
* contained in this header file. The "normal" use of inline functions
```

```
* is to use the inline functions in the module in which they are
* defined. In this case these inline functions are used in external
* modules and therefore the use of "inline extern". What this means
* is as follows: if a function foo is declared as "inline extern" this
* definition is only used for inlining, in no case is the function
* compiled on its own. If the compiler cannot inline the function it
* becomes an external reference. Therefore in IxQMgrAqmIf.c all
* inline functions are defined without the "inline extern" specifier
* and so define the external references. In all other modules these
* funtions are defined as "inline extern".
*
*
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*/
```

```
./drivers/net/npe/include/IxQMgrDefines_p.h
IxQMgrDefines_p.h
/**
 * @file    IxQMgrDefines_p.h
 *
 * @author  Intel Corporation
 * @date    19-Jul-2002
 *
 * @brief   IxQMgr Defines and tuneable constants
 *
 *
 * @par
 * IXP400 SW Release version 2.0
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 */
```

```
./drivers/net/npe/include/IxQMgrDispatcher_p.h
IxQMgrDispatcher_p.h
/**
 * @file    IxQMgrDispatcher_p.h
 *
 * @author  Intel Corporation
 * @date    07-Feb-2002
 *
 * @brief   This file contains the internal functions for dispatcher
 *
 *
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 * IXP400 SW Release version 2.0
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 */
```

./drivers/net/npe/include/IxQMgrLog_p.h

IxQMgrLog_p.h

/**

* @file IxQMgrLog_p.h

*

* @author Intel Corporation

* @date 07-Feb-2002

*

* @brief This file contains the internal functions for config

*

*

* @par

* IXP400 SW Release version 2.0

*

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*/

./drivers/net/npe/include/IxQMgrQAccess_p.h

IxQMgrQAccess_p.h

/**

* @file IxQMgrQAccess_p.h

*

* @author Intel Corporation

* @date 30-Oct-2001

*

* @brief QAccess private header file

*

*

* @par

* IXP400 SW Release version 2.0

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*/

./drivers/net/npe/include/IxQMgrQCfg_p.h

IxQMgrQCfg_p.h

/**

* @file IxQMgrQCfg_p.h

*

* @author Intel Corporation

* @date 07-Feb-2002

*

* @brief This file contains the internal functions for config

*

*

* @par

* IXP400 SW Release version 2.0

*

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*/

```
./drivers/net/npe/include/IxQueueAssignments.h
IxQueueAssignments.h
```

```
/**
 * @file IxQueueAssignments.h
 *
 * @author Intel Corporation
 * @date 29-Oct-2004
 *
 * @brief Central definition for queue assignments
 *
 * Design Notes:
 * This file contains queue assignments used by Ethernet (EthAcc),
 * HSS (HssAcc), ATM (atmdAcc) and DMA (dmaAcc) access libraries.
 *
 * Note: Ethernet QoS traffic class definitions are managed separately
 * by EthDB in IxEthDBQoS.h.
 *
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*/
```

```
./drivers/net/npe/include/IxSspAcc.h
```

```
IxSspAcc.h
```

```
/**
```

```
* @file IxSspAcc.h
```

```
*
```

```
* @brief Header file for the IXP400 SSP Serial Port Access (IxSspAcc)
```

```
*
```

```
* @version $Revision: 0.1 $
```

```
*
```

```
* @par
```

```
* IXP400 SW Release version 2.0
```

```
*
```

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*/

./drivers/net/npe/include/IxTimerCtrl.h
IxTimerCtrl.h
/**
* @file IxTimerCtrl.h
* @brief
* This is the header file for the Timer Control component.
*
* The timer callback control component provides a mechanism by which different
* client components can start a timer and have a supplied callback function
* invoked when the timer expires.
* The callbacks are all dispatched from one thread inside this component.
* Any component that needs to be called periodically should use this facility
* rather than create its own task with a sleep loop.
*
* @par
*
* @par
* IXP400 SW Release version 2.0
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*/
```

```
./drivers/net/npe/include/IxTimeSyncAcc.h
```

```
IxTimeSyncAcc.h
```

```
/**
```

```
* @file IxTimeSyncAcc.h
*
* @author Intel Corporation
* @date 07 May 2004
*
* @brief Header file for IXP400 Access Layer to IEEE 1588(TM) Precision
* Clock Synchronisation Protocol Hardware Assist
*
* @version 1
*
* @par
* IXP400 SW Release version 2.0
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*/
```

```
./drivers/net/npe/include/IxTypes.h
```

```
IxTypes.h
```

```
/**
```

```
* @file IxTypes.h (Replaced by OSAL)
```

```
*
```

```
* @date 28-NOV-2001
```

```
* @brief This file contains basic types used by the IXP400 software
```

```
*
```

```
* Design Notes:
```

```
* This file shall only include fundamental types and definitions to be
* shared by all the IXP400 components.
```

```
* Please DO NOT add component-specific types here.
```

```
*
```

```
*
```

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* @par
```

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*/
```

```
./drivers/net/npe/include/IxUART.h
IxUART.h
```

```
/**
 * @file IxUART.h
 *
 * @date 12-OCT-01
 *
 * @brief Public header for the Intel IXP400 internal UART, generic driver.
 *
 * Design Notes:
 * This driver allows you to perform the following functions:
 *     Device Initialization,
 *     send/receive characters.
 *
 * Perform Uart IOCTL for the following:
 *     Set/Get the current baud rate,
 *     set parity,
 *     set the number of Stop bits,
 *     set the character Length (5,6,7,8),
 *     enable/disable Hardware flow control.
 *
 * Only Polled mode is supported for now.
 *
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*/
```

```
./drivers/net/npe/include/IxVersionId.h
IxVersionId.h
/**
* @file IxVersionId.h
*
* @date 22-Aug-2002
*
* @brief This file contains the IXP400 Software version identifier
*
* @par
* IXP400 SW Release version 2.0
*
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*/

```

```
./drivers/net/npe/include/ix_error.h
```

```
ix_error.h
```

```
/**
```

```

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*
* = FILENAME
*   ix_error.h (Replaced by OSAL)
*
* = DESCRIPTION
*   This file will describe the basic error type and support functions that
*   will be used by the IXA SDK Framework API.
*
* = AUTHOR
*   Intel Corporation
*
* = CHANGE HISTORY
*   4/22/2002 4:19:03 PM - creation time
* =====
*/

./drivers/net/npe/include/ix_macros.h
ix_macros.h
/**
* =====
* = COPYRIGHT
*
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* IXP400 SW Release version 2.0
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*
* = FILENAME
*     ix_macros.h
*
* = DESCRIPTION
*     This file will define the basic preprocessor macros that are going to be
used
*     the IXA SDK Framework API.
*
* = AUTHOR
*     Intel Corporation
*
* = CHANGE HISTORY
*     4/22/2002 4:41:05 PM - creation time
* =====
*/
```

./drivers/net/npe/include/ix_oss1.h

ix_oss1.h

/**

* =====

* = COPYRIGHT

*

* @par

* IXP400 SW Release version 2.0

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OSSL - Operating System Services Library

*

* = MODULE

```

*      OSSL  Abstraction layer header file
*
* = FILENAME
*      ix_oss1.h (Replaced by OSAL)
*
* = DESCRIPTION
* This file contains the prototypes of OS-independent wrapper
* functions which allow the programmer not to be tied to a specific
* operating system. The OSSL functions can be divided into three classes:
*
* 1) synchronization-related wrapper functions around thread system calls
* 2) thread-related wrapper functions around thread calls
* 3) transactor/workbench osapi calls -- defined in osApi.h
*
* Both 1 and 2 classes of functions provide Thread Management, Thread
* Synchronization, Mutual Exclusion and Timer primitives. Namely,
* creation and deletion functions as well as the standard "wait" and
* "exit". Additionally, a couple of utility functions which enable to
* pause the execution of a thread are also provided.
*
* The 3rd class provides a slew of other OSAPI functions to handle
* Transactor/WorkBench OS calls.
*
*
* OSSL Thread APIs:
*      The OSSL thread functions that allow for thread creation,
*      get thread id, thread deletion and set thread prioirty.
*
*      ix_oss1_thread_create
*      ix_oss1_thread_get_id
*      ix_oss1_thread_exit
*      ix_oss1_thread_kill
*      ix_oss1_thread_set_priority
*      ix_oss1_thread__delay
*
* OSSL Semaphore APIs:
*      The OSSL semaphore functions that allow for initialization,
*      posting, waiting and deletion of semaphores.
*
*      ix_oss1_sem_init
*      ix_oss1_sem_fini
*      ix_oss1_sem_take
*      ix_oss1_sem_give
*      ix_oss1_sem_flush
*
* OSSL Mutex APIs:
*      The OSSL wrapper functions that allow for initialization,
*      posting, waiting and deletion of mutexes.
*
*      ix_oss1_mutex_init

```

```

*          ix_oss1_mutex_fini
*          ix_oss1_mutex_lock

./drivers/net/npe/include/ix_os_type.h
ix_os_type.h
/**
* =====
* = COPYRIGHT
*
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* IXP400 SW Release version 2.0
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* = FILENAME

```

```

*      ix_os_type.h (Replaced by OSAL)
*
* = DESCRIPTION
*      This file provides protable symbol definitions for the current OS type.
*
* = AUTHOR
*      Intel Corporation
*
* = CHANGE HISTORY
*      4/22/2002 4:43:30 PM - creation time
* =====
*/

./drivers/net/npe/include/ix_symbols.h
ix_symbols.h
/**
* =====
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* = FILENAME
*     ix_symbols.h
*
* = DESCRIPTION
*     This file declares all the global preprocessor symbols required by
*     the IXA SDK Framework API.
*
* = AUTHOR
*     Intel Corporation
*
* = CHANGE HISTORY
*     4/23/2002 10:41:13 AM - creation time
* =====
*/
```

```
./drivers/net/npe/include/ix_types.h
```

```
ix_types.h
```

```
/**
```

```
* =====
```

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* = FILENAME
*   ix_types.h
*
* = DESCRIPTION
*   This file will define generic types that will guarantee the protability
*   between different architectures and compilers. It should be used the entire
*   IXA SDK Framework API.
*
* = AUTHOR
*   Intel Corporation
*
* = CHANGE HISTORY
*   4/22/2002 4:44:17 PM - creation time
* =====
*/

./drivers/net/npe/include/npe.h
npe.h
/*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./drivers/net/npe/include/os_datatypes.h
```

```
os_datatypes.h
```

```
/**
```

```
* =====
```

```
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```

```
*
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*
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*     OSSL - Operating System Services  Library
*
* = MODULE
*     OS Specific Data Types header file
*
* = FILENAME
*     OSSL.h (Replaced by OSAL)
*
* = DESCRIPTION
*     This file  contains definitions and encapsulations for OS specific data types.
These
*     encapsulated  data types are  used by OSSL header files and OS API functions.
*
*
*****
*
* = AUTHOR
*     Intel Corporation
*
* = ACKNOWLEDGEMENTS
*
*
* = CREATION TIME
*     1/8/2002 1:53:42 PM
*
* = CHANGE HISTORY

./drivers/net/npe/IxEthAcc.c
IxEthAcc.c
/**
* @file IxEthAcc.c
*
* @author Intel Corporation
* @date 20-Feb-2001
*
* @brief This file contains the implementation of the IXP425 Ethernet Access
Component
*
* Design Notes:
*
* @par

```

```
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*/
```

```
./drivers/net/npe/IxEthAccCommon.c
```

```
IxEthAccCommon.c
```

```
/**
```

```
* @file IxEthAccCommon.c
```

```
*
```

```
* @author Intel Corporation
```

```
* @date 12-Feb-2002
```

```
*
```

```
* @brief This file contains the implementation common support routines for the  
component
```

```
*
```

```
* Design Notes:
```

```

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*/

./drivers/net/npe/IxEthAccControlInterface.c
IxEthAccControlInterface.c
/**
* @file IxEthAccControlInterface.c
*
* @author Intel Corporation
* @date
*
* @brief IX_ETH_ACC_PUBLIC wrappers for control plane functions
*

```

```

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./drivers/net/npe/IxEthAccDataPlane.c
IxEthAccDataPlane.c
/**
* @file IxEthDataPlane.c
*
* @author Intel Corporation
* @date 12-Feb-2002
*
* @brief This file contains the implementation of the IXPxxx

```

```

* Ethernet Access Data plane component
*
* Design Notes:
*
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./drivers/net/npe/IxEthAccMac.c
IxEthAccMac.c
/**
* @file IxEthAccMac.c
*
* @author Intel Corporation
* @date

```

```

*
* @brief MAC control functions
*
* Design Notes:
*
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./drivers/net/npe/IxEthAccMii.c
IxEthAccMii.c
/**
* @file IxEthAccMii.c
*
* @author Intel Corporation

```

```

* @date
*
* @brief MII control functions
*
* Design Notes:
*
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./drivers/net/npe/IxEthDBAPI.c
IxEthDBAPI.c
/**
* @file IxEthDBAPI.c
*
* @brief Implementation of the public API

```

```
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./drivers/net/npe/IxEthDBAPISupport.c
IxEthDBAPISupport.c
/**
* @file IxEthDBAPISupport.c
*
* @brief Public API support functions
*
* @par
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*
```

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*/
```

```
./drivers/net/npe/IxEthDBCore.c
IxEthDBCore.c
/**
* @file IxEthDBDBCore.c
*
* @brief Database support functions
*
* @par
* IXP400 SW Release version 2.0
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IxEthDBEvents.c
/**
* @file IxEthDBEvents.c
*
* @brief Implementation of the event processor component
*
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* IXP400 SW Release version 2.0
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*/
```

```
./drivers/net/npe/IxEthDBFeatures.c
IxEthDBFeatures.c
```

```
/**
 * @file IxEthDBFeatures.c
 *
 * @brief Implementation of the EthDB feature control API
 *
 * @par
 * IXP400 SW Release version 2.0
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```

```
./drivers/net/npe/IxEthDBFirewall.c
IxEthDBFirewall.c
/**
```

```
* @file IxEthDBFirewall.c
*
* @brief Implementation of the firewall API
*
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* IXP400 SW Release version 2.0
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*/

./drivers/net/npe/IxEthDBHashtable.c
IxEthDBHashtable.c
/**
* @file ethHash.c
*
* @brief Hashtable implementation
*
* @par
* IXP400 SW Release version 2.0
*
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*/
```

```
./drivers/net/npe/IxEthDBLearning.c
```

```
IxEthDBLearning.c
```

```
/**
```

```
* @file IxEthDBLearning.c
```

```
*
```

```
* @par
```

```
* IXP400 SW Release version 2.0
```

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*
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```

```
./drivers/net/npe/IxEthDBMem.c
IxEthDBMem.c
```

```
/**
```

```
* @file IxEthDBDBMem.c
```

```
*
```

```
* @brief Memory handling routines for the MAC address database
```

```
*
```

```
* @par
```

```
* IXP400 SW Release version 2.0
```

```
*
```

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*
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*/

./drivers/net/npe/IxEthDBNPEAdaptor.c
IxEthDBNPEAdaptor.c
/**
* @file IxEthDBDBNPEAdaptor.c
*
* @brief Routines that read and write learning/search trees in NPE-specific format
*
* @par
* IXP400 SW Release version 2.0
*
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```

```
*/

./drivers/net/npe/IxEthDBPortUpdate.c
IxEthDBPortUpdate.c
/**
 * @file IxEthDBDBPortUpdate.c
 *
 * @brief Implementation of dependency and port update handling
 *
 * @par
 * IXP400 SW Release version 2.0
 *
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 */

./drivers/net/npe/IxEthDBReports.c
IxEthDBReports.c
```

```
/**
 * @file IxEthDBAPI.c
 *
 * @brief Implementation of the public API
 *
 * @par
 * IXP400 SW Release version 2.0
 *
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./drivers/net/npe/IxEthDBSearch.c
IxEthDBSearch.c
/**
 * @file IxEthDBSearch.c
 *
 * @par
```

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```

```
./drivers/net/npe/IxEthDBSpanningTree.c
IxEthDBSpanningTree.c
```

```
/**
* @file IxEthDBSpanningTree.c
*
* @brief Implementation of the STP API
*
* @par
* IXP400 SW Release version 2.0
*
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*
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*/

./drivers/net/npe/IxEthDBUtil.c
IxEthDBUtil.c
/**
* @file ethUtil.c
*
* @brief Utility functions
*
* @par
* IXP400 SW Release version 2.0
*
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```

```
./drivers/net/npe/IxEthDBVlan.c
```

```
IxEthDBVlan.c
```

```
/**
```

```
* @file IxEthDBVlan.c
```

```
*
```

```
* @brief Implementation of the VLAN API
```

```
*
```

```
* @par
```

```
* IXP400 SW Release version 2.0
```

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*
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*/

./drivers/net/npe/IxEthDBWiFi.c

IxEthDBWiFi.c

/**

* @file IxEthDBAPI.c

*

* @brief Implementation of the public API

*

* @par

* IXP400 SW Release version 2.0

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```

```
./drivers/net/npe/IxEthMii.c
```

```
IxEthMii.c
```

```
/**
```

```
* @file IxEthMii.c
```

```
*
```

```
* @author Intel Corporation
```

```
* @date
```

```
*
```

```
* @brief MII control functions
```

```
*
```

```
* Design Notes:
```

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*
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* @par
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./drivers/net/npe/IxFeatureCtrl.c

IxFeatureCtrl.c

/**

* @file IxFeatureCtrl.c

*

* @author Intel Corporation

* @date 29-Jan-2003

*

* @brief Feature Control Public API Implementation

*

*

* @par

* IXP400 SW Release version 2.0

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```

```
./drivers/net/npe/IxNpeDl.c
IxNpeDl.c
```

```
/**
 * @file IxNpeDl.c
 *
 * @author Intel Corporation
 * @date 08 January 2002
 *
 * @brief This file contains the implementation of the public API for the
 *        IXP425 NPE Downloader component
 *
 *
 * @par
 * IXP400 SW Release version 2.0
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```

```
./drivers/net/npe/IxNpeDlImageMgr.c
IxNpeDlImageMgr.c
/**
```

```
* @file IxNpeDlImageMgr.c
*
* @author Intel Corporation
* @date 09 January 2002
*
* @brief This file contains the implementation of the private API for the
*        IXP425 NPE Downloader ImageMgr module
*
*
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```

```
./drivers/net/npe/IxNpeDlNpeMgr.c
IxNpeDlNpeMgr.c
```

```
/**
 * @file IxNpeDlNpeMgr.c
 *
 * @author Intel Corporation
 * @date 09 January 2002
 *
 * @brief This file contains the implementation of the private API for the
 *        IXP425 NPE Downloader NpeMgr module
 *
 * @par
 * IXP400 SW Release version 2.0
 *
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```

```
./drivers/net/npe/IxNpeDlNpeMgrUtils.c
```

```
IxNpeDlNpeMgrUtils.c
```

```
/**
```

```
* @file IxNpeDlNpeMgrUtils.c
```

```
*
```

```
* @author Intel Corporation
```

```
* @date 18 February 2002
```

```
*
```

```
* @brief This file contains the implementation of the private API for the
*        IXP425 NPE Downloader NpeMgr Utils module
```

```
*
```

```
*
```

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* @par
```

```
* IXP400 SW Release version 2.0
```

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*/
```

```
./drivers/net/npe/IxNpeMh.c
IxNpeMh.c
/**
* @file IxNpeMh.c
*
* @author Intel Corporation
* @date 18 Jan 2002
*
* @brief This file contains the implementation of the public API for the
* IXP425 NPE Message Handler component.
*
* @par
* IXP400 SW Release version 2.0
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IxNpeMhConfig.c
/**
* @file IxNpeMhConfig.c
*
* @author Intel Corporation
* @date 18 Jan 2002
*
* @brief This file contains the implementation of the private API for the
* Configuration module.
*
*
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IxNpeMhReceive.c
/**
* @file IxNpeMhReceive.c
*
* @author Intel Corporation
* @date 18 Jan 2002
*
* @brief This file contains the implementation of the private API for the
* Receive module.
*
* @par
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IxNpeMhSend.c
/**
* @file IxNpeMhSend.c
*
* @author Intel Corporation
* @date 18 Jan 2002
*
* @brief This file contains the implementation of the private API for the
* Send module.
*
*
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./drivers/net/npe/IxNpeMhSolicitedCbMgr.c
IxNpeMhSolicitedCbMgr.c
/**
* @file IxNpeMhSolicitedCbMgr.c
*
* @author Intel Corporation
* @date 18 Jan 2002
*
* @brief This file contains the implementation of the private API for the
* Solicited Callback Manager module.
*
*
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./drivers/net/npe/IxNpeMhUnsolicitedCbMgr.c
IxNpeMhUnsolicitedCbMgr.c
/**
* @file IxNpeMhUnsolicitedCbMgr.c
*
* @author Intel Corporation
* @date 18 Jan 2002
*
* @brief This file contains the implementation of the private API for
* the Unsolicited Callback Manager module.
*
*
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./drivers/net/npe/IxOsalBufferMgt.c
IxOsalBufferMgt.c
/**
* @file IxOsalBufferMgt.c
*
* @brief Default buffer pool management and buffer management
* Implementation.
*
* Design Notes:
*
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* IXP400 SW Release version 2.0
*
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./drivers/net/npe/IxOsalIoMem.c
IxOsalIoMem.c
/**
* @file IxOsalIoMem.c
*
* @brief OS-independent IO/Mem implementation
*
* @par
* IXP400 SW Release version 2.0
*
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```
./drivers/net/npe/IxOsal0sCacheMMU.c
IxOsal0sCacheMMU.c
```

```
/**
* @file IxOsal0sCacheMMU.c (linux)
*
* @brief Cache MemAlloc and MemFree.
*
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* IXP400 SW Release version 1.5
*
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*/

./drivers/net/npe/IxOsalOsMsgQ.c

IxOsalOsMsgQ.c

/**

* @file IxOsalOsMsgQ.c (eCos)

*

* @brief OS-specific Message Queue implementation.

*

*

* @par

* IXP400 SW Release version 1.5

*

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```

```
./drivers/net/npe/IxOsa10sSemaphore.c
IxOsa10sSemaphore.c
```

```
/**
* @file IxOsa10sSemaphore.c (eCos)
*
* @brief Implementation for semaphore and mutex.
*
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* IXP400 SW Release version 1.5
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*/

./drivers/net/npe/IxOsal0sServices.c

IxOsal0sServices.c

/**

* @file IxOsal0sServices.c (linux)

*

* @brief Implementation for Irq, Mem, sleep.

*

*

* @par

* IXP400 SW Release version 1.5

*

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*/

./drivers/net/npe/IxOsal0sThread.c
IxOsal0sThread.c
/**
* @file IxOsal0sThread.c (eCos)
*
* @brief OS-specific thread implementation.
*
* @par
* IXP400 SW Release version 1.5
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./drivers/net/npe/IxQMGrAqmIf.c

IxQMGrAqmIf.c

/*

* @file: IxQMGrAqmIf.c

*

* @author Intel Corporation

* @date 30-Oct-2001

*

* @brief This component provides a set of functions for
* performing I/O on the AQM hardware.

*

* Design Notes:

* These functions are intended to be as fast as possible
* and as a result perform NO PARAMETER CHECKING.

*

*

* @par

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```

```
./drivers/net/npe/IxQMGrDispatcher.c
IxQMGrDispatcher.c
```

```
/**
 * @file    IxQMGrDispatcher.c
 *
 * @author  Intel Corporation
 * @date    20-Dec-2001
 *
 * @brief   This file contains the implementation of the Dispatcher sub component
 *
 * @par
 * IXP400 SW Release version 2.0
 *
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```

```
./drivers/net/npe/IxQMgrInit.c
IxQMgrInit.c
```

```
/**
 * @file    IxQMgrInit.c
 *
 * @author  Intel Corporation
 * @date    30-Oct-2001
 *
 * @brief:  Provided initialization of the QMgr component and its subcomponents.
 *
 * @par
 * IXP400 SW Release version 2.0
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./drivers/net/npe/IxQMgrQAccess.c
IxQMgrQAccess.c
/**
* @file    IxQMgrQAccess.c
*
* @author  Intel Corporation
* @date    30-Oct-2001
*
* @brief   This file contains functions for putting entries on a queue and
* removing entries from a queue.
*
* @par
* IXP400 SW Release version 2.0
*
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```

```
./drivers/net/npe/IxQMGrQCfg.c
IxQMGrQCfg.c
```

```
/**
 * @file QMGrQCfg.c
 *
 * @author Intel Corporation
 * @date 30-Oct-2001
 *
 * @brief This modules provides an interface for setting up the static
 * configuration of AQM queues.This file contains the following
 * functions:
 *
 *
 *
 * @par
 * IXP400 SW Release version 2.0
 *
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```

```
./drivers/net/npe/miiphy.c
miiphy.c
/*-----+
```

```
./drivers/net/npe/npe.c
npe.c
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```

* MA 02111-1307 USA
*/

./drivers/net/ns8382x.c
ns8382x.c
/*

./drivers/net/pcnet.c
pcnet.c
/*

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*
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*/

./drivers/net/phy/atheros.c
atheros.c
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* Atheros PHY drivers
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*/
```

```
./drivers/net/phy/broadcom.c
broadcom.c
```

```
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 * Broadcom PHY drivers
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```

```
./drivers/net/phy/davicom.c
davicom.c
```

```
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 * Davicom PHY drivers
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```

```
./drivers/net/phy/generic_10g.c
generic_10g.c
```

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 * Generic PHY Management code
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 *
 * Based loosely off of Linux's PHY Lib
 */
```

```
./drivers/net/phy/lxt.c
lxt.c
```

```
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 * LXT PHY drivers
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*/
```

```
./drivers/net/phy/marvell.c
```

```
marvell.c
```

```
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* Marvell PHY drivers
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```

```
./drivers/net/phy/micrel.c
```

```
micrel.c
```

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* Micrel PHY drivers
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*/
```

```
./drivers/net/phy/miiphybb.c
```

```
miiphybb.c
```

```
/*
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```

```
./drivers/net/phy/mv88e61xx.c
```

```
mv88e61xx.c
```

```
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```

```
./drivers/net/phy/mv88e61xx.h
mv88e61xx.h
```

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```
./drivers/net/phy/mv88e6352.c
mv88e6352.c
```

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```

```
./drivers/net/phy/natsemi.c
```

```
natsemi.c
```

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```
./drivers/net/phy/phy.c
```

```
phy.c
```

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* Based loosely off of Linux's PHY Lib
*/
```

```
./drivers/net/phy/realtek.c
```

```
realtek.c
```

```
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* RealTek PHY drivers
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```

```
./drivers/net/phy/smsc.c
```

```
smsc.c
```

```
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*
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* author Andy Fleming
*
* Some code get from linux kernel
* Copyright (c) 2006 Herbert Valerio Riedel <hvr@gnu.org>
*
*/
```

```
./drivers/net/phy/teranetics.c
```

```
teranetics.c
```

```
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* Teranetics PHY drivers
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*/
```

```
./drivers/net/phy/vitesse.c
```

```
vitesse.c
```

```
/*
```

```
* Vitesse PHY drivers
*
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*/
```

```
./drivers/net/plb2800_eth.c
plb2800_eth.c
```

```
/*
* PLB2800 internal switch ethernet driver.
*
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* MA 02111-1307 USA
*/
```

```
./drivers/net/rtl8139.c
rtl8139.c
```

```
/*
 * rtl8139.c : U-Boot driver for the RealTek RTL8139
 *
 * Masami Komiya (mkomiya@sonare.it)
 *
 * Most part is taken from rtl8139.c of etherboot
 */
/* rtl8139.c - etherboot driver for the Realtek 8139 chipset

./drivers/net/rtl8169.c
rtl8169.c
/*
 * rtl8169.c : U-Boot driver for the RealTek RTL8169
 *
 * Masami Komiya (mkomiya@sonare.it)
 *
 * Most part is taken from r8169.c of etherboot
 */
/*****
 *   r8169.c: Etherboot device driver for the RealTek RTL-8169 Gigabit
 *   Written 2003 by Timothy Legge <tlegge@rogers.com>
 *
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 *   along with this program; if not, write to the Free Software
 *   Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
 *
 *   Portions of this code based on:
 *   r8169.c: A RealTek RTL-8169 Gigabit Ethernet driver
 *           for Linux kernel 2.4.x.
 *
 *   Written 2002 ShuChen <shuchen@realtek.com.tw>
 *   See Linux Driver for full information
 *
 *   Linux Driver Version 1.27a, 10.02.2002
 *
 *   Thanks to:
 *   Jean Chen of RealTek Semiconductor Corp. for
 *   providing the evaluation NIC used to develop
```

```
*      this driver. RealTek's support for Etherboot
*      is appreciated.
*
*      REVISION HISTORY:
*      =====
*
*      v1.0          11-26-2003      timlegge          Initial port of Linux driver
*      v1.5          01-17-2004      timlegge          Initial driver output cleanup
```

```
*      Indent Options: indent -kr -i8
*****/
/*
* 26 August 2006 Mihai Georgian <u-boot@linuxnotincluded.org.uk>
* Modified to use le32_to_cpu and cpu_to_le32 properly
*/
```

```
./drivers/net/sh_eth.c
sh_eth.c
```

```
/*
* sh_eth.c - Driver for Renesas ethernet controler.
*
* Copyright (C) 2008, 2011 Renesas Solutions Corp.
* Copyright (c) 2008, 2011 Nobuhiro Iwamatsu
* Copyright (c) 2007 Carlos Munoz <carlos@kenati.com>
*
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./drivers/net/sh_eth.h
sh_eth.h
```

```
/*
* sh_eth.h - Driver for Renesas SuperH ethernet controler.
*
* Copyright (C) 2008 - 2012 Renesas Solutions Corp.
* Copyright (c) 2008 - 2012 Nobuhiro Iwamatsu
* Copyright (c) 2007 Carlos Munoz <carlos@kenati.com>
*
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*/
```

```
./drivers/net/smc91111.c
smc91111.c
```

```
/*-----
```

```
./drivers/net/smc91111.h
smc91111.h
```

```
/*-----
```

```
./drivers/net/smc911x.c
smc911x.c
```

```
/*
```

```
* SMSC LAN9[12]1[567] Network driver
```

```
*
```

```
* (c) 2007 Pengutronix, Sascha Hauer <s.hauer@pengutronix.de>
```

```
*
```

```
* See file CREDITS for list of people who contributed to this
* project.
```

```
*
```

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```
*
```

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```

```
*
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```

```
*/
```

```
./drivers/net/smc911x.h
smc911x.h
```

```
/*
 * SMC LAN9[12]1[567] Network driver
 *
 * (c) 2007 Pengutronix, Sascha Hauer <s.hauer@pengutronix.de>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./drivers/net/tsec.c
```

```
tsec.c
```

```
/*
 * Freescale Three Speed Ethernet Controller driver
 *
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 * terms of the GNU Public License, Version 2, incorporated
 * herein by reference.
 *
 * Copyright 2004-2011 Freescale Semiconductor, Inc.
 * (C) Copyright 2003, Motorola, Inc.
 * author Andy Fleming
 *
 */
```

```
./drivers/net/tsi108_eth.c
```

```
tsi108_eth.c
```

```
/*
 *
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 *
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
* Description:
* Ethernet interface for Tundra TSI108 bridge chip
*
*****/
```

```
./drivers/net/uli526x.c
uli526x.c
```

```
/*
* Copyright 2007, 2010 Freescale Semiconductor, Inc.
*
* Author: Roy Zang <tie-fei.zang@freescale.com>, Sep, 2007
*
* Description:
* ULI 526x Ethernet port driver.
* Based on the Linux driver: drivers/net/tulip/uli526x.c
*
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* it under the terms of the GNU General Public License as published by
* the Free Software Foundation; either version 2 of the License, or
* (at your option) any later version.
*/
```

```
./drivers/net/vsc7385.c
vsc7385.c
```

```
/*
* Vitesse 7385 Switch Firmware Upload
*
* Author: Timur Tabi <timur@freescale.com>
*
* Copyright 2008 Freescale Semiconductor, Inc. This file is licensed
* under the terms of the GNU General Public License version 2. This
* program is licensed "as is" without any warranty of any kind, whether
* express or implied.
*
* This module uploads proprietary firmware for the Vitesse VSC7385 5-port
* switch.
*/
```

```
./drivers/net/xilinx_axi_emac.c
xilinx_axi_emac.c
/*
 * Copyright (C) 2011 Michal Simek <monstr@monstr.eu>
 * Copyright (C) 2011 PetaLogix
 * Copyright (C) 2010 Xilinx, Inc. All rights reserved.
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./drivers/net/xilinx_emaclite.c
xilinx_emaclite.c
/*
 * (C) Copyright 2007-2009 Michal Simek
 * (C) Copyright 2003 Xilinx Inc.
 *
 * Michal SIMEK <monstr@monstr.eu>
 *
 * See file CREDITS for list of people who contributed to this
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```

```
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* MA 02111-1307 USA  
*/
```

```
./drivers/net/xilinx_ll_temac.c  
xilinx_ll_temac.c
```

```
/*  
* Xilinx xps_ll_temac ethernet driver for u-boot  
*  
* supports SDMA or FIFO access and MDIO bus communication  
*  
* Copyright (C) 2011 - 2012 Stephan Linz <linz@li-pro.net>  
* Copyright (C) 2008 - 2011 Michal Simek <monstr@monstr.eu>  
* Copyright (C) 2008 - 2011 PetaLogix  
*  
* Based on Yoshio Kashiwagi kashiwagi@co-nss.co.jp driver  
* Copyright (C) 2008 Nissin Systems Co.,Ltd.  
* March 2008 created  
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*  
* [S]: [0]/ip_documentation/xps_ll_temac.pdf  
* [A]: [0]/application_notes/xapp1041.pdf  
*/
```

```
./drivers/net/xilinx_ll_temac.h  
xilinx_ll_temac.h
```

```
/*  
* Xilinx xps_ll_temac ethernet driver for u-boot  
*  
* LL_TEMAC interface  
*  
* Copyright (C) 2011 - 2012 Stephan Linz <linz@li-pro.net>  
* Copyright (C) 2008 - 2011 Michal Simek <monstr@monstr.eu>  
* Copyright (C) 2008 - 2011 PetaLogix  
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* option) any later version.  
*  
*/
```

```

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*
* [S]: [0]/ip_documentation/xps_ll_temac.pdf
* [A]: [0]/application_notes/xapp1041.pdf
*/

./drivers/net/xilinx_ll_temac_fifo.c
xilinx_ll_temac_fifo.c
/*
* Xilinx xps_ll_temac ethernet driver for u-boot
*
* FIFO sub-controller
*
* Copyright (C) 2011 - 2012 Stephan Linz <linz@li-pro.net>
* Copyright (C) 2008 - 2011 Michal Simek <monstr@monstr.eu>
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* [F]: [0]/ip_documentation/xps_ll_fifo.pdf
* [S]: [0]/ip_documentation/xps_ll_temac.pdf
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*/

./drivers/net/xilinx_ll_temac_fifo.h
xilinx_ll_temac_fifo.h
/*
* Xilinx xps_ll_temac ethernet driver for u-boot
*
* FIFO sub-controller interface
*
* Copyright (C) 2011 - 2012 Stephan Linz <linz@li-pro.net>
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*/

./drivers/net/xilinx_ll_temac_mdio.c

xilinx_ll_temac_mdio.c

/*

* Xilinx xps_ll_temac ethernet driver for u-boot

*

* MDIO bus access

*

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* [A]: [Ø]/application_notes/xapp1041.pdf

*/

./drivers/net/xilinx_ll_temac_mdio.h

xilinx_ll_temac_mdio.h

/*

* Xilinx xps_ll_temac ethernet driver for u-boot

*

* MDIO bus access interface

*

* Copyright (C) 2011 - 2012 Stephan Linz <linz@li-pro.net>

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```
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* [S]: [0]/ip_documentation/xps_ll_temac.pdf
* [A]: [0]/application_notes/xapp1041.pdf
*/
```

```
./drivers/net/xilinx_ll_temac_sdma.c
xilinx_ll_temac_sdma.c
```

```
/*
 * Xilinx xps_ll_temac ethernet driver for u-boot
 *
 * SDMA sub-controller
 *
 * Copyright (C) 2011 - 2012 Stephan Linz <linz@li-pro.net>
 * Copyright (C) 2008 - 2011 Michal Simek <monstr@monstr.eu>
 * Copyright (C) 2008 - 2011 PetaLogix
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 * Based on Yoshio Kashiwagi kashiwagi@co-nss.co.jp driver
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 * [S]: [0]/ip_documentation/xps_ll_temac.pdf
 * [A]: [0]/application_notes/xapp1041.pdf
*/
```

```
./drivers/net/xilinx_ll_temac_sdma.h
xilinx_ll_temac_sdma.h
```

```
/*
 * Xilinx xps_ll_temac ethernet driver for u-boot
 *
 * SDMA sub-controller interface
```

```
*
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*
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* [S]: [0]/ip_documentation/xps_ll_temac.pdf
* [A]: [0]/application_notes/xapp1041.pdf
*/
```

```
./drivers/net/zynq_gem.c
```

```
zynq_gem.c
```

```
/*
```

```
* (C) Copyright 2011 Michal Simek
```

```
*
```

```
* Michal SIMEK <monstr@monstr.eu>
```

```
*
```

```
* Based on Xilinx gmac driver:
```

```
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```
*
```

```
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* MA 02111-1307 USA
```

```
*/
```

```
./drivers/pci/fsl_pci_init.c
```

fsl_pci_init.c

```
/*
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./drivers/pci/pci.c

pci.c

```
/*
 * (C) Copyright 2001 Sysgo Real-Time Solutions, GmbH <www.elinos.com>
 * Andreas Heppel <aheppel@sysgo.de>
 *
 * (C) Copyright 2002, 2003
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./drivers/pci/pci_auto.c

pci_auto.c

```
/*
 * arch/powerpc/kernel/pci_auto.c
 *
 * PCI autoconfiguration library
 *
 * Author: Matt Porter <mporter@mvista.com>
 *
 * Copyright 2000 MontaVista Software Inc.
 *
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 * under the terms of the GNU General Public License as published by the
 * Free Software Foundation; either version 2 of the License, or (at your
 * option) any later version.
 */
```

```
./drivers/pci/pci_ftpci100.c
pci_ftpci100.c
```

```
/*
 * Faraday FTPCI100 PCI Bridge Controller Device Driver Implementation
 *
 * Copyright (C) 2011 Andes Technology Corporation
 * Gavin Guo, Andes Technology Corporation <gavinguo@andestech.com>
 * Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
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 */
```

```
./drivers/pci/pci_ftpci100.h
pci_ftpci100.h
```

```
/*
 * Faraday FTPCI100 PCI Bridge Controller Device Driver Implementation
 *
 * Copyright (C) 2010 Andes Technology Corporation
 * Gavin Guo, Andes Technology Corporation <gavinguo@andestech.com>
 * Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
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*/
```

```
./drivers/pci/pci_indirect.c
```

```
pci_indirect.c
```

```
/*
* Support for indirect PCI bridges.
*
* Copyright (C) 1998 Gabriel Paubert.
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* as published by the Free Software Foundation; either version
* 2 of the License, or (at your option) any later version.
*/
```

```
./drivers/pci/pci_ixp.c
```

```
pci_ixp.c
```

```
/*
* IXP PCI Init
*
* (C) Copyright 2011
* Michael Schwingen, michael@schwingen.org
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```
./drivers/pci/pci_sh4.c
```

```
pci_sh4.c
```

```
/*
 * SH4 PCI Controller (PCIC) for U-Boot.
 * (C) Dustin McIntire (dustin@sensoria.com)
 * (C) 2007,2008 Nobuhiro Iwamatsu <iwamatsu@nigauri.org>
 * (C) 2008 Yusuke Goda <goda.yusuke@renesas.com>
 *
 * u-boot/arch/sh/cpu/sh4/pci-sh4.c
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```
./drivers/pci/pci_sh7751.c
```

```
pci_sh7751.c
```

```
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 * SH7751 PCI Controller (PCIC) for U-Boot.
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```

```
./drivers/pci/pci_sh7780.c
```

```
pci_sh7780.c
```

```
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```
./drivers/pci/tsi108_pci.c
```

```
tsi108_pci.c
```

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```

```
./drivers/pci/w83c553f.c
w83c553f.c
```

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/*
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```
./drivers/pcmcia/i82365.c
i82365.c
```

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* i82365.c 1.352 - Linux driver for Intel 82365 and compatible
* PC Card controllers, and Yenta-compatible PCI-to-CardBus controllers.
* (C) 1999 David A. Hinds <dahinds@users.sourceforge.net>
*/
```

```
./drivers/pcmcia/marubun_pcmcia.c
marubun_pcmcia.c
```

```
/*
* Marubun MR-SHPC-01 PCMCIA controller device driver
*
* (c) 2007 Nobuhiro Iwamatsu <iwamatsu@nigauri.org>
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*/
```

```
./drivers/pcmcia/mpc8xx_pcmcia.c
mpc8xx_pcmcia.c
No license information found.
```

```
./drivers/pcmcia/rpx_pcmcia.c
rpx_pcmcia.c
```

```
/* ----- */
/* RPX Boards from Embedded Planet          */
/* ----- */
```

```
./drivers/pcmcia/ti_pci1410a.c
ti_pci1410a.c
```

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/*
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 *****
 *
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 *
 * m8xx_pcmcia.c - Linux PCMCIA socket driver for the mpc8xx series.
 * (C) 1999-2000 Magnus Damm <damm@bitsmart.com>
 *
 * "The ExCA standard specifies that socket controllers should provide
 * two IO and five memory windows per socket, which can be independently
 * configured and positioned in the host address space and mapped to
 * arbitrary segments of card address space. " - David A Hinds. 1999
 *
 * This controller does not meet the ExCA standard.
 *
 * m8xx pcmcia controller brief info:
 * + 8 windows (attrib, mem, i/o)
 * + up to two slots (SLOT_A and SLOT_B)
 * + inputpins, outputpins, event and mask registers.
 * - no offset register. sigh.
 *
```

```
* Because of the lacking offset register we must map the whole card.
* We assign each memory window PCMCIA_MEM_WIN_SIZE address space.
* Make sure there is (PCMCIA_MEM_WIN_SIZE * PCMCIA_MEM_WIN_NO
* * PCMCIA_SOCKETS_NO) bytes at PCMCIA_MEM_WIN_BASE.
* The i/o windows are dynamically allocated at PCMCIA_IO_WIN_BASE.
* They are maximum 64KByte each...
*/
```

```
./drivers/pcmcia/tqm8xx_pcmcia.c
```

```
tqm8xx_pcmcia.c
```

```
/* ----- */
/* TQM8xxL Boards by TQ Components */
/* SC8xx Boards by SinoVee Microsystems */
/* ----- */
```

```
./drivers/power/battery/bat_trats.c
```

```
bat_trats.c
```

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```
./drivers/power/ftpmu010.c
```

```
ftpmu010.c
```

```
/*
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 * Po-Yu Chuang <ratbert@faraday-tech.com>
 *
 * Copyright (C) 2010 Andes Technology Corporation
 * Shawn Lin, Andes Technology Corporation <nobuhiro@andestech.com>
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 */
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```

```
./drivers/power/fuel_gauge/fg_max17042.c
fg_max17042.c
```

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./drivers/power/pmic/muic_max8997.c
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```

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./drivers/power/pmic/pmic_max77686.c
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```

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./drivers/power/pmic/pmic_max8997.c
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./drivers/power/pmic/pmic_max8998.c
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```
./drivers/power/power_core.c
power_core.c
```

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./drivers/power/power_dialog.c
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```
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```

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./drivers/power/twl4030.c

twl4030.c

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 *
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 *
 * twl4030_power_init is from cpu/omap3/common.c, power_init_r
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 * Derived from Beagle Board and 3430 SDP code by
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./drivers/power/twl6030.c

twl6030.c

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./drivers/power/twl6035.c
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*/
```

```
./drivers/qe/fdt.c
```

```
fdt.c
```

```
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*
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```

```
./drivers/qe/qe.c
```

```
qe.c
```

```
/*  
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*  
* Dave Liu <daveliu@freescale.com>  
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```

```
./drivers/qe/qe.h
```

```
qe.h
```

```
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```

```
./drivers/qe/uccf.c
```

```
uccf.c
```

```
/*
```

```
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```

```
*
```

```
* Dave Liu <daveliu@freescale.com>
```

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```
*
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```
*/
```

```
./drivers/qe/uccf.h
```

```
uccf.h
```

```
/*
```

```
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```

```
*
```

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* Dave Liu <daveliu@freescale.com>
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*
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*/
```

```
./drivers/qe/uec.c
```

```
uec.c
```

```
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* Dave Liu <daveliu@freescale.com>
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*/
```

```
./drivers/qe/uec.h
```

```
uec.h
```

```
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```

```
./drivers/qe/uec_phy.c
```

```
uec_phy.c
```

```
/*
```

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```

```
*
```

```
* Author: Shlomi Gridish
```

```
*
```

```
* Description: UCC GETH Driver -- PHY handling
```

```
*             Driver for UEC on QE
```

```
*             Based on 8260_io/fcc_enet.c
```

```
*
```

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```
*
```

```
*/
```

```
./drivers/qe/uec_phy.h
```

```
uec_phy.h
```

```
/*
```

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*
```

```
* Author: Shlomi Gridish <gridish@freescale.com>
```

```
*
```

```
* Description: UCC ethernet driver -- PHY handling
```

```
*             Driver for UEC on QE
```

```
*             Based on 8260_io/fcc_enet.c
```

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```
*/
```

```
./drivers/rtc/at91sam9_rtt.c
```

```
at91sam9_rtt.c
```

```
/*
```

```
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```
./drivers/rtc/bfin_rtc.c
```

```
bfin_rtc.c
```

```
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```

```
./drivers/rtc/date.c
```

```
date.c
```

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```

```
./drivers/rtc/davinci.c
```

```
davinci.c
```

```
/*  
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```
./drivers/rtc/ds12887.c
```

```
ds12887.c
```

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```

```
./drivers/rtc/ds1302.c  
ds1302.c
```

```
/*  
* ds1302.c - Support for the Dallas Semiconductor DS1302 Timekeeping Chip  
*  
* Rex G. Feany <rfeany@zumanetworks.com>  
*  
*/
```

```
./drivers/rtc/ds1306.c  
ds1306.c
```

```
/*  
* (C) Copyright 2002 SIXNET, dge@sixnetio.com.  
*  
* (C) Copyright 2004, Li-Pro.Net <www.li-pro.net>  
* Stephan Linz <linz@li-pro.net>  
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```
./drivers/rtc/ds1307.c  
ds1307.c
```

```
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* Keith Outwater, keith_outwater@mvis.com`  
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```

```
./drivers/rtc/ds1337.c
ds1337.c
```

```
/*
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```

```
./drivers/rtc/ds1374.c
ds1374.c
```

```
/*
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* Steven Scholz, steven.scholz@imc-berlin.de
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```
./drivers/rtc/ds1556.c
ds1556.c
```

```
/*
* (C) Copyright 2002
* ARIIO Data Networks, Inc. dchiu@ariodata.com
*
* modified for DS1556:
* Frank Panno <fpanno@delphintech.com>, Delphin Technology AG
*
* Based on MontaVista DS1743 code and U-Boot mc146818 code
*
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```

./drivers/rtc/ds164x.c

ds164x.c

/*

* (C) Copyright 2002

* ARIO Data Networks, Inc. dchiu@ariodata.com

*

* modified for DS164x:

* The LEOX team <team@leox.org>, <http://www.leox.org>

*

* Based on MontaVista DS1743 code and U-Boot mc146818 code

*

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./drivers/rtc/ds174x.c

ds174x.c

/*

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* Based on MontaVista DS1743 code and U-Boot mc146818 code

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```

```
./drivers/rtc/ds3231.c
```

```
ds3231.c
```

```
/*
```

```
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* Markus Klotzbuecher, mk@denx.de
```

```
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```

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```
*/
```

```
./drivers/rtc/ftrtc010.c
```

```
ftrtc010.c
```

```
/*
```

```
* Faraday FTRTC010 Real Time Clock
```

```
*
```

```
* (C) Copyright 2009 Faraday Technology
* Po-Yu Chuang <ratbert@faraday-tech.com>
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*/
```

```
./drivers/rtc/imxdi.c
```

```
imxdi.c
```

```
/*
```

```
* (C) Copyright 2009-2012 ADVANSEE
* Benoît Thébaudeau <benoit.thebaudeau@advansee.com>
*
* Based on the Linux rtc-imxdi.c driver, which is:
* Copyright 2008-2009 Freescale Semiconductor, Inc. All Rights Reserved.
* Copyright 2010 Orex Computed Radiography
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*/
```

```
./drivers/rtc/isl1208.c
```

```
isl1208.c
```

```
/*
```

```
* (C) Copyright 2008
* Tor Krill, Excito Elektronik i Skåne , tor@excito.com
*
* Modelled after the ds1337 driver
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```

```
./drivers/rtc/m41t11.c
```

```
m41t11.c
```

```
/*
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*/
```

```
./drivers/rtc/m41t60.c
```

```
m41t60.c
```

```
/*
* (C) Copyright 2007
* Larry Johnson, lrj@acm.org
*
* based on rtc/m41t11.c which is ...
*
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* Andrew May, Viasat Inc, amay@viasat.com
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```
./drivers/rtc/m41t62.c
```

```
m41t62.c
```

```
/*
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*
* based on a the Linux rtc-m41t80.c driver which is:
* Alexander Bigga <ab@mycable.de>, 2006 (c) mycable GmbH
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*/
```

```
./drivers/rtc/m41t94.c
```

```
m41t94.c
```

```
/*
* Driver for ST M41T94 SPI RTC
*
* Taken from the Linux kernel driver:
* Copyright (C) 2008 Kim B. Heino
*
* Adaptation for U-Boot:
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* Albin Tonnerre, Free Electrons <albin.tonnerre@free-electrons.com>
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*/
```

```
./drivers/rtc/m48t35ax.c
```

m48t35ax.c

```
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 */
```

./drivers/rtc/max6900.c

max6900.c

```
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 */
```

./drivers/rtc/mc13xxx-rtc.c

mc13xxx-rtc.c

```
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 */
```

./drivers/rtc/mc146818.c

mc146818.c

```
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 */
```

./drivers/rtc/mcfrtc.c

mcfrtc.c

```
/*
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 * TsiChung Liew (Tsi-Chung.Liew@freescale.com)
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 */
```

```
./drivers/rtc/mk48t59.c
mk48t59.c
```

```
/*
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 * Andreas Heppel <aheppel@sysgo.de>
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 */
```

```
./drivers/rtc/mpc5xxx.c
mpc5xxx.c
```

```
/*
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 * r.meyer@emk-elektronik.de
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 */
```

```
./drivers/rtc/mpc8xx.c
mpc8xx.c
```

```
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 */
```

```
./drivers/rtc/mvrtc.c
```

```
mvrtc.c
```

```
/*
```

```
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```
* Jason Cooper <u-boot@lakedaemon.net>
```

```
*
```

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```
*/
```

```
./drivers/rtc/mvrtc.h
```

```
mvrtc.h
```

```
/*
```

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```
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```
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```

```
*/
```

```
./drivers/rtc/mx27rtc.c
```

```
mx27rtc.c
```

```
/*  
 * Freescale i.MX27 RTC Driver  
 *  
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 */
```

```
./drivers/rtc/mxsrtc.c
```

```
mxsrtc.c
```

```
/*  
 * Freescale i.MX28 RTC Driver  
 *  
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 */
```

```
./drivers/rtc/pcf8563.c
```

```
pcf8563.c
```

```
/*
```

```
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*/
```

```
./drivers/rtc/pl031.c
```

```
pl031.c
```

```
/*
```

```
* (C) Copyright 2008
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*
* reference linux-2.6.20.6/drivers/rtc/rtc-pl031.c
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*/
```

```
./drivers/rtc/pt7c4338.c
```

pt7c4338.c

```
/*
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 *
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 */
```

./drivers/rtc/rs5c372.c

rs5c372.c

```
/*
 * rs5c372.c
 *
 * Device driver for Ricoh's Real Time Controller RS5C372A.
 *
 * Copyright (C) 2004 Gary Jennejohn garyj@denx.de
 *
 * Based in part in ds1307.c -
 * (C) Copyright 2001, 2002, 2003
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 * Keith Outwater, keith_outwater@mvis.com`
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*/
```

```
./drivers/rtc/rtc4543.c
```

```
rtc4543.c
```

```
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*/
```

```
./drivers/rtc/rv3029.c
```

```
rv3029.c
```

```
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```

```
./drivers/rtc/rx8025.c
```

```
rx8025.c
```

```
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*/
```

```
./drivers/rtc/s3c24x0_rtc.c
```

```
s3c24x0_rtc.c
```

```
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./drivers/rtc/s3c44b0_rtc.c
s3c44b0_rtc.c

/*
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*/

./drivers/rtc/x1205.c
x1205.c

/*
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*
* based on a the Linux rtc-x1207.c driver which is:
* Copyright 2004 Karen Speare1
* Copyright 2005 Alessandro Zummo
*
* Information and datasheet:
* <http://www.intersil.com/cda/deviceinfo/0,1477,X1205,00.html>
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```
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```

```
./drivers/serial/altera_jtag_uart.c
```

```
altera_jtag_uart.c
```

```
/*  
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* Scott McNutt <smcnutt@psyent.com>  
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```

```
./drivers/serial/altera_uart.c
```

```
altera_uart.c
```

```
/*  
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./drivers/serial/arm_dcc.c
arm_dcc.c
/*
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./drivers/serial/atmel_usart.c
atmel_usart.c
/*
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*/
```

```
./drivers/serial/atmel_usart.h
atmel_usart.h
```

```
/*
* Register definitions for the Atmel USART3 module.
*
* Copyright (C) 2005-2006 Atmel Corporation
*
* Modified to support C structure SoC access by
* Andreas Bießmann <biessmann@corsscience.de>
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*/
```

```
./drivers/serial/lpc32xx_hsuart.c
lpc32xx_hsuart.c
```

```
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```

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* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
* MA 02110-1301, USA.
*/
```

```
./drivers/serial/mcfuart.c
```

```
mcfuart.c
```

```
/*
* (C) Copyright 2004-2007 Freescale Semiconductor, Inc.
* TsiChung Liew, Tsi-Chung.Liew@freescale.com.
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* MA 02111-1307 USA
*/
```

```
./drivers/serial/ns16550.c
```

```
ns16550.c
```

```
/*
* COM1 NS16550 support
* originally from linux source (arch/powerpc/boot/ns16550.c)
* modified to use CONFIG_SYS_ISA_MEM and new defines
*/
```

```
./drivers/serial/ns9750_serial.c
```

```
ns9750_serial.c
```

```
/******
```

```
*
* Copyright (C) 2004 by FS Forth-Systeme GmbH.
* All rights reserved.
*
* $Id: ns9750_serial.c,v 1.1 2004/02/16 10:37:20 mpietrek Exp $
* @Author: Markus Pietrek
* @Descr: Serial driver for the NS9750. Only one UART is supported yet.
* @References: [1] NS9750 Hardware Reference/December 2003
* @TODO: Implement Character GAP Timer when chip is fixed for PLL bypass
*
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*
*****/
```

```
./drivers/serial/opencores_yanu.c
```

```
opencores_yanu.c
```

```
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*/
```

```
./drivers/serial/s3c64xx.c
s3c64xx.c
```

```
/*
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
 */
```

```
./drivers/serial/sandbox.c
sandbox.c
```

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 * MA 02111-1307 USA
 */
```

```
./drivers/serial/serial.c
```

serial.c

```
/*
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 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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 * project.
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 * MA 02111-1307 USA
 */
```

./drivers/serial/serial_imx.c

serial_imx.c

```
/*
 * (c) 2004 Sascha Hauer <sascha@saschahauer.de>
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 */
```

./drivers/serial/serial_ixp.c

serial_ixp.c

```
/*
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 * Wolfgang Denk, DENX Software Engineering, <wd@denx.de>
```

```
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* Marius Groeger <mgroeger@sysgo.de>
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* Alex Zuepke <azu@sysgo.de>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*/
```

```
./drivers/serial/serial_ks8695.c
```

```
serial_ks8695.c
```

```
/*
```

```
* serial.c -- KS8695 serial driver
```

```
*
```

```
* (C) Copyright 2004, Greg Ungerer <greg.ungerer@opengear.com>
```

```
*
```

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```

```
*/
```

```
./drivers/serial/serial_max3100.c
```

serial_max3100.c

```
/*
 * (C) Copyright 2003
 *
 * Pantelis Antoniou <panto@intracom.gr>
 * Intracom S.A.
 *
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 */
```

./drivers/serial/serial_mxc.c

serial_mxc.c

```
/*
 * (c) 2007 Sascha Hauer <s.hauer@pengutronix.de>
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
 */
```

./drivers/serial/serial_ns16550.c

serial_ns16550.c

```
/*
```

```
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* MA 02111-1307 USA
*/
```

```
./drivers/serial/serial_pl01x.c
```

```
serial_pl01x.c
```

```
/*
* (C) Copyright 2000
* Rob Taylor, Flying Pig Systems. robt@flyingpig.com.
*
* (C) Copyright 2004
* ARM Ltd.
* Philippe Robin, <philippe.robin@arm.com>
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*/
```

```
./drivers/serial/serial_pl01x.h
serial_pl01x.h
/*
 * (C) Copyright 2003, 2004
 * ARM Ltd.
 * Philippe Robin, <philippe.robin@arm.com>
 *
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 * project.
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 */

./drivers/serial/serial_pxa.c
serial_pxa.c
/*
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 *
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*  
*/
```

```
./drivers/serial/serial_s3c24x0.c
```

```
serial_s3c24x0.c
```

```
/*  
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* Gary Jennejohn, DENX Software Engineering, <garyj@denx.de>  
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*  
*/
```

```
./drivers/serial/serial_s3c44b0.c
```

```
serial_s3c44b0.c
```

```
/*  
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* http://www.wawnet.biz  
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*/
```

```
./drivers/serial/serial_s5p.c
```

```
serial_s5p.c
```

```
/*
* (C) Copyright 2009 SAMSUNG Electronics
* Minkyu Kang <mk7.kang@samsung.com>
* Heungjun Kim <riverful.kim@samsung.com>
*
* based on drivers/serial/s3c64xx.c
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*
*/
```

```
./drivers/serial/serial_sa1100.c
```

```
serial_sa1100.c
```

```
/*
* (C) Copyright 2002
```

```
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*
*/
```

```
./drivers/serial/serial_sh.c
serial_sh.c
```

```
/*
* SuperH SCIF device driver.
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* Copyright (C) 2002 - 2008 Paul Mundt
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*/
```

```
./drivers/serial/serial_sh.h
```

```
serial_sh.h
```

```
/*
```

```
* Copy and modify from linux/drivers/serial/sh-sci.h
```

```
*/
```

```
./drivers/serial/serial_xuartlite.c
```

```
serial_xuartlite.c
```

```
/*
```

```
* (C) Copyright 2008-2011 Michal Simek <monstr@monstr.eu>
```

```
* Clean driver and add xilinx constant from header file
```

```
*
```

```
* (C) Copyright 2004 Atmark Techno, Inc.
```

```
* Yasushi SHOJI <yashi@atmark-techno.com>
```

```
*
```

```
* See file CREDITS for list of people who contributed to this
```

```
* project.
```

```
*
```

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```

```
*/
```

```
./drivers/serial/serial_zynq.c
```

```
serial_zynq.c
```

```
/*
```

```
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```

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```

```
*
```

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*/
```

```
./drivers/serial/usbttty.c
usbttty.c
```

```
/*
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* Gerry Hamel, geh@ti.com, Texas Instruments
*
* (C) Copyright 2006
* Bryan O'Donoghue, bodonoghue@codehermit.ie
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*/
```

```
./drivers/serial/usbttty.h
usbttty.h
```

```
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* Bryan O'Donoghue, bodonoghue@codehermit.ie, CodeHermit
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*
*/
```

```
./drivers/sound/samsung-i2s.c
samsung-i2s.c
```

```
/*
* Copyright (C) 2012 Samsung Electronics
* R. Chandrasekar <rcsekar@samsung.com>
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* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/sound/sound.c
sound.c
```

```
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* R. Chandrasekar <rcsekar@samsung.com>
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* MA 02111-1307 USA
*/
```

```
./drivers/sound/wm8994.c
wm8994.c
```

```
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* MA 02111-1307 USA
*/
```

```
./drivers/sound/wm8994.h
wm8994.h
```

```
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*/
```

```
./drivers/sound/wm8994_registers.h
wm8994_registers.h
```

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 * under the terms of the GNU General Public License as published by the
 * Free Software Foundation; either version 2 of the License, or (at your
 * option) any later version.
 *
 */
```

```
./drivers/spi/altera_spi.c
altera_spi.c
```

```
/*
 * Altera SPI driver
 *
 * based on bfin_spi.c
 * Copyright (c) 2005-2008 Analog Devices Inc.
 * Copyright (C) 2010 Thomas Chou <thomas@wytron.com.tw>
 *
 * Licensed under the GPL-2 or later.
 */
```

```
./drivers/spi/andes_spi.c
andes_spi.c
```

```
/*
 * Driver of Andes SPI Controller
 *
 * (C) Copyright 2011 Andes Technology
 * Macpaul Lin <macpaul@andestech.com>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/spi/andes_spi.h
```

```
andes_spi.h
```

```
/*
* Register definitions for the Andes SPI Controller
*
* (C) Copyright 2011 Andes Technology
* Macpaul Lin <macpaul@andestech.com>
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/spi/armada100_spi.c
```

```
armada100_spi.c
```

```
/*
* (C) Copyright 2011
* eInfochips Ltd. <www.einfochips.com>
* Written-by: Ajay Bhargav <ajay.bhargav@einfochips.com>
*
* (C) Copyright 2009
* Marvell Semiconductor <www.marvell.com>
* Based on SSP driver
* Written-by: Lei Wen <leiw@marvell.com>
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./drivers/spi/atmel_dataflash_spi.c
atmel_dataflash_spi.c
```

```
/*
* Driver for ATMEL DataFlash support
* Author : Hamid Ikdoumi (Atmel)
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
```

```
./drivers/spi/atmel_spi.c
atmel_spi.c
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/spi/atmel_spi.h
atmel_spi.h
```

```
/*
 * Register definitions for the Atmel AT32/AT91 SPI Controller
 */
/* Register offsets */
```

```
./drivers/spi/bfin_spi.c
bfin_spi.c
```

```
/*
 * Driver for Blackfin On-Chip SPI device
 *
 * Copyright (c) 2005-2010 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */
/*#define DEBUG*/
```

```
./drivers/spi/cadence_qspi.c
cadence_qspi.c
```

```
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 * Altera Corporation <www.altera.com>
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```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/spi/cadence_qspi.h
cadence_qspi.h
```

```
/*
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 *
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 * project.
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./drivers/spi/cadence_qspi_apb.c
cadence_qspi_apb.c
```

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* THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.
*/
```

```
./drivers/spi/cf_qspi.c
```

```
cf_qspi.c
```

```
/*
 * Freescale Coldfire Queued SPI driver
 *
 * NOTE:
 * This driver is written to transfer 8 bit at-a-time and uses the dedicated
 * SPI slave select pins as bit-banged GPIO to work with spi_flash subsystem.
 *
 * Copyright (C) 2011 Ruggedcom, Inc.
 * Richard Retanubun (richardretanubun@freescale.com)
 *
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 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./drivers/spi/cf_spi.c
```

```
cf_spi.c
```

```
/*
 *
 * (C) Copyright 2000-2003
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
 * Copyright (C) 2004-2009 Freescale Semiconductor, Inc.
 * TsiChung Liew (Tsi-Chung.Liew@freescale.com)
 *
 */
```

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*/

./drivers/spi/davinci_spi.c
davinci_spi.c
/*
* Copyright (C) 2009 Texas Instruments Incorporated - http://www.ti.com/
*
* Driver for SPI controller on DaVinci. Based on atmel_spi.c
* by Atmel Corporation
*
* Copyright (C) 2007 Atmel Corporation
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* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./drivers/spi/davinci_spi.h
davinci_spi.h
```

```
/*
 * Copyright (C) 2009 Texas Instruments Incorporated - http://www.ti.com/
 *
 * Register definitions for the DaVinci SPI Controller
 *
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 * MA 02111-1307 USA
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```

```
./drivers/spi/exynos_spi.c
```

```
exynos_spi.c
```

```
/*
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 * Padmavathi Venna <padma.v@samsung.com>
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
 */
```

```
./drivers/spi/fsl_espi.c
```

```
fsl_espi.c
```

```
/*
 * eSPI controller driver.
 *
 * Copyright 2010-2011 Freescale Semiconductor, Inc.
 * Author: Mingkai Hu (Mingkai.hu@freescale.com)
 */
```

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*/
```

```
./drivers/spi/kirkwood_spi.c
kirkwood_spi.c
```

```
/*
* (C) Copyright 2009
* Marvell Semiconductor <www.marvell.com>
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
*
* Derived from drivers/spi/mpc8xxx_spi.c
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* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./drivers/spi/mpc52xx_spi.c
mpc52xx_spi.c
```

```
/*
* (C) Copyright 2009
* Frank Bodammer <frank.bodammer@gcd-solutions.de>
* (C) Copyright 2009 Semihalf, Grzegorz Bernacki
```

```
*
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* MA 02111-1307 USA
*/

./drivers/spi/mpc8xxx_spi.c
mpc8xxx_spi.c
/*
* Copyright (c) 2006 Ben Warren, Qstreams Networks Inc.
* With help from the common/soft_spi and arch/powerpc/cpu/mpc8260 drivers
*
* See file CREDITS for list of people who contributed to this
* project.
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* MA 02111-1307 USA
*/

./drivers/spi/mxc_spi.c
mxc_spi.c
/*
* Copyright (C) 2008, Guennadi Liakhovetski <lg@denx.de>
*
```

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* MA 02111-1307 USA
*
*/
```

```
./drivers/spi/mxs_spi.c
```

```
mxs_spi.c
```

```
/*
 * Freescale i.MX28 SPI driver
 *
 * Copyright (C) 2011 Marek Vasut <marek.vasut@gmail.com>
 * on behalf of DENX Software Engineering GmbH
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 *
 * NOTE: This driver only supports the SPI-controller chipselects,
 *       GPIO driven chipselects are not supported.
 */
```

```
./drivers/spi/oc_tiny_spi.c
```

```
oc_tiny_spi.c
```

```
/*
 * Opencore tiny_spi driver
 *
 * http://opencores.org/project,tiny_spi
```

```
*
* based on bfin_spi.c
* Copyright (c) 2005-2008 Analog Devices Inc.
* Copyright (C) 2010 Thomas Chou <thomas@wytron.com.tw>
*
* Licensed under the GPL-2 or later.
*/
```

```
./drivers/spi/omap3_spi.c
```

```
omap3_spi.c
```

```
/*
* Copyright (C) 2010 Dirk Behme <dirk.behme@googlegmail.com>
*
* Driver for McSPI controller on OMAP3. Based on davinci_spi.c
* Copyright (C) 2009 Texas Instruments Incorporated - http://www.ti.com/
*
* Copyright (C) 2007 Atmel Corporation
*
* Parts taken from linux/drivers/spi/omap2_mcspi.c
* Copyright (C) 2005, 2006 Nokia Corporation
*
* Modified by Ruslan Araslanov <ruslan.araslanov@vitecmm.com>
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* MA 02111-1307 USA
*/
```

```
./drivers/spi/omap3_spi.h
```

```
omap3_spi.h
```

```
/*
* Register definitions for the OMAP3 McSPI Controller
*
* Copyright (C) 2010 Dirk Behme <dirk.behme@googlegmail.com>
*/
```

```
* Parts taken from linux/drivers/spi/omap2_mcspi.c
* Copyright (C) 2005, 2006 Nokia Corporation
*
* Modified by Ruslan Araslanov <ruslan.araslanov@vitecmm.com>
*
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* MA 02111-1307 USA
*/

./drivers/spi/sh_spi.c
sh_spi.c
/*
* SH SPI driver
*
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*
*/

./drivers/spi/sh_spi.h
sh_spi.h
/*
* SH SPI driver
```

```
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*
*/
```

```
./drivers/spi/soft_spi.c
```

```
soft_spi.c
```

```
/*
```

```
* (C) Copyright 2002
* Gerald Van Baren, Custom IDEAS, vanbaren@cideas.com.
```

```
* Influenced by code from:
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
```

```
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```

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```

```
*/
```

```
./drivers/spi/tegra_spi.c
```

```
tegra_spi.c
```

```
/*
```

```
* Copyright (c) 2010-2012 NVIDIA Corporation
```

```
* With help from the mpc8xxx SPI driver
* With more help from omap3_spi SPI driver
*
* See file CREDITS for list of people who contributed to this
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*/
```

```
./drivers/spi/xilinx_spi.c
```

```
xilinx_spi.c
```

```
/*
 * Xilinx SPI driver
 *
 * supports 8 bit SPI transfers only, with or w/o FIFO
 *
 * based on bfin_spi.c, by way of altera_spi.c
 * Copyright (c) 2005-2008 Analog Devices Inc.
 * Copyright (c) 2010 Thomas Chou <thomas@wytron.com.tw>
 * Copyright (c) 2010 Graeme Smecher <graeme.smecher@mail.mcgill.ca>
 * Copyright (c) 2012 Stephan Linz <linz@li-pro.net>
 *
 * Licensed under the GPL-2 or later.
 *
 * [0]: http://www.xilinx.com/support/documentation
 *
 * [S]: [0]/ip_documentation/xps_spi.pdf
 *       [0]/ip_documentation/axi_spi_ds742.pdf
 */
```

```
./drivers/spi/xilinx_spi.h
```

```
xilinx_spi.h
```

```
/*
 * Xilinx SPI driver
 *
 * XPS/AXI bus interface
 */
```

```
* based on bfin_spi.c, by way of altera_spi.c
* Copyright (c) 2005-2008 Analog Devices Inc.
* Copyright (c) 2010 Thomas Chou <thomas@wytron.com.tw>
* Copyright (c) 2010 Graeme Smecher <graeme.smecher@mail.mcgill.ca>
* Copyright (c) 2012 Stephan Linz <linz@li-pro.net>
*
* Licensed under the GPL-2 or later.
*
* [0]: http://www.xilinx.com/support/documentation
*
* [S]: [0]/ip_documentation/xps_spi.pdf
*       [0]/ip_documentation/axi_spi_ds742.pdf
*/
```

```
./drivers/tpm/generic_lpc_tpm.c
```

```
generic_lpc_tpm.c
```

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* MA 02111-1307 USA
*/
```

```
./drivers/twserial/soft_tws.c
```

```
soft_tws.c
```

```
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* Detlev Zundel, DENX Software Engineering, dzu@denx.de.
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*
*/
```

```
./drivers/usb/eth/asix.c
```

```
asix.c
```

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```

```
./drivers/usb/eth/smsc95xx.c
```

```
smsc95xx.c
```

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```

```
./drivers/usb/eth/usb_ether.c
```

```
usb_ether.c
```

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* MA 02111-1307 USA  
*/
```

```
./drivers/usb/gadget/composite.c
```

```
composite.c
```

```
/*  
* composite.c - infrastructure for Composite USB Gadgets  
*  
* Copyright (C) 2006-2008 David Brownell  
* U-boot porting: Lukasz Majewski <l.majewski@samsung.com>  
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*/
```

```
./drivers/usb/gadget/config.c
```

```
config.c
```

```
/*
 * usb/gadget/config.c -- simplify building config descriptors
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
 *
 * Ported to U-boot by: Thomas Smits <ts.smits@gmail.com> and
 *                      Remy Bohmer <linux@bohmer.net>
 */
```

```
./drivers/usb/gadget/core.c
```

```
core.c
```

```
/*
 * (C) Copyright 2003
 * Gerry Hamel, geh@ti.com, Texas Instruments
 *
 * Based on
 * linux/drivers/usbd/usbd.c.c - USB Device Core Layer
 *
 * Copyright (c) 2000, 2001, 2002 Lineo
 * Copyright (c) 2001 Hewlett Packard
 *
 * By:
 *   Stuart Lynne <sl@lineo.com>,
 *   Tom Rushworth <tbr@lineo.com>,
 *   Bruce Balden <balden@lineo.com>
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*
*/
```

```
./drivers/usb/gadget/designware_udc.c
designware_udc.c
```

```
/*
* Based on drivers/usb/gadget/omap1510_udc.c
* TI OMAP1510 USB bus interface driver
*
* (C) Copyright 2009
* Vipin Kumar, ST Micoelectronics, vipin.kumar@st.com.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/usb/gadget/ep0.c
ep0.c
```

```
/*
* (C) Copyright 2003
* Gerry Hamel, geh@ti.com, Texas Instruments
*
* (C) Copyright 2006
```

```
* Bryan O'Donoghue, deckard@CodeHermit.ie
*
* Based on
* linux/drivers/usbd/ep0.c
*
* Copyright (c) 2000, 2001, 2002 Lineo
* Copyright (c) 2001 Hewlett Packard
*
* By:
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*   Tom Rushworth <tbr@lineo.com>,
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*
*/
```

```
./drivers/usb/gadget/ep0.h
```

```
ep0.h
```

```
/*
* (C) Copyright 2003
* Gerry Hamel, geh@ti.com, Texas Instruments
*
* Based on
* linux/drivers/usbd/ep0.c
*
* Copyright (c) 2000, 2001, 2002 Lineo
* Copyright (c) 2001 Hewlett Packard
*
* By:
*   Stuart Lynne <sl@lineo.com>,
*   Tom Rushworth <tbr@lineo.com>,
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*
*/

./drivers/usb/gadget/epautoconf.c
epautoconf.c
/*
* epautoconf.c -- endpoint autoconfiguration for usb gadget drivers
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*
* Ported to U-boot by: Thomas Smits <ts.smits@gmail.com> and
* Remy Bohmer <linux@bohmer.net>
*/
```

```
./drivers/usb/gadget/ether.c
ether.c
/*
* ether.c -- Ethernet gadget driver, with CDC and non-CDC options
*
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* Copyright (C) 2003-2004 Robert Schwebel, Benedikt Spranger
* Copyright (C) 2008 Nokia Corporation
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*/
```

```
./drivers/usb/gadget/f_dfu.c
```

```
f_dfu.c
```

```
/*
* f_dfu.c -- Device Firmware Update USB function
*
* Copyright (C) 2012 Samsung Electronics
* authors: Andrzej Pietrasiewicz <andrzej.p@samsung.com>
*          Lukasz Majewski <l.majewski@samsung.com>
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*/
```

```
./drivers/usb/gadget/f_dfu.h
```

```
f_dfu.h
```

```
/*
* f_dfu.h -- Device Firmware Update gadget
*
* Copyright (C) 2011-2012 Samsung Electronics
* author: Andrzej Pietrasiewicz <andrzej.p@samsung.com>
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*/
```

```
./drivers/usb/gadget/gadget_chips.h
gadget_chips.h
```

```
/*
 * USB device controllers have lots of quirks. Use these macros in
 * gadget drivers or other code that needs to deal with them, and which
 * autoconfigures instead of using early binding to the hardware.
 *
 * This SHOULD eventually work like the ARM mach_is_*() stuff, driven by
 * some config file that gets updated as new hardware is supported.
 * (And avoiding all runtime comparisons in typical one-choice configs!)
 *
 * NOTE: some of these controller drivers may not be available yet.
 * Some are available on 2.4 kernels; several are available, but not
 * yet pushed in the 2.6 mainline tree.
 *
 * Ported to U-boot by: Thomas Smits <ts.smits@gmail.com> and
 * Remy Bohmer <linux@bohmer.net>
*/
```

```
./drivers/usb/gadget/g_dnl.c
g_dnl.c
```

```
/*
 * g_dnl.c -- USB Downloader Gadget
 *
 * Copyright (C) 2012 Samsung Electronics
 * Lukasz Majewski <l.majewski@samsung.com>
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*/
```

./drivers/usb/gadget/mpc8xx_udc.c

mpc8xx_udc.c

/*

* Copyright (C) 2006 by Bryan O'Donoghue, CodeHermit

* bodonoghue@CodeHermit.ie

*

* References

* DasUBoot/drivers/usb/gadget/omap1510_udc.c, for design and implementation ideas.

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*

*/

./drivers/usb/gadget/mv_udc.c

mv_udc.c

/*

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* Lei Wen <leiw@marvell.com>

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```
*
* Back ported to the 8xx platform (from the 8260 platform) by
* Murray.Jensen@cmst.csiro.au, 27-Jan-01.
*/
```

```
./drivers/usb/gadget/ndis.h
```

```
ndis.h
```

```
/*
 * ndis.h
 *
 * ntddndis.h modified by Benedikt Spranger <b.spranger@pengutronix.de>
 *
 * Thanks to the cygwin development team,
 * especially to Casper S. Hornstrup <chorns@users.sourceforge.net>
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 */
```

```
./drivers/usb/gadget/omap1510_udc.c
```

```
omap1510_udc.c
```

```
/*
 * (C) Copyright 2003
 * Gerry Hamel, geh@ti.com, Texas Instruments
 *
 * Based on
 * linux/drivers/usb/device/bi/omap.c
 * TI OMAP1510 USB bus interface driver
 *
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 *        source@mvista.com
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*
*/
```

```
./drivers/usb/gadget/pxa25x_udc.c
pxa25x_udc.c
```

```
/*
* Intel PXA25x and IXP4xx on-chip full speed USB device controllers
*
* Copyright (C) 2002 Intrinsyc, Inc. (Frank Becker)
* Copyright (C) 2003 Robert Schwebel, Pengutronix
* Copyright (C) 2003 Benedikt Spranger, Pengutronix
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*
* MODULE_AUTHOR("Frank Becker, Robert Schwebel, David Brownell");
*/
```

```
./drivers/usb/gadget/pxa25x_udc.h
pxa25x_udc.h
```

```
/*
* Intel PXA25x on-chip full speed USB device controller
*
* Copyright (C) 2003 Robert Schwebel <r.schwebel@pengutronix.de>, Pengutronix
* Copyright (C) 2003 David Brownell
* Copyright (C) 2012 Lukasz Dalek <luk0104@gmail.com>
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*
*/
```

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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA  
*/
```

```
./drivers/usb/gadget/pxa27x_udc.c
```

```
pxa27x_udc.c
```

```
/*
```

```
* PXA27x USB device driver for u-boot.
```

```
*
```

```
* Copyright (C) 2007 Rodolfo Giometti <giometti@linux.it>
```

```
* Copyright (C) 2007 Eurotech S.p.A. <info@eurotech.it>
```

```
* Copyright (C) 2008 Vivek Kotal <vivek.kotal@azingo.com>
```

```
*
```

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```

```
*
```

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```
*
```

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* MA 02111-1307 USA
```

```
*
```

```
*/
```

```
./drivers/usb/gadget/regs-otg.h
```

```
regs-otg.h
```

```
/* linux/arch/arm/plat-s3c/include/plat/regs-otg.h
```

```
*
```

```
* Copyright (C) 2004 Herbert Poetzl <herbert@13thfloor.at>
```

```
*
```

```
* Registers remapping:
```

```
* Lukasz Majewski <l.majewski@samsung.com>
```

```
*
```

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* published by the Free Software Foundation; either version 2 of  
* the License, or (at your option) any later version.
```

```
*/
```

./drivers/usb/gadget/rndis.c

rndis.c

```
/*
 * RNDIS MSG parser
 *
 * Authors:      Benedikt Spranger, Pengutronix
 *              Robert Schwebel, Pengutronix
 *
 *              This program is free software; you can redistribute it and/or
 *              modify it under the terms of the GNU General Public License
 *              version 2, as published by the Free Software Foundation.
 *
 *              This software was originally developed in conformance with
 *              Microsoft's Remote NDIS Specification License Agreement.
 *
 * 03/12/2004 Kai-Uwe Bloem <linux-development@auerswald.de>
 *              Fixed message length bug in init_response
 *
 * 03/25/2004 Kai-Uwe Bloem <linux-development@auerswald.de>
 *              Fixed rndis_rm_hdr length bug.
 *
 * Copyright (C) 2004 by David Brownell
 *              updates to merge with Linux 2.6, better match RNDIS spec
 */
```

./drivers/usb/gadget/rndis.h

rndis.h

```
/*
 * RNDIS      Definitions for Remote NDIS
 *
 * Authors:    Benedikt Spranger, Pengutronix
 *            Robert Schwebel, Pengutronix
 *
 *            This program is free software; you can redistribute it and/or
 *            modify it under the terms of the GNU General Public License
 *            version 2, as published by the Free Software Foundation.
 *
 *            This software was originally developed in conformance with
 *            Microsoft's Remote NDIS Specification License Agreement.
 */
```

./drivers/usb/gadget/s3c_udc_otg.c

s3c_udc_otg.c

```
/*
 * drivers/usb/gadget/s3c_udc_otg.c
 * Samsung S3C on-chip full/high speed USB OTG 2.0 device controllers
 *
 * Copyright (C) 2008 for Samsung Electronics
 */
```

```
* BSP Support for Samsung's UDC driver
* available at:
* git://git.kernel.org/pub/scm/linux/kernel/git/kki_ap/linux-2.6-samsung.git
*
* State machine bugfixes:
* Marek Szyprowski <m.szyprowski@samsung.com>
*
* Ported to u-boot:
* Marek Szyprowski <m.szyprowski@samsung.com>
* Lukasz Majewski <l.majewski@samsung.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*
*/
```

```
./drivers/usb/gadget/s3c_udc_otg_xfer_dma.c
```

```
s3c_udc_otg_xfer_dma.c
```

```
/*
```

```
* drivers/usb/gadget/s3c_udc_otg_xfer_dma.c
* Samsung S3C on-chip full/high speed USB OTG 2.0 device controllers
*
* Copyright (C) 2009 for Samsung Electronics
*
* BSP Support for Samsung's UDC driver
* available at:
* git://git.kernel.org/pub/scm/linux/kernel/git/kki_ap/linux-2.6-samsung.git
*
* State machine bugfixes:
* Marek Szyprowski <m.szyprowski@samsung.com>
*
* Ported to u-boot:
* Marek Szyprowski <m.szyprowski@samsung.com>
* Lukasz Majewski <l.majewski@samsung.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*
*/
```

```
./drivers/usb/gadget/usbstring.c
usbstring.c
```

```
/*
* Copyright (C) 2003 David Brownell
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* it under the terms of the GNU Lesser General Public License as published
* by the Free Software Foundation; either version 2.1 of the License, or
* (at your option) any later version.
*
* Ported to U-boot by: Thomas Smits <ts.smits@gmail.com> and
* Remy Bohmer <linux@bohmer.net>
*/
```

```
./drivers/usb/host/dwc2_otg-hcd.c
dwc2_otg-hcd.c
```

```
/*
* Copyright (C) 2012 Oleksandr Tymoshenko <gonzo at freebsd.org>
*
* SPDX-License-Identifier: GPL-2.0
*/
```

```
./drivers/usb/host/dwc2_otg.c
dwc2_otg.c
No license information found.
```

```
./drivers/usb/host/dwc2_otg.h
dwc2_otg.h
```

```
/*
* Copyright (C) 2012 Oleksandr Tymoshenko <gonzo at freebsd.org>
*
* SPDX-License-Identifier: GPL-2.0
*/
/* USB HUB CONSTANTS (not OHCI-specific; see hub.h, based on usb_ohci.h) */
/* destination of request */
```

```
./drivers/usb/host/dwc2_otg_core_if.h
dwc2_otg_core_if.h
```

```
/* =====
* $File: //dwh/usb_iip/dev/software/otg/linux/drivers/dwc_otg_core_if.h $
* $Revision: #4 $
* $Date: 2008/12/18 $
* $Change: 1155299 $
*
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* LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY
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* DAMAGE.
* ===== */
```

```
./drivers/usb/host/dwc2_otg_regs.h
dwc2_otg_regs.h
```

```
/* =====
* $File: //dwh/usb_iip/dev/software/otg/linux/drivers/dwc_otg_regs.h $
* $Revision: #76 $
* $Date: 2009/04/02 $
* $Change: 1224216 $
*
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* LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY
* OUT OF THE USE OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH
* DAMAGE.
* ===== */
```

```
./drivers/usb/host/ehci-armada100.c
ehci-armada100.c
```

```
/*
* (C) Copyright 2012
* eInfochips Ltd. <www.einfochips.com>
* Written-by: Ajay Bhargav <ajay.bhargav@einfochips.com>
*
* This driver is based on Kirkwood echi driver
* (C) Copyright 2009
* Marvell Semiconductor <www.marvell.com>
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
*
* See file CREDITS for list of people who contributed to this
* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./drivers/usb/host/ehci-atmel.c
```

ehci-atmel.c

```
/*
 * (C) Copyright 2012
 * Atmel Semiconductor <www.atmel.com>
 * Written-by: Bo Shen <voice.shen@atmel.com>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
 * MA 02110-1301 USA
 */
```

./drivers/usb/host/ehci-exynos.c

ehci-exynos.c

```
/*
 * SAMSUNG EXYNOS USB HOST EHCI Controller
 *
 * Copyright (C) 2012 Samsung Electronics Co.Ltd
 * Vivek Gautam <gautam.vivek@samsung.com>
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 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
 * MA 02110-1301 USA
 */
```

./drivers/usb/host/ehci-fsl.c

ehci-fsl.c

```
/*
 * (C) Copyright 2009, 2011 Freescale Semiconductor, Inc.
 *
 * (C) Copyright 2008, Excito Elektronik i Sk=E5ne AB
 *
 * Author: Tor Krill tor@excito.com
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./drivers/usb/host/ehci-hcd.c

ehci-hcd.c

```
/*-
 * Copyright (c) 2007-2008, Juniper Networks, Inc.
 * Copyright (c) 2008, Excito Elektronik i Skåne AB
 * Copyright (c) 2008, Michael Trimarchi <trimarchimichael@yahoo.it>
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./drivers/usb/host/ehci-ixp4xx.c

ehci-ixp4xx.c

```
/*
 * (C) Copyright 2008, Michael Trimarchi <trimarchimichael@yahoo.it>
 *
 * Author: Michael Trimarchi <trimarchimichael@yahoo.it>
 * This code is based on ehci freescale driver
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./drivers/usb/host/ehci-marvell.c

ehci-marvell.c

```
/*
 * (C) Copyright 2009
 * Marvell Semiconductor <www.marvell.com>
 * Written-by: Prafulla Wadaskar <prafulla@marvell.com>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
 * MA 02110-1301 USA
 */
```

./drivers/usb/host/ehci-mpc512x.c

ehci-mpc512x.c

```
/*
 * (C) Copyright 2010, Damien Dusha, <d.dusha@gmail.com>
 *
 * (C) Copyright 2009, Value Team S.p.A.
 * Francesco Rendine, <francesco.rendine@valueteam.com>
 *
 * (C) Copyright 2009 Freescale Semiconductor, Inc.
 *
 * (C) Copyright 2008, Excito Elektronik i Sk=E5ne AB
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./drivers/usb/host/ehci-mx5.c

ehci-mx5.c

```
/*
 * Copyright (c) 2009 Daniel Mack <daniel@caiaq.de>
 * Copyright (C) 2010 Freescale Semiconductor, Inc.
 *
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 */
```

./drivers/usb/host/ehci-mx6.c

ehci-mx6.c

```
/*
 * Copyright (c) 2009 Daniel Mack <daniel@caiaq.de>
```

```
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*/
```

```
./drivers/usb/host/ehci-mxc.c
```

```
ehci-mxc.c
```

```
/*
```

```
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* along with this program; if not, write to the Free Software Foundation,
* Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./drivers/usb/host/ehci-mxs.c
```

```
ehci-mxs.c
```

```
/*
```

```
* Freescale i.MX28 USB Host driver
*
* Copyright (C) 2011 Marek Vasut <marek.vasut@gmail.com>
* on behalf of DENX Software Engineering GmbH
*
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```

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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*/
```

```
./drivers/usb/host/ehci-omap.c
```

```
ehci-omap.c
```

```
/*
* (C) Copyright 2011 Ilya Yanok, Emcraft Systems
* (C) Copyright 2004-2008
* Texas Instruments, <www.ti.com>
*
* Derived from Beagle Board code by
* Sunil Kumar <sunilsaini05@gmail.com>
* Shashi Ranjan <shashiranjnca05@gmail.com>
*
* See file CREDITS for list of people who contributed to this
* project.
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*/
```

```
./drivers/usb/host/ehci-pci.c
```

```
ehci-pci.c
```

```
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* MA 02111-1307 USA
*/
```

```
./drivers/usb/host/ehci-ppc4xx.c
ehci-ppc4xx.c
```

```
/*
* (C) Copyright 2010, Chris Zhang <chris@seamicro.com>
*
* Author: Chris Zhang <chris@seamicro.com>
* This code is based on ehci freescale driver
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```

```
./drivers/usb/host/ehci-tegra.c
ehci-tegra.c
```

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```

```
./drivers/usb/host/ehci-vct.c
```

```
ehci-vct.c
```

```
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```

```
./drivers/usb/host/ehci.h
```

```
ehci.h
```

```
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*/
```

```
./drivers/usb/host/isp116x-hcd.c
isp116x-hcd.c
/*
 * ISP116x HCD (Host Controller Driver) for u-boot.
 *
 * Copyright (C) 2006-2007 Rodolfo Giometti <giometti@linux.it>
 * Copyright (C) 2006-2007 Eurotech S.p.A. <info@eurotech.it>
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 *
 * Derived in part from the SL811 HCD driver "u-boot/drivers/usb/sl811_usb.c"
 * (original copyright message follows):
 *
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 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
 * This code is based on linux driver for sl811hs chip, source at
 * drivers/usb/host/sl811.c:
 *
 * SL811 Host Controller Interface driver for USB.
 *
 * Copyright (c) 2003/06, Courage Co., Ltd.
 *
 * Based on:
 * 1.uhci.c by Linus Torvalds, Johannes Erdfelt, Randy Dunlap,
 * Georg Acher, Deti Fliegl, Thomas Sailer, Roman Weissgaerber,
 * Adam Richter, Gregory P. Smith;
 * 2.Original SL811 driver (hc_sl811.o) by Pei Liu <pbl@cypress.com>
 * 3.Rewritten as sl811.o by Yin Aihua <yinah:couragetech.com.cn>
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 * and in part from AU1x00 OHCI HCD driver "u-boot/arch/mips/cpu/au1x00_usb_ohci.c"
 * (original copyright message follows):
 *
 * URB OHCI HCD (Host Controller Driver) for USB on the AU1x00.
```

```
*
* (C) Copyright 2003
* Gary Jennejohn, DENX Software Engineering <garyj@denx.de>
*
* [[GNU/GPL disclaimer]]
*
* Note: Part of this code has been derived from linux
*/
```

```
./drivers/usb/host/isp116x.h
```

```
isp116x.h
```

```
/*
* ISP116x register declarations and HCD data structures
*
* Copyright (C) 2007 Rodolfo Giometti <giometti@linux.it>
* Copyright (C) 2007 Eurotech S.p.A. <info@eurotech.it>
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* MA 02111-1307 USA
*/
```

```
./drivers/usb/host/ohci-at91.c
```

```
ohci-at91.c
```

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*/
```

```
./drivers/usb/host/ohci-da8xx.c
```

```
ohci-da8xx.c
```

```
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```

```
./drivers/usb/host/ohci-hcd.c
```

```
ohci-hcd.c
```

```
/*
* URB OHCI HCD (Host Controller Driver) for USB on the AT91RM9200 and PCI bus.
*
* Interrupt support is added. Now, it has been tested
* on ULI1575 chip and works well with USB keyboard.
*
* (C) Copyright 2007
* Zhang Wei, Freescale Semiconductor, Inc. <wei.zhang@freescale.com>
*
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* Gary Jennejohn, DENX Software Engineering <garyj@denx.de>
*
* Note: Much of this code has been derived from Linux 2.4
* (C) Copyright 1999 Roman Weissgaerber <weissg@vienna.at>
* (C) Copyright 2000-2002 David Brownell
*
* Modified for the MP2USB by (C) Copyright 2005 Eric Benard
* ebenard@eukrea.com - based on s3c24x0's driver
*/
```

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* See file CREDITS for list of people who contributed to this
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* MA 02111-1307 USA
*
*/
/*
* IMPORTANT NOTES
* 1 - Read doc/README.generic_usb_ohci
* 2 - this driver is intended for use with USB Mass Storage Devices
*     (BBB) and USB keyboard. There is NO support for Isochronous pipes!
* 2 - when running on a PQFP208 AT91RM9200, define CONFIG_AT91C_PQFP_UHPBUG
*     to activate workaround for bug #41 or this driver will NOT work!
*/

./drivers/usb/host/ohci-s3c24xx.c
ohci-s3c24xx.c
/*
* URB OHCI HCD (Host Controller Driver) for USB on the S3C2400.
*
* (C) Copyright 2003
* Gary Jennejohn, DENX Software Engineering <garyj@denx.de>
*
* Note: Much of this code has been derived from Linux 2.4
* (C) Copyright 1999 Roman Weissgaerber <weissg@vienna.at>
* (C) Copyright 2000-2002 David Brownell
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* MA 02111-1307 USA
*
*/
/*
* IMPORTANT NOTES
* 1 - this driver is intended for use with USB Mass Storage Devices
* (BBB) ONLY. There is NO support for Interrupt or Isochronous pipes!
*/

./drivers/usb/host/ohci-s3c24xx.h
ohci-s3c24xx.h
/*
* URB OHCI HCD (Host Controller Driver) for USB.
*
* (C) Copyright 1999 Roman Weissgaerber <weissg@vienna.at>
* (C) Copyright 2000-2001 David Brownell <dbrownell@users.sourceforge.net>
*
* usb-ohci.h
*/
/* mapping of the OHCI CC status to error codes */

./drivers/usb/host/ohci.h
ohci.h
/*
* URB OHCI HCD (Host Controller Driver) for USB.
*
* (C) Copyright 1999 Roman Weissgaerber <weissg@vienna.at>
* (C) Copyright 2000-2001 David Brownell <dbrownell@users.sourceforge.net>
*
* usb-ohci.h
*/
/*
* e.g. PCI controllers need this
*/

./drivers/usb/host/r8a66597-hcd.c
r8a66597-hcd.c
/*
* R8A66597 HCD (Host Controller Driver) for u-boot
*
* Copyright (C) 2008 Yoshihiro Shimoda <shimoda.yoshihiro@renesas.com>
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*
*/
```

```
./drivers/usb/host/r8a66597.h
```

```
r8a66597.h
```

```
/*
 * R8A66597 HCD (Host Controller Driver) for u-boot
 *
 * Copyright (C) 2008 Yoshihiro Shimoda <shimoda.yoshihiro@renesas.com>
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*/
```

```
./drivers/usb/host/s3c64xx-hcd.c
```

```
s3c64xx-hcd.c
```

```
/*
 * URB OHCI HCD (Host Controller Driver) initialization for USB on the S3C64XX.
 *
 * Copyright (C) 2008,
 * Guennadi Liakhovetski, DENX Software Engineering <lg@denx.de>
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./drivers/usb/host/sl811-hcd.c
sl811-hcd.c
/*
* (C) Copyright 2004
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* This code is based on linux driver for sl811hs chip, source at
* drivers/usb/host/sl811.c:
*
* SL811 Host Controller Interface driver for USB.
*
* Copyright (c) 2003/06, Courage Co., Ltd.
*
* Based on:
* 1.uhci.c by Linus Torvalds, Johannes Erdfelt, Randy Dunlap,
* Georg Acher, Deti Fliegl, Thomas Sailer, Roman Weissgaerber,
* Adam Richter, Gregory P. Smith;
* 2.Original SL811 driver (hc_sl811.o) by Pei Liu <pbl@cypress.com>
* 3.Rewrited as sl811.o by Yin Aihua <yinah:couragetech.com.cn>
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```

*/

./drivers/usb/host/sl811.h
sl811.h
No license information found.

./drivers/usb/host/utmi-armada100.c
utmi-armada100.c

```
/*
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 * Written-by: Ajay Bhargav <ajay.bhargav@einfochips.com>
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 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
 * MA 02110-1301 USA
 */
```

./drivers/usb/musb/am35x.c
am35x.c

```
/*
 * am35x.c - TI's AM35x platform specific usb wrapper functions.
 *
 * Author: Ajay Kumar Gupta <ajay.gupta@ti.com>
 *
 * Based on drivers/usb/musb/da8xx.c
 *
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*/
```

```
./drivers/usb/musb/am35x.h
```

```
am35x.h
```

```
/*
* am35x.h - TI's AM35x platform specific usb wrapper definitions.
*
* Author: Ajay Kumar Gupta <ajay.gupta@ti.com>
*
* Based on drivers/usb/musb/da8xx.h
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*/
```

```
./drivers/usb/musb/blackfin_usb.c
```

```
blackfin_usb.c
```

```
/*
* Blackfin MUSB HCD (Host Controller Driver) for u-boot
*
* Copyright (c) 2008-2009 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/
```

```
./drivers/usb/musb/blackfin_usb.h
```

```
blackfin_usb.h
```

```
/*
```

```
* Blackfin MUSB HCD (Host Controller Driver) for u-boot
*
* Copyright (c) 2008-2009 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*/
```

```
./drivers/usb/musb/da8xx.c
da8xx.c
```

```
/*
 * da8xx.c - TI's DA8xx platform specific usb wrapper functions.
 *
 * Author: Ajay Kumar Gupta <ajay.gupta@ti.com>
 *
 * Based on drivers/usb/musb/davinci.c
 *
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*/
```

```
./drivers/usb/musb/davinci.c
davinci.c
```

```
/*
 * TI's Davinci platform specific USB wrapper functions.
 *
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*
* Author: Thomas Abraham t-abraham@ti.com, Texas Instruments
*/
```

```
./drivers/usb/musb/davinci.h
```

```
davinci.h
```

```
/*
 * TI's Davinci platform specific USB wrapper functions.
 *
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 * MA 02111-1307 USA
 *
 * Author: Thomas Abraham t-abraham@ti.com, Texas Instruments
*/
```

```
./drivers/usb/musb/musb_core.c
```

```
musb_core.c
```

```
/*
 * Mentor USB OTG Core functionality common for both Host and Device
 * functionality.
 *
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*
* Author: Thomas Abraham t-abraham@ti.com, Texas Instruments
*/
```

```
./drivers/usb/musb/musb_core.h
musb_core.h
```

```
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*/
```

```
./drivers/usb/musb/musb_debug.h
musb_debug.h
```

```
/*
* Copyright (c) 2009 Wind River Systems, Inc.
* Tom Rix <Tom.Rix@windriver.com>
*
*/
```

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* MA 02111-1307 USA
*/
```

```
./drivers/usb/musb/musb_hcd.c
musb_hcd.c
```

```
/*
 * Mentor USB OTG Core host controller driver.
 *
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 * MA 02111-1307 USA
 *
 * Author: Thomas Abraham t-abraham@ti.com, Texas Instruments
 */
```

```
./drivers/usb/musb/musb_hcd.h
musb_hcd.h
```

```
/*
 * Mentor USB OTG Core host controller driver.
 *
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*
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*/
```

```
./drivers/usb/musb/musb_udc.c
musb_udc.c
```

```
/*
 * Copyright (c) 2009 Wind River Systems, Inc.
 * Tom Rix <Tom.Rix@windriver.com>
 *
 * This file is a rewrite of the usb device part of
 * repository git.omapzoom.org/repo/u-boot.git, branch master,
 * file cpu/omap3/fastboot.c
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 *
 * -----
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 *
 * -----
 *
 * The details of connecting the device to the uboot usb device subsystem
 * came from the old omap3 repository www.sakoman.net/u-boot-omap3.git,
 * branch omap3-dev-usb, file drivers/usb/usbdcore_musb.c
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 * -----
 *
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 *
 * Based on
 * u-boot OMAP1510 USB drivers (drivers/usbdcore_omap1510.c)
 * twl4030 init based on linux (drivers/i2c/chips/twl4030_usb.c)
 */
```

```
* Author: Diego Dompe (diego.dompe@ridgerun.com)
*         Atin Malaviya (atin.malaviya@gmail.com)
*
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*/
```

```
./drivers/usb/musb/omap3.c
omap3.c
```

```
/*
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 * Tom Rix <Tom.Rix@windriver.com>
 *
 * This file is based on
 * repository git.gitorious.org/u-boot-omap3/mainline.git,
 * branch omap3-dev-usb, file drivers/usb/host/omap3530_usb.c
 *
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```

```
./drivers/usb/musb/omap3.h
```

```
omap3.h
```

```
/*
```

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```

```
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```

```
*
```

```
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```

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```
*/
```

```
./drivers/usb/musb-new/am35x.c
```

```
am35x.c
```

```
/*
```

```
* Texas Instruments AM35x "glue layer"
```

```
*
```

```
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```

```
*
```

```
* Based on the DA8xx "glue layer" code.
```

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```

```
*
```

```
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```

```
*
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```

```
*/
```

```
./drivers/usb/musb-new/linux-compat.h
linux-compat.h
No license information found.
```

```
./drivers/usb/musb-new/musb_core.c
musb_core.c
```

```
/*
* USB OTG driver core code
*
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```

```
*
```

```
*/
```

```
./drivers/usb/musb-new/musb_core.h
```

```
musb_core.h
```

```
/*
```

```
* MUSB OTG driver defines
```

```
*
```

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```
*
```

```
*/
```

```
./drivers/usb/musb-new/musb_debug.h
```

```
musb_debug.h
```

```
/*
```

```
* MUSB OTG driver debug defines
```

```
*
```

```
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*
*/
```

```
./drivers/usb/musb-new/musb_dma.h
```

```
musb_dma.h
```

```
/*
 * MUSB OTG driver DMA controller abstraction
 *
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*/
```

```
./drivers/usb/musb-new/musb_dsps.c
musb_dsps.c
```

```
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 * Texas Instruments DSPS platforms "glue layer"
 *
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 *
 * Based on the am35x "glue layer" code.
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 *
 * musb_dsps.c will be a common file for all the TI DSPS platforms
 * such as dm64x, dm36x, dm35x, da8x, am35x and ti81x.
 * For now only ti81x is using this and in future davinci.c, am35x.c
 * da8xx.c would be merged to this file after testing.
 */
```

```
./drivers/usb/musb-new/musb_gadget.c
```

musb_gadget.c

```
/*
 * MUSB OTG driver peripheral support
 *
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 */
```

./drivers/usb/musb-new/musb_gadget.h

musb_gadget.h

```
/*
 * MUSB OTG driver peripheral defines
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*
*/
```

```
./drivers/usb/musb-new/musb_gadget_ep0.c
```

```
musb_gadget_ep0.c
```

```
/*
 * USB OTG peripheral driver ep0 handling
 *
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```
*/
```

```
./drivers/usb/musb-new/musb_host.c
musb_host.c
```

```
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 * MUSB OTG driver host support
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 */
```

```
./drivers/usb/musb-new/musb_host.h
musb_host.h
```

```
/*
 * MUSB OTG driver host defines
```

```
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*/
```

```
./drivers/usb/musb-new/musb_io.h
```

```
musb_io.h
```

```
/*
```

```
* MUSB OTG driver register I/O
```

```
*
```

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*/
```

```
./drivers/usb/musb-new/musb_regs.h
musb_regs.h
```

```
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 * MUSB OTG driver register defines
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*/
```

```
./drivers/usb/musb-new/musb_uboot.c  
musb_uboot.c  
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```

```
./drivers/usb/musb-new/omap2430.c  
omap2430.c
```

```
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 * Copyrights for that are attributable to:  
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 * Tony Lindgren <tony@atomide.com>  
 *  
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 * Suite 330, Boston, MA 02111-1307 USA  
 *  
 */
```

```
./drivers/usb/musb-new/omap2430.h  
omap2430.h
```

```
/*  
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 *  
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 */
```

```
./drivers/usb/musb-new/usb-compat.h  
usb-compat.h
```

No license information found.

./drivers/usb/phy/twl4030.c
twl4030.c

```
/*
 * Copyright (c) 2009 Wind River Systems, Inc.
 * Tom Rix <Tom.Rix@windriver.com>
 *
 * This file is based on
 * repository git.gitorious.org/u-boot-omap3/mainline.git,
 * branch omap3-dev-usb, file drivers/usb/gadget/twl4030_usb.c
 *
 * This is the unique part of its copyright :
 *
 * -----
 * * (C) Copyright 2009 Atin Malaviya (atin.malaviya@gmail.com)
 *
 * Based on: twl4030_usb.c in linux 2.6 (drivers/i2c/chips/twl4030_usb.c)
 * Copyright (C) 2004-2007 Texas Instruments
 * Copyright (C) 2008 Nokia Corporation
 * Contact: Felipe Balbi <felipe.balbi@nokia.com>
 *
 * Author: Atin Malaviya (atin.malaviya@gmail.com)
 *
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 * MA 02111-1307 USA
 */
```

./drivers/usb/ulpi/omap-ulpi-viewport.c
omap-ulpi-viewport.c

```
/*
 * OMAP ulpi viewport support
 * Based on drivers/usb/ulpi/ulpi-viewport.c
 *
 * Copyright (C) 2011 Texas Instruments Incorporated - http://www.ti.com
```

```
* Author: Govindraj R <govindraj.raja@ti.com>
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*/
```

```
./drivers/usb/ulpi/ulpi-viewport.c
ulpi-viewport.c
```

```
/*
 * Copyright (C) 2011 Jana Rapava <fermata7@gmail.com>
 * Copyright (C) 2011 CompuLab, Ltd. <www.compulab.co.il>
 *
 * Authors: Jana Rapava <fermata7@gmail.com>
 *          Igor Grinberg <grinberg@compulab.co.il>
 *
 * Based on:
 * linux/drivers/usb/otg/ulpi_viewport.c
 *
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 * Copyright (C) 2011 Google, Inc.
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 */
```

```
./drivers/usb/ulpi/ulpi.c
ulpi.c
```

```
/*
 * Copyright (C) 2011 Jana Rapava <fermata7@gmail.com>
 * Copyright (C) 2011 CompuLab, Ltd. <www.compulab.co.il>
 *
 * Authors: Jana Rapava <fermata7@gmail.com>
 *          Igor Grinberg <grinberg@compulab.co.il>
 */
```

```
* Based on:
* linux/drivers/usb/otg/ulpi.c
* Generic ULPI USB transceiver support
*
* Original Copyright follow:
* Copyright (C) 2009 Daniel Mack <daniel@caiaq.de>
*
* Based on sources from
*
* Sascha Hauer <s.hauer@pengutronix.de>
* Freescale Semiconductors
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*/
```

```
./drivers/video/amba.c
```

```
amba.c
```

```
/*
* Driver for AMBA PrimeCell CLCD
*
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* project.
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* MA 02111-1307 USA
*/
```

```
./drivers/video/ati_ids.h
```

ati_ids.h

```
/*
 * ATI PCI IDs from XFree86, kept here to make sync'ing with
 * XFree much simpler. Currently, this list is only used by
 * radeonfb
 */
```

./drivers/video/ati_radeon_fb.c

ati_radeon_fb.c

```
/*
 * ATI Radeon Video card Framebuffer driver.
 *
 * Copyright 2007 Freescale Semiconductor, Inc.
 * Zhang Wei <wei.zhang@freescale.com>
 * Jason Jin <jason.jin@freescale.com>
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 *
 * Some codes of this file is partly ported from Linux kernel
 * ATI video framebuffer driver.
 *
 * Now the driver is tested on below ATI chips:
 * 9200
 * X300
 * X700
 */
```

./drivers/video/ati_radeon_fb.h

ati_radeon_fb.h

```
/*
 * Most of the definitions here are adapted right from XFree86 *
 *
 */
```

* Chip families. Must fit in the low 16 bits of a long word
*/

./drivers/video/atmel_hlcdfb.c
atmel_hlcdfb.c

```
/*
 * Driver for AT91/AT32 MULTI LAYER LCD Controller
 *
 * Copyright (C) 2012 Atmel Corporation
 *
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 * project.
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 * MA 02111-1307 USA
 */
```

./drivers/video/atmel_lcdfb.c
atmel_lcdfb.c

```
/*
 * Driver for AT91/AT32 LCD Controller
 *
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 *
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*/
```

```
./drivers/video/bus_vcxc.c
```

```
bus_vcxc.c
```

```
/*
```

```
* (C) Copyright 2005-2009
```

```
* Jens Scharsig @ BuS Elektronik GmbH & Co. KG, <esw@bus-elektronik.de>
```

```
*
```

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* See file CREDITS for list of people who contributed to this
```

```
* project.
```

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*
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```

```
*/
```

```
./drivers/video/cfb_console.c
```

```
cfb_console.c
```

```
/*
```

```
* (C) Copyright 2002 ELTEC Elektronik AG
```

```
* Frank Gottschling <fgottschling@eltec.de>
```

```
*
```

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* See file CREDITS for list of people who contributed to this
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```
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*/
```

```
./drivers/video/coreboot_fb.c
```

```
coreboot_fb.c
```

```
/*
 * coreboot framebuffer driver.
 *
 * Copyright (C) 2011 The Chromium OS authors
 *
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 * project.
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 * MA 02111-1307 USA
*/
```

```
./drivers/video/ct69000.c
```

```
ct69000.c
```

```
/* ported from ctfb.c (linux kernel):
 * Created in Jan - July 2000 by Thomas Höhenleitner <th@visuelle-maschinen.de>
 *
 * Ported to U-Boot:
 * (C) Copyright 2002 Denis Peter, MPL AG Switzerland
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./drivers/video/da8xx-fb.c
```

```
da8xx-fb.c
```

```
/*
* Porting to u-boot:
*
* (C) Copyright 2011
* Stefano Babic, DENX Software Engineering, sbabic@denx.de.
*
* Copyright (C) 2008-2009 MontaVista Software Inc.
* Copyright (C) 2008-2009 Texas Instruments Inc
*
* Based on the LCD driver for TI Avalanche processors written by
* Ajay Singh and Shalom Hai.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*/
```

```
./drivers/video/exynos_dp.c
```

```
exynos_dp.c
```

```
/*
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*
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*/
```

```
./drivers/video/exynos_dp_lowlevel.c
exynos_dp_lowlevel.c
```

```
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```

```
./drivers/video/exynos_dp_lowlevel.h
exynos_dp_lowlevel.h
```

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*/
```

```
./drivers/video/exynos_fb.c
```

```
exynos_fb.c
```

```
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*/
```

```
./drivers/video/exynos_fb.h
```

```
exynos_fb.h
```

```
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```

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*/
```

```
./drivers/video/exynos_fimd.c  
exynos_fimd.c
```

```
/*  
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*/
```

```
./drivers/video/exynos_mipi_dsi.c  
exynos_mipi_dsi.c
```

```
/*  
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```

```
*/  
  
./drivers/video/exynos_mipi_dsi_common.c  
exynos_mipi_dsi_common.c  
/*  
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```

```
./drivers/video/exynos_mipi_dsi_common.h  
exynos_mipi_dsi_common.h  
/*  
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*/
```

```
./drivers/video/exynos_mipi_dsi_lowlevel.c
exynos_mipi_dsi_lowlevel.c
```

```
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 */
```

```
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exynos_mipi_dsi_lowlevel.h
```

```
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 */
```

```
./drivers/video/exynos_pwm_bl.c
exynos_pwm_bl.c
```

```
/*
 * PWM BACKLIGHT driver for Board based on EXYNOS.
 *
 * Author: Donghwa Lee <dh09.lee@samsung.com>
 *
 * Derived from linux/drivers/video/backlight/pwm_backlight.c
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 */
```

```
./drivers/video/fsl_diu_fb.c
```

```
fsl_diu_fb.c
```

```
/*
 * Copyright 2007, 2010-2011 Freescale Semiconductor, Inc.
 * Authors: York Sun <yorksun@freescale.com>
 *          Timur Tabi <timur@freescale.com>
 *
 * FSL DIU Framebuffer driver
 *
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 */
```

```
./drivers/video/ipu.h
ipu.h
/*
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 *
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 */
```

```
./drivers/video/ipu_common.c
ipu_common.c
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*/
```

```
./drivers/video/ipu_disp.c
ipu_disp.c
```

```
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*/
```

```
./drivers/video/ipu_regs.h
ipu_regs.h
```

```
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*/
```

```
./drivers/video/ld9040.c
```

```
ld9040.c
```

```
/*
* ld9040 AMOLED LCD panel driver.
*
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* Donghwa Lee <dh09.lee@samsung.com>
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*/
```

./drivers/video/mb862xx.c

mb862xx.c

/*

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*/

./drivers/video/mb86r0xgdc.c

mb86r0xgdc.c

/*

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* Matthias Weisser <weisserm@arcor.de>

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*/

./drivers/video/mx3fb.c

mx3fb.c

```
/*
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 */
```

./drivers/video/mxcfb.h

mxcfb.h

```
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*/
```

```
./drivers/video/mxc_ipuv3_fb.c
mxc_ipuv3_fb.c
```

```
/*
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*/
```

```
./drivers/video/omap3_dss.c
omap3_dss.c
```

```
/*
* (C) Copyright 2010
* Texas Instruments, <www.ti.com>
* Syed Mohammed Khasim <khasim@ti.com>
*
* Referred to Linux Kernel DSS driver files for OMAP3 by
* Tomi Valkeinen from drivers/video/omap2/dss/
*
*/
```

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```

```
./drivers/video/s6e63d6.c
```

```
s6e63d6.c
```

```
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```

```
./drivers/video/s6e8ax0.c
```

```
s6e8ax0.c
```

```
/*
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*/
```

```
./drivers/video/sed13806.c
sed13806.c
```

```
/*
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* Pierre AUBERT p.aubert@staubli.com
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*/
```

```
/* Video support for Epson SED13806 chipset
```

```
*/
```

```
./drivers/video/sed156x.c
sed156x.c
```

```
/*
* (C) Copyright 2003
*
* Pantelis Antoniou <panto@intracom.gr>
```

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```

```
./drivers/video/sm501.c
```

```
sm501.c
```

```
/*
```

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```

./drivers/video/smilynxEM.c

smilynxEM.c

```
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```

./drivers/video/tegra.c

tegra.c

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```

./drivers/video/videomodes.c

videomodes.c

```
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```

```
./drivers/video/videomodes.h
videomodes.h
```

```
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```
./drivers/watchdog/at91sam9_wdt.c
```

at91sam9_wdt.c

```
/*
 * [origin: Linux kernel drivers/watchdog/at91sam9_wdt.c]
 *
 * Watchdog driver for Atmel AT91SAM9x processors.
 *
 * Copyright (C) 2008 Jean-Christophe PLAGNIOL-VILLARD <plagnioj@jcrosoft.com>
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 */
```

./drivers/watchdog/designware_wdt.c

designware_wdt.c

```
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```

./drivers/watchdog/ftwtdt010_wdt.c

ftwtdt010_wdt.c

```
/*
 * Watchdog driver for the FTWDT010 Watch Dog Driver
 *
 * (c) Copyright 2004 Faraday Technology Corp. (www.faraday-tech.com)
 * Based on sa1100_wdt.c by Oleg Drokin <green@crimea.edu>
 * Based on SoftDog driver by Alan Cox <alan@redhat.com>
 *
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 * Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
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*
* 27/11/2004 Initial release, Faraday.
* 12/01/2011 Port to u-boot, Macpaul Lin.
*/
```

```
./drivers/watchdog/imx_watchdog.c
```

```
imx_watchdog.c
```

```
/*
* watchdog.c - driver for i.mx on-chip watchdog
*
* Licensed under the GPL-2 or later.
*/
```

```
./drivers/watchdog/s5p_wdt.c
```

```
s5p_wdt.c
```

```
/*
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```

```
*/  
  
./drivers/watchdog/tnetv107x_wdt.c  
tnetv107x_wdt.c  
/*  
* TNETV107X: Watchdog timer implementation (for reset)  
*  
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*/
```

```
./examples/api/demo.c  
demo.c  
/*  
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*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*  
*/
```

*/

./examples/api/glue.c

glue.c

/*

* (C) Copyright 2007-2008 Semihalf, Rafal Jaworowski <raj@semihalf.com>

*

* See file CREDITS for list of people who contributed to this
* project.

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* MA 02111-1307 USA

*

*/

./examples/api/glue.h

glue.h

/*

* (C) Copyright 2007 Semihalf

*

* Written by: Rafal Jaworowski <raj@semihalf.com>

*

* See file CREDITS for list of people who contributed to this
* project.

*

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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,

```
* MA 02111-1307 USA
*
*/

./examples/api/libgenwrap.c
libgenwrap.c
/*
* (C) Copyright 2007 Semihalf
*
* Written by: Rafal Jaworowski <raj@semihalf.com>
*
* See file CREDITS for list of people who contributed to this
* project.
*
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*
* This is is a set of wrappers/stubs that allow to use certain routines from
* U-Boot's lib in the standalone app. This way way we can re-use
* existing code e.g. operations on strings and similar.
*
*/

./examples/standalone/82559_eeprom.c
82559_eeprom.c
/*
* Copyright 1998-2001 by Donald Becker.
* This software may be used and distributed according to the terms of
* the GNU General Public License (GPL), incorporated herein by reference.
* Contact the author for use under other terms.
*
* This program must be compiled with "-O"!
* See the bottom of this file for the suggested compile-command.
*
* The author may be reached as becker@scyld.com, or C/O
* Scyld Computing Corporation
* 410 Severn Ave., Suite 210
```

```
* Annapolis MD 21403
*
* Common-sense licensing statement: Using any portion of this program in
* your own program means that you must give credit to the original author
* and release the resulting code under the GPL.
*/
```

```
./examples/standalone/atmel_df_pow2.c
atmel_df_pow2.c
```

```
/*
* atmel_df_pow2.c - convert Atmel Dataflashes to Power of 2 mode
*
* Copyright 2009 Analog Devices Inc.
*
* Licensed under the 2-clause BSD.
*/
```

```
./examples/standalone/eeepro100_eeeprom.c
eeepro100_eeeprom.c
```

```
/*
* Copyright 1998-2001 by Donald Becker.
* This software may be used and distributed according to the terms of
* the GNU General Public License (GPL), incorporated herein by reference.
* Contact the author for use under other terms.
*
* This program must be compiled with "-O"!
* See the bottom of this file for the suggested compile-command.
*
* The author may be reached as becker@scyld.com, or C/O
* Scyld Computing Corporation
* 410 Severn Ave., Suite 210
* Annapolis MD 21403
*
* Common-sense licensing statement: Using any portion of this program in
* your own program means that you must give credit to the original author
* and release the resulting code under the GPL.
*/
```

```
./examples/standalone/hello_world.c
hello_world.c
```

```
/*
* (C) Copyright 2000
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* See file CREDITS for list of people who contributed to this
* project.
*
* This program is free software; you can redistribute it and/or
* modify it under the terms of the GNU General Public License as
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```

```
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./examples/standalone/interrupt.c
```

```
interrupt.c
```

```
/*
* (C) Copyright 2006
* Detlev Zundel, DENX Software Engineering, dzu@denx.de.
*
* See file CREDITS for list of people who contributed to this
* project.
*
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*
* You should have received a copy of the GNU General Public License
* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
* This is a very simple standalone application demonstrating
* catching IRQs on the MPC52xx architecture.
*
* The interrupt to be intercepted can be specified as an argument
* to the application. Specifying nothing will intercept IRQ1 on the
* MPC5200 platform. On the CR825 carrier board from MicroSys this
* maps to the ABORT switch :)
*
* Note that the specified vector is only a logical number specified
* by the respective header file.
*/
```

```
./examples/standalone/mem_to_mem_idma2intr.c
```

mem_to_mem_idma2intr.c

```
/* The dpalloc function used and implemented in this file was derieved
 * from PPCBoot/U-Boot file "arch/powerpc/cpu/mpc8260/commproc.c".
 */
/* Author: Arun Dharankar <ADharankar@ATTBI.Com>
 * This example is meant to only demonstrate how the IDMA could be used.
 */
/*
 * This file is based on "arch/powerpc/8260_io/commproc.c" - here is it's
 * copyright notice:
 *
 * General Purpose functions for the global management of the
 * 8260 Communication Processor Module.
 * Copyright (c) 1999 Dan Malek (dmalek@jlc.net)
 * Copyright (c) 2000 MontaVista Software, Inc (source@mvista.com)
 * 2.3.99 Updates
 *
 * In addition to the individual control of the communication
 * channels, there are a few functions that globally affect the
 * communication processor.
 *
 * Buffer descriptors must be allocated from the dual ported memory
 * space. The allocator for that is here. When the communication
 * process is reset, we reclaim the memory available. There is
 * currently no deallocator for this memory.
 */
```

./examples/standalone/sched.c

sched.c

```
/*
 * This program is free software; you can redistribute it and/or
 * modify it under the terms of the GNU General Public License as
 * published by the Free Software Foundation; either version 2 of
 * the License, or (at your option) any later version.
 *
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 * but WITHOUT ANY WARRANTY; without even the implied warranty of
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 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./examples/standalone/smc91111_eeprom.c

smc91111_eeprom.c

```
/*
 * (C) Copyright 2004
```

```
* Robin Getz rgetz@blacfin.uclinux.org
*
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* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
* Heavily borrowed from the following peoples GPL'ed software:
* - Wolfgang Denk, DENX Software Engineering, wd@denx.de
*   Das U-boot
* - Ladislav Michl ladis@linux-mips.org
*   A rejected patch on the U-Boot mailing list
*/
```

```
./examples/standalone/smc911x_eeprom.c
```

```
smc911x_eeprom.c
```

```
/*
* smc911x_eeprom.c - EEPROM interface to SMC911x parts.
* Only tested on SMSC9118 though ...
*
* Copyright 2004-2009 Analog Devices Inc.
*
* Licensed under the GPL-2 or later.
*
* Based on smc91111_eeprom.c which:
* Heavily borrowed from the following peoples GPL'ed software:
* - Wolfgang Denk, DENX Software Engineering, wd@denx.de
*   Das U-boot
* - Ladislav Michl ladis@linux-mips.org
*   A rejected patch on the U-Boot mailing list
*/
```

```
./examples/standalone/stubs.c
```

```
stubs.c
```

```
/*
* x86 does not have a dedicated register to store the pointer to
* the global_data. Thus the jump table address is stored in a
```

```
* global variable, but such approach does not allow for execution
* from flash memory. The global_data address is passed as argv[-1]
* to the application program.
*/
```

```
./examples/standalone/test_burst.c
```

```
test_burst.c
```

```
/*
 * (C) Copyright 2005
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 *
 * The test exercises SDRAM accesses in burst mode
 */
```

```
./examples/standalone/test_burst.h
```

```
test_burst.h
```

```
/*
 * (C) Copyright 2005
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
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 */
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./examples/standalone/timer.c
```

```
timer.c
```

```
/*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./examples/standalone/x86-testapp.c
```

```
x86-testapp.c
```

```
No license information found.
```

```
./fs/cbfs/cbfs.c
```

```
cbfs.c
```

```
/*
* Copyright (c) 2011 The Chromium OS Authors. All rights reserved.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./fs/cramfs/cramfs.c
cramfs.c
/*
* cramfs.c
*
* Copyright (C) 1999 Linus Torvalds
*
* Copyright (C) 2000-2002 Transmeta Corporation
*
* Copyright (C) 2003 Kai-Uwe Bloem,
* Auerswald GmbH & Co KG, <linux-development@auerswald.de>
* - adapted from the www.tuxbox.org u-boot tree, added "ls" command
*
* This program is free software; you can redistribute it and/or modify
* it under the terms of the GNU General Public License (Version 2) as
* published by the Free Software Foundation.
*
* Compressed ROM filesystem for Linux.
*
* TODO:
* add support for resolving symbolic links
*/

./fs/cramfs/uncompress.c
uncompress.c
/*
* uncompress.c
*
* Copyright (C) 1999 Linus Torvalds
* Copyright (C) 2000-2002 Transmeta Corporation
*
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* it under the terms of the GNU General Public License (Version 2) as
* published by the Free Software Foundation.
*
* cramfs interfaces to the uncompression library. There's really just
* three entrypoints:
*
* - cramfs_uncompress_init() - called to initialize the thing.
* - cramfs_uncompress_exit() - tell me when you're done
* - cramfs_uncompress_block() - uncompress a block.
*
* NOTE NOTE NOTE! The uncompression is entirely single-threaded. We
```

```
* only have one stream, and we'll initialize it only once even if it
* then is used by multiple filesystems.
*/
```

```
./fs/ext4/crc16.c
```

```
crc16.c
```

```
/*
 *      crc16.c
 *
 * This source code is licensed under the GNU General Public License,
 * Version 2. See the file COPYING for more details.
 */
/** CRC table for the CRC-16. The poly is 0x8005 (x16 + x15 + x2 + 1) */
```

```
./fs/ext4/crc16.h
```

```
crc16.h
```

```
/*
 * crc16.h - CRC-16 routine
 * Implements the standard CRC-16:
 * Width 16
 * Poly 0x8005 (x16 + x15 + x2 + 1)
 * Init 0
 *
 * Copyright (c) 2005 Ben Gardner <bgardner@wabtec.com>
 * This source code is licensed under the GNU General Public License,
 * Version 2. See the file COPYING for more details.
 */
```

```
./fs/ext4/dev.c
```

```
dev.c
```

```
/*
 * (C) Copyright 2011 - 2012 Samsung Electronics
 * EXT4 filesystem implementation in Uboot by
 * Uma Shankar <uma.shankar@samsung.com>
 * Manjunatha C Achar <a.manjunatha@samsung.com>
 *
 * made from existing ext2/dev.c file of Uboot
 * (C) Copyright 2004
 * esd gmbh <www.esd-electronics.com>
 * Reinhard Arlt <reinhard.arlt@esd-electronics.com>
 *
 * based on code of fs/reiserfs/dev.c by
 *
 * (C) Copyright 2003 - 2004
 * Sysgo AG, <www.elinos.com>, Pavel Bartusek <pba@sysgo.com>
 *
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 * (at your option) any later version.
```

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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*
*/
```

```
./fs/ext4/ext4fs.c
ext4fs.c
```

```
/*
* (C) Copyright 2011 - 2012 Samsung Electronics
* EXT4 filesystem implementation in Uboot by
* Uma Shankar <uma.shankar@samsung.com>
* Manjunatha C Achar <a.manjunatha@samsung.com>
*
* ext4ls and ext4load : Based on ext2 ls and load support in Uboot.
*                       Ext4 read optimization taken from Open-Moko
*                       Qi bootloader
*
* (C) Copyright 2004
* esd gmbh <www.esd-electronics.com>
* Reinhard Arlt <reinhard.arlt@esd-electronics.com>
*
* based on code from grub2 fs/ext2.c and fs/fshelp.c by
* GRUB -- GRand Unified Bootloader
* Copyright (C) 2003, 2004 Free Software Foundation, Inc.
*
* ext4write : Based on generic ext4 protocol.
*
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* the Free Software Foundation; either version 2 of the License, or
* (at your option) any later version.
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./fs/ext4/ext4_common.c
```

ext4_common.c

```
/*
 * (C) Copyright 2011 - 2012 Samsung Electronics
 * EXT4 filesystem implementation in Uboot by
 * Uma Shankar <uma.shankar@samsung.com>
 * Manjunatha C Achar <a.manjunatha@samsung.com>
 *
 * ext4ls and ext4load : Based on ext2 ls load support in Uboot.
 *
 * (C) Copyright 2004
 * esd gmbh <www.esd-electronics.com>
 * Reinhard Arlt <reinhard.arlt@esd-electronics.com>
 *
 * based on code from grub2 fs/ext2.c and fs/fshelp.c by
 * GRUB -- GRand Unified Bootloader
 * Copyright (C) 2003, 2004 Free Software Foundation, Inc.
 *
 * ext4write : Based on generic ext4 protocol.
 *
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 * Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
 */
```

./fs/ext4/ext4_common.h

ext4_common.h

```
/*
 * (C) Copyright 2011 - 2012 Samsung Electronics
 * EXT4 filesystem implementation in Uboot by
 * Uma Shankar <uma.shankar@samsung.com>
 * Manjunatha C Achar <a.manjunatha@samsung.com>
 *
 * ext4ls and ext4load : based on ext2 ls load support in Uboot.
 *
 * (C) Copyright 2004
 * esd gmbh <www.esd-electronics.com>
 * Reinhard Arlt <reinhard.arlt@esd-electronics.com>
 *
 * based on code from grub2 fs/ext2.c and fs/fshelp.c by
 * GRUB -- GRand Unified Bootloader
```

```
* Copyright (C) 2003, 2004 Free Software Foundation, Inc.
*
* ext4write : Based on generic ext4 protocol.
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./fs/ext4/ext4_journal.c
```

```
ext4_journal.c
```

```
/*
* (C) Copyright 2011 - 2012 Samsung Electronics
* EXT4 filesystem implementation in Uboot by
* Uma Shankar <uma.shankar@samsung.com>
* Manjunatha C Achar <a.manjunatha@samsung.com>
*
* Journal data structures and headers for Journaling feature of ext4
* have been referred from JBD2 (Journaling Block device 2)
* implementation in Linux Kernel.
* Written by Stephen C. Tweedie <sct@redhat.com>
*
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```

```
*/  
  
./fs/ext4/ext4_journal.h  
ext4_journal.h  
/*  
* (C) Copyright 2011 - 2012 Samsung Electronics  
* EXT4 filesystem implementation in Uboot by  
* Uma Shankar <uma.shankar@samsung.com>  
* Manjunatha C Achar <a.manjunatha@samsung.com>  
*  
* Journal data structures and headers for Journaling feature of ext4  
* have been referred from JBD2 (Journaling Block device 2)  
* implementation in Linux Kernel.  
*  
* Written by Stephen C. Tweedie <sct@redhat.com>  
*  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.  
*/
```

```
./fs/fat/fat.c  
fat.c  
/*  
* fat.c  
*  
* R/O (V)FAT 12/16/32 filesystem implementation by Marcus Sundberg  
*  
* 2002-07-28 - rjones@nexus-tech.net - ported to ppcboot v1.1.6  
* 2003-03-10 - kharris@nexus-tech.net - ported to uboot  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
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```

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* MA 02111-1307 USA
*/
```

```
./fs/fat/fat_write.c
```

```
fat_write.c
```

```
/*
* fat_write.c
*
* R/W (V)FAT 12/16/32 filesystem implementation by Donggeun Kim
*
* See file CREDITS for list of people who contributed to this
* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./fs/fat/file.c
```

```
file.c
```

```
/*
* file.c
*
* Mini "VFS" by Marcus Sundberg
*
* 2002-07-28 - rjones@nexus-tech.net - ported to ppcboot v1.1.6
* 2003-03-10 - kharris@nexus-tech.net - ported to uboot
```

```
*
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*/
```

```
./fs/fdos/dev.c
```

```
dev.c
```

```
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* Pierre AUBERT p.aubert@staubli.com
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
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*/
```

```
./fs/fdos/dos.h
```

```
dos.h
```

```
/*
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```

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*/
```

```
./fs/fdos/fat.c
```

```
fat.c
```

```
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```

```
./fs/fdos/fdos.c
```

```
fdos.c
```

```
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 */
```

```
./fs/fdos/fdos.h
```

```
fdos.h
```

```
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 * MA 02111-1307 USA
 */
```

./fs/fdos/fs.c

fs.c

/*

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*/

./fs/fdos/subdir.c

subdir.c

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*/

./fs/fdos/vfat.c

vfat.c

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*/

./fs/fs.c

fs.c

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*/

./fs/jffs2/compr_lzo.c

compr_lzo.c

/*

* JFFS2 -- Journalling Flash File System, Version 2.

```
*
* Copyright (C) 2004 Patrik Kluba,
*           University of Szeged, Hungary
*
* For licensing information, see the file 'LICENCE' in the
* jffs2 directory.
*
* $Id: compr_lzo.c,v 1.3 2004/06/23 16:34:39 havasi Exp $
*
*/
```

```
./fs/jffs2/compr_rtime.c
```

```
compr_rtime.c
```

```
/*
* JFFS2 -- Journalling Flash File System, Version 2.
*
* Copyright (C) 2001 Red Hat, Inc.
*
* Created by Arjan van de Ven <arjanv@redhat.com>
*
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*
* $Id: compr_rtime.c,v 1.2 2002/01/24 22:58:42 rfeany Exp $
*
*
* Very simple lz77-ish encoder.
```

```
*
* Theory of operation: Both encoder and decoder have a list of "last
* occurrences" for every possible source-value; after sending the
* first source-byte, the second byte indicated the "run" length of
* matches
*
* The algorithm is intended to only send "whole bytes", no bit-messing.
*
*/
```

```
./fs/jffs2/compr_rubin.c
```

```
compr_rubin.c
```

```
/*
```

```
* JFFS2 -- Journalling Flash File System, Version 2.
```

```
*
```

```
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```

```
*
```

```
* Created by Arjan van de Ven <arjanv@redhat.com>
```

```
*
```

```
* Heavily modified by Russ Dill <Russ.Dill@asu.edu> in an attempt at  
* a little more speed.
```

```
*
```

```
* The original JFFS, from which the design for JFFS2 was derived,  
* was designed and implemented by Axis Communications AB.
```

```
*
```

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```

```
*
```

```
* $Id: compr_rubin.c,v 1.2 2002/01/24 22:58:42 rfeany Exp $
```

```
*
```

```

*/

./fs/jfffs2/compr_zlib.c
compr_zlib.c
/*
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 *
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 *
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 *
 * $Id: compr_zlib.c,v 1.2 2002/01/24 22:58:42 rfeany Exp $
 */

./fs/jfffs2/jfffs2_1pass.c
jfffs2_1pass.c
/*
 * Filename:      jfffs2.c
 * Version:      $Id: jfffs2_1pass.c,v 1.7 2002/01/25 01:56:47 nyet Exp $
 * Copyright:    Copyright (C) 2001, Russ Dill
 * Author:       Russ Dill <Russ.Dill@asu.edu>
 * Description:   Module to load kernel from jfffs2
 *-----*/

```

```
/*
 * some portions of this code are taken from jfffs2, and as such, the
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 *
 * JFFS2 -- Journalling Flash File System, Version 2.
 *
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 *
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 *
 * $Id: jfffs2_1pass.c,v 1.7 2002/01/25 01:56:47 nyet Exp $
 */
```

```
./fs/jfffs2/jfffs2_nand_1pass.c
jfffs2_nand_1pass.c
No license information found.
```

```
./fs/jfffs2/jfffs2_nand_private.h
jfffs2_nand_private.h
No license information found.
```

```
./fs/jfffs2/jfffs2_private.h
jfffs2_private.h
```

No license information found.

./fs/jfffs2/mini_inflate.c
mini_inflate.c

```
/*-----  
* Filename:      mini_inflate.c  
* Version:      $Id: mini_inflate.c,v 1.3 2002/01/24 22:58:42 rfeany Exp $  
* Copyright:    Copyright (C) 2001, Russ Dill  
* Author:       Russ Dill <Russ.Dill@asu.edu>  
* Description:   Mini inflate implementation (RFC 1951)  
*-----*/  
/*  
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*  
*/
```

./fs/jfffs2/summary.h
summary.h

```
/*  
* JFFS2 -- Journalling Flash File System, Version 2.  
*  
* Copyright © 2004 Ferenc Havasi <havasi@inf.u-szeged.hu>,  
* Zoltan Sogor <weth@inf.u-szeged.hu>,  
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*  
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*  
*/
```

./fs/reiserfs/dev.c
dev.c

```
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*/
```

```
./fs/reiserfs/mode_string.c
```

```
mode_string.c
```

```
/*
* mode_string implementation for busybox
*
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*
*/
```

```
./fs/reiserfs/reiserfs.c
```

```
reiserfs.c
```

```
/*
* Copyright 2000-2002 by Hans Reiser, licensing governed by reiserfs/README
*
* GRUB -- GRand Unified Bootloader
* Copyright (C) 2000, 2001 Free Software Foundation, Inc.
*
* (C) Copyright 2003 - 2004
* Sysgo AG, <www.elinos.com>, Pavel Bartusek <pba@sysgo.com>
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*/
```

```
./fs/reiserfs/reiserfs_private.h
reiserfs_private.h
```

```
/*
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*
* GRUB -- GRand Unified Bootloader
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*
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*/
```

```
./fs/ubifs/budget.c
budget.c
```

```
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*
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* Franklin St, Fifth Floor, Boston, MA 02110-1301 USA
*
* Authors: Adrian Hunter
*          Artem Bityutskiy (Битюцкий Артём)
*/
```

```
./fs/ubifs/crc16.c
crc16.c
```

```
/*
 *      crc16.c
 *
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 * Version 2. See the file COPYING for more details.
 */
/** CRC table for the CRC-16. The poly is 0x8005 ( $x^{16} + x^{15} + x^2 + 1$ ) */
```

```
./fs/ubifs/crc16.h
crc16.h
```

```
/*
 *      crc16.h - CRC-16 routine
 *
 * Implements the standard CRC-16:
 *   Width 16
 *   Poly  0x8005 ( $x^{16} + x^{15} + x^2 + 1$ )
 *   Init  0
 *
 * Copyright (c) 2005 Ben Gardner <bgardner@wabtec.com>
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 */
```

```
./fs/ubifs/debug.c
debug.c
```

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*/
```

```
./fs/ubifs/debug.h  
debug.h
```

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```
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io.c
```

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*         Adrian Hunter
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```

```
./fs/ubifs/key.h
```

```
key.h
```

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```
./fs/ubifs/log.c
```

```
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```
./fs/ubifs/lprops.c
```

```
lprops.c
```

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```
./fs/ubifs/lpt.c
```

```
lpt.c
```

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```
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```

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```
./fs/ubifs/master.c
master.c
/*
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```
./fs/ubifs/misc.h
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```

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```
./fs/ubifs/orphan.c
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```
./fs/ubifs/recovery.c
```

```
recovery.c
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```
./fs/ubifs/replay.c
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```
replay.c
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```
./fs/ubifs/sb.c
```

```
sb.c
```

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```
./fs/ubifs/scan.c
```

```
scan.c
```

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*/

./fs/ubifs/super.c

super.c

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./fs/ubifs/tnc.c

tnc.c

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./fs/ubifs/tnc_misc.c

tnc_misc.c

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./fs/ubifs/ubifs-media.h

ubifs-media.h

```
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./fs/ubifs/ubifs.c

ubifs.c

```
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```
./fs/ubifs/ubifs.h
ubifs.h
```

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```

*/

./fs/yaffs2/yaffscfg.h

yaffscfg.h

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./fs/yaffs2/yaffsfs.c

yaffsfs.c

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./fs/yaffs2/yaffsfs.h

yaffsfs.h

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```
./fs/yaffs2/yaffs_allocator.c
```

```
yaffs_allocator.c
```

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./fs/yaffs2/yaffs_allocator.h
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yaffs_allocator.h
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```
./fs/yaffs2/yaffs_attribs.c
```

```
yaffs_attribs.c
```

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```
./fs/yaffs2/yaffs_attribs.h
```

```
yaffs_attribs.h
```

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```
./fs/yaffs2/yaffs_bitmap.c
yaffs_bitmap.c
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yaffs_bitmap.h
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```
./fs/yaffs2/yaffs_checkptrw.c
yaffs_checkptrw.c
```

```
/*
```

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yaffs_checkptrw.h
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```
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```

```
yaffs_error.c
```

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```
./fs/yaffs2/yaffs_flashif.h
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```
yaffs_flashif.h
```

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*/
```

```
./fs/yaffs2/yaffs_flashif2.h
```

```
yaffs_flashif2.h
```

```
/*
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```
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```

```
./fs/yaffs2/yaffs_getblockinfo.h
```

```
yaffs_getblockinfo.h
```

```
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```

```
./fs/yaffs2/yaffs_guts.c
```

```
yaffs_guts.c
```

```
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```

```
./fs/yaffs2/yaffs_guts.h
```

```
yaffs_guts.h
```

```
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```

```
./fs/yaffs2/yaffs_list.h
yaffs_list.h
```

```
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```

```
./fs/yaffs2/yaffs_mtdif.c
yaffs_mtdif.c
```

```
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```

```
./fs/yaffs2/yaffs_mtdif.h
yaffs_mtdif.h
```

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```

```
./fs/yaffs2/yaffs_mtdif2.c
```

```
yaffs_mtdif2.c
```

```
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```

```
./fs/yaffs2/yaffs_mtdif2.h
```

```
yaffs_mtdif2.h
```

```
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```

```
./fs/yaffs2/yaffs_nameval.c
```

```
yaffs_nameval.c
```

```
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```

```
./fs/yaffs2/yaffs_nameval.h
```

```
yaffs_nameval.h
```

```
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```

```
./fs/yaffs2/yaffs_nand.c
```

```
yaffs_nand.c
```

```
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```

```
./fs/yaffs2/yaffs_nand.h
```

```
yaffs_nand.h
```

```
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./fs/yaffs2/yaffs_nandemul2k.h
yaffs_nandemul2k.h
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```

```
./fs/yaffs2/yaffs_nandif.c
yaffs_nandif.c
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```

```
./fs/yaffs2/yaffs_nandif.h
yaffs_nandif.h
/*
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```

```
./fs/yaffs2/yaffs_osglue.h
```

```
yaffs_osglue.h
```

```
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```

```
./fs/yaffs2/yaffs_packedtags1.c
```

```
yaffs_packedtags1.c
```

```
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```
./fs/yaffs2/yaffs_packedtags1.h
```

```
yaffs_packedtags1.h
```

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```
*/

./fs/yaffs2/yaffs_packedtags2.c
yaffs_packedtags2.c
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./fs/yaffs2/yaffs_packedtags2.h
yaffs_packedtags2.h
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./fs/yaffs2/yaffs_qsort.c
yaffs_qsort.c
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```

```
./fs/yaffs2/yaffs_summary.c
yaffs_summary.c
```

```
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```

```
./fs/yaffs2/yaffs_summary.h
yaffs_summary.h
```

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```
./fs/yaffs2/yaffs_tagscompat.c
yaffs_tagscompat.c
```

```
/*
```

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```
./fs/yaffs2/yaffs_tagscompat.h
yaffs_tagscompat.h
```

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```

```
./fs/yaffs2/yaffs_trace.h
yaffs_trace.h
```

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```

```
./fs/yaffs2/yaffs_uboot_glue.c
yaffs_uboot_glue.c
```

```
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```

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./fs/yaffs2/yaffs_verify.c
yaffs_verify.c
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./fs/yaffs2/yaffs_verify.h
yaffs_verify.h
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./fs/yaffs2/yaffs_yaffs1.c
yaffs_yaffs1.c
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./fs/yaffs2/yaffs_yaffs1.h
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./fs/yaffs2/yaffs_yaffs2.c
yaffs_yaffs2.c
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./fs/yaffs2/yaffs_yaffs2.h
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```

```
./fs/yaffs2/ydirectenv.h
```

```
ydirectenv.h
```

```
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```

```
./fs/yaffs2/yportenv.h
```

```
yportenv.h
```

```
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*/
```

```
./fs/zfs/dev.c
```

```
dev.c
```

```
/*
 *
 *   based on code of fs/reiserfs/dev.c by
 *
 *   (C) Copyright 2003 - 2004
 *   Sysgo AG, <www.elinos.com>, Pavel Bartusek <pba@sysgo.com>
 *
 */
```

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```

```
*/
```

```
./fs/zfs/zfs.c
```

```
zfs.c
```

```
/*
```

```
*
```

```
* ZFS filesystem ported to u-boot by
* Jorgen Lundman <lundman at lundman.net>
```

```
*
```

```
* GRUB -- GRand Unified Bootloader
* Copyright (C) 1999,2000,2001,2002,2003,2004
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* Copyright 2004 Sun Microsystems, Inc.
```

```
*
```

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```

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```

```
*
```

```
*/
```

```
./fs/zfs/zfs_fletcher.c
```

```
zfs_fletcher.c
```

```
/*
```

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```
*/
```

```
/*
```

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```

```
*/
```

```
./fs/zfs/zfs_lzjb.c
```

```
zfs_lzjb.c
```

```
/*
```

```
* GRUB -- GRand Unified Bootloader
* Copyright (C) 1999,2000,2001,2002,2003,2004 Free Software Foundation, Inc.
```

```
*
```

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```

```
*
```

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```

```
*
```

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```

```
*/
```

```
/*
```

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```

```
*/
```

```
./fs/zfs/zfs_sha256.c
```

```
zfs_sha256.c
```

```
/*
```

```
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```

```
*
```

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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
/*
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*/
```

```
./include/74xx_7xx.h
74xx_7xx.h
```

```
/*
* (C) Copyright 2001
* Josh Huber, Mission Critical Linux, Inc. <huber@mclx.com>
*
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* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/ACEX1K.h
ACEX1K.h
```

```
/*
* (C) Copyright 2003
* Steven Scholz, imc Measurement & Control, steven.scholz@imc-berlin.de
*
* (C) Copyright 2002
* Rich Ireland, Enterasys Networks, rireland@enterasys.com.
```

```
*
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* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
```

```
./include/addr_map.h
```

```
addr_map.h
```

```
/*
* Copyright 2008 Freescale Semiconductor, Inc.
*
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* modify it under the terms of the GNU General Public License
* Version 2 as published by the Free Software Foundation.
*
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/aes.h
```

```
aes.h
```

```
/*
* Copyright (c) 2011 The Chromium OS Authors.
* (C) Copyright 2010 - 2011 NVIDIA Corporation <www.nvidia.com>
*
* See file CREDITS for list of people who contributed to this
* project.
*
*/
```

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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/ahci.h
```

```
ahci.h
```

```
/*
* Copyright (C) Freescale Semiconductor, Inc. 2006.
* Author: Jason Jin<Jason.jin@freescale.com>
*         Zhang Wei<wei.zhang@freescale.com>
*
* See file CREDITS for list of people who contributed to this
* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/ali512x.h
```

```
ali512x.h
```

```
/*
* (C) Copyright 2002
* Daniel Engström, Omicron Ceti AB <daniel@omicron.se>.
*
* See file CREDITS for list of people who contributed to this
```

```
* project.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/altera.h
```

```
altera.h
```

```
/*  
* (C) Copyright 2013  
* Altera Corporation <www.altera.com>  
*  
* (C) Copyright 2002  
* Rich Ireland, Enterasys Networks, rireland@enterasys.com.  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*  
*/
```

```
./include/ambapp.h
```

```
ambapp.h
```

```
/* Interface for accessing Gaisler AMBA Plug&Play Bus.
```

```
* The AHB bus can be interfaced with a simpler bus -
* the APB bus, also freely available in GRLIB at
* www.gaisler.com.
*
* (C) Copyright 2007
* Daniel Hellstrom, Gaisler Research, daniel@gaisler.com.
*
* See file CREDITS for list of people who contributed to this
* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/

./include/amba_clcd.h
amba_clcd.h
/*
* Register definitions for the AMBA CLCD logic cell.
*
* derived from David A Rusling, although rearranged as a C structure
*   linux/include/asm-arm/hardware/amba_clcd.h -- Integrator LCD panel.
*
* Copyright (C) 2001 ARM Limited
*
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* License. See the file COPYING in the main directory of this archive
* for more details.
*/

./include/andestech/andes_pcu.h
andes_pcu.h
/*
* (C) Copyright 2011 Andes Technology Corp
* Macpaul Lin <macpaul@andestech.com>
*
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* it under the terms of the GNU General Public License as published by
```

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* along with this program; if not, write to the Free Software
* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./include/api_public.h
api_public.h
```

```
/*
* (C) Copyright 2007-2008 Semihalf
*
* Written by: Rafal Jaworowski <raj@semihalf.com>
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* LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY
* OUT OF THE USE OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF
* SUCH DAMAGE.
*/
```

```
./include/arm925t.h
```

```
arm925t.h
```

```
/*
*****
* NAME       : arm925t.h
* Version    : 23 June 2003
*****/
```

```
./include/armcoremodule.h
```

```
armcoremodule.h
```

```
/*
* (C) Copyright 2005
* ARM Ltd.
* Peter Pearse, <Peter.Pearse@arm.com>
* Configuration for ARM Core Modules.
* No standalonw port yet available
* - this file is included by both integratorap.h & integratorcp.h
*
* See file CREDITS for list of people who contributed to this
* project.
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
```

```
*/  
  
./include/asm-generic/errno.h  
errno.h  
/*  
* U-boot - errno.h Error number defines  
*  
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*  
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* project.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston,  
* MA 02110-1301 USA  
*/
```

```
./include/asm-generic/global_data_flags.h  
global_data_flags.h  
/*  
* transitional header until we merge global_data.h  
*  
* (C) Copyright 2000-2010  
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
* Licensed under the GPL-2 or later.  
*/  
/*  
* Global Data Flags  
*  
* Note: The low 16 bits are expected for common code. If your arch  
* really needs to add your own, use the high 16bits.  
*/
```

```
./include/asm-generic/gpio.h  
gpio.h  
/*  
* Copyright (c) 2011 The Chromium OS Authors.  
* Copyright (c) 2011, NVIDIA Corp. All rights reserved.
```

```
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* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/asm-generic/ioctl.h
```

```
ioctl.h
```

```
/* ioctl command encoding: 32 bits total, command in lower 16 bits,
 * size of the parameter structure in the lower 14 bits of the
 * upper 16 bits.
 * Encoding the size of the parameter structure in the ioctl request
 * is useful for catching programs compiled with old versions
 * and to avoid overwriting user space outside the user buffer area.
 * The highest 2 bits are reserved for indicating the ``access mode''.
 * NOTE: This limits the max parameter size to 16kB -1 !
*/
```

```
./include/asm-generic/signal.h
```

```
signal.h
```

```
No license information found.
```

```
./include/asm-generic/unaligned.h
```

```
unaligned.h
```

```
/*
 * Select endianness
*/
```

```
./include/asm-offsets.h
```

```
asm-offsets.h
```

```
/* #include <generated/asm-offsets.h> */
```

```
./include/at45.h
```

```
at45.h
```

```
No license information found.
```

```
./include/at91rm9200_i2c.h
```

```

at91rm9200_i2c.h
/* ----- */
/*           ATMEL Microcontroller Software Support  -  ROUSSET  -           */
/* ----- */
/* The software is delivered "AS IS" without warranty or condition of any   */
/* kind, either express, implied or statutory. This includes without        */
/* limitation any warranty or condition with respect to merchantability or    */
/* fitness for any particular purpose, or against the infringements of      */
/* intellectual property rights of others.                                   */
/* ----- */
/* File Name           : at91rm9200_i2c.h                                   */
/* Object              : AT91RM9200 / TWI definitions                       */
/* Generated           : AT91 SW Application Group 12/03/2002 (10:48:02)    */
/* ----- */

```

```

./include/at91rm9200_net.h
at91rm9200_net.h

```

```

/*
 * Ethernet:   An implementation of the Ethernet Device Driver suite for the
 *             uClinux 2.0.38 operating system. This Driver has been developed
 *             for AT75C220 board.
 *
 * NOTE:      The driver is implemented for one MAC
 *
 * Version:   @(#)at91rm9200_net.h   1.0.0   01/10/2001
 *
 * Authors:   Lineo Inc <www.lineo.com>
 *
 *
 *           This program is free software; you can redistribute it and/or
 *           modify it under the terms of the GNU General Public License
 *           as published by the Free Software Foundation; either version
 *           2 of the License, or (at your option) any later version.
 */

```

```

./include/ata.h
ata.h

```

```

/*
 * (C) Copyright 2000
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
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 * project.
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 */

```

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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/atmel_hlcdc.h
```

```
atmel_hlcdc.h
```

```
/*  
* Header file for AT91/AT32 MULTI LAYER LCD Controller  
*  
* Data structure and register user interface  
*  
* Copyright (C) 2012 Atmel Corporation  
*  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA  
*/
```

```
./include/atmel_lcdc.h
```

```
atmel_lcdc.h
```

```
/*  
* Header file for AT91/AT32 LCD Controller  
*  
* Data structure and register user interface  
*  
* Copyright (C) 2007 Atmel Corporation  
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*/
```

```
./include/atmel_mci.h
```

```
atmel_mci.h
```

```
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 *
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 * project.
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./include/bcd.h
```

```
bcd.h
```

```
/* Permission is hereby granted to copy, modify and redistribute this code
 * in terms of the GNU Library General Public License, Version 2 or later,
 * at your option.
 */
/* inline functions to translate to/from binary and binary-coded decimal
 * (frequently found in RTC chips).
 */
```

```
./include/bcm5221.h
```

```
bcm5221.h
```

```
/*
 * Broadcom BCM5221 Ethernet PHY
 *
 * (C) Copyright 2005 REA Elektronik GmbH <www.rea.de>
 * Anders Larsen <alarsen@rea.de>
```

```

*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./include/bedbug/bedbug.h
bedbug.h
/* $Id$ */

./include/bedbug/ppc.h
ppc.h
/* $Id$ */
/*=====
*
* OPERANDS
*
*=====*/

./include/bedbug/regs.h
regs.h
/* $Id$ */
/* Special Purpose Registers */

./include/bedbug/tables.h
tables.h
/* $Id$ */
/* This is only included by common/bedbug.c, and depends on the following
* files to already be included
* common.h
* bedbug/bedbug.h
* bedbug/ppc.h
* bedbug/regs.h
*/

./include/bedbug/type.h

```

```
type.h
/* Supporting routines */

./include/bmp_layout.h
bmp_layout.h
/* (C) Copyright 2002
 * Detlev Zundel, DENX Software Engineering, dzu@denx.de.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
 * This program is free software; you can redistribute it and/or
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 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */

./include/bootcount.h
bootcount.h
/*
 * (C) Copyright 2012
 * Stefan Roese, DENX Software Engineering, sr@denx.de.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
 * This program is free software; you can redistribute it and/or
 * modify it under the terms of the GNU General Public License as
 * published by the Free Software Foundation; either version 2 of
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 * GNU General Public License for more details.
 */

./include/bootstage.h
bootstage.h
/*
```

```
* This file implements recording of each stage of the boot process. It is
* intended to implement timing of each stage, reporting this information
* to the user and passing it to the OS for logging / further analysis.
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/bouncebuf.h
```

```
bouncebuf.h
```

```
/*
* Generic bounce buffer implementation
*
* Copyright (C) 2012 Marek Vasut <marex@denx.de>
*
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* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

./include/bus_vcxc.h

bus_vcxc.h

```
/*
 * (C) Copyright 2005-2009
 * Jens Scharsig @ BuS Elektronik GmbH & Co. KG, <esw@bus-elektronik.de>
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./include/bzlib.h

bzlib.h

```
/*
 * This file is a modified version of bzlib.h from the bzip2-1.0.2
 * distribution which can be found at http://sources.redhat.com/bzip2/
 */
/*-----*/
/*--- Public header file for the library. ---*/
/*--- bzlib.h ---*/
/*-----*/
/*--
```

./include/cbfs.h

cbfs.h

```
/*
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 *
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./include/circbuf.h
circbuf.h
/*
* (C) Copyright 2003
* Gerry Hamel, geh@ti.com, Texas Instruments
*
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* it under the terms of the GNU General Public License as published by
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*/

./include/cmd_spl.h
cmd_spl.h
/* Copyright (C) 2011
* Corscience GmbH & Co. KG - Simon Schwarz <schwarz@corscience.de>
*
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* project.
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```

```
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/command.h  
command.h
```

```
/*  
* (C) Copyright 2000-2009  
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/common.h  
common.h
```

```
/*  
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
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```

```
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/commproc.h  
commproc.h
```

```
/*  
* MPC8xx Communication Processor Module.  
* Copyright (c) 1997 Dan Malek (dmalek@jlc.net)  
*  
* (C) Copyright 2000-2006  
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
* This file contains structures and information for the communication  
* processor channels. Some CPM control and status is available  
* through the MPC8xx internal memory map. See immap.h for details.  
* This file only contains what I need for the moment, not the total  
* CPM capabilities. I (or someone else) will add definitions as they  
* are needed. -- Dan  
*  
* On the MBX board, EPPC-Bug loads CPM microcode into the first 512  
* bytes of the DP RAM and relocates the I2C parameter area to the  
* IDMA1 space. The remaining DP RAM is available for buffer descriptors  
* or other use.  
*/
```

```
./include/compiler.h  
compiler.h
```

```
/*  
* Keep all the ugly #ifdef for system stuff here  
*/
```

```
./include/config.h  
config.h
```

```
/* Automatically generated - do not edit */
```

```
./include/configs/A3000.h  
A3000.h
```

```
/*  
* (C) Copyright 2001, 2002, 2003  
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
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* project.  
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*/
```

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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/a320evb.h
```

```
a320evb.h
```

```
/*  
* (C) Copyright 2009 Faraday Technology  
* Po-Yu Chuang <ratbert@faraday-tech.com>  
*  
* Configuration settings for the Faraday A320 board.  
*  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.  
*/
```

```
./include/configs/a3m071.h
```

```
a3m071.h
```

```
/*  
* Copyright 2012 Stefan Roese <sr@denx.de>  
*  
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* project.  
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```

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*/

./include/configs/a4m072.h

a4m072.h

/*

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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.

*

* (C) Copyright 2010

* Sergei Poselenov, Emcraft Systems, sposelenov@emcraft.com.

*

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*/

./include/configs/acadia.h

acadia.h

/*

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* Stefan Roes, DENX Software Engineering, sr@denx.de.

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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/actux1.h
```

```
actux1.h
```

```
/*
 * (C) Copyright 2007
 * Michael Schwingen, michael@schwingen.org
 *
 * Configuration settings for the AcTux-1 board.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./include/configs/actux2.h
```

```
actux2.h
```

```
/*
 * (C) Copyright 2007
 * Michael Schwingen, michael@schwingen.org
 *
 * Configuration settings for the AcTux-2 board.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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* MA 02111-1307 USA
*/
```

```
./include/configs/actux3.h
```

```
actux3.h
```

```
/*
```

```
* (C) Copyright 2007
* Michael Schwingen, michael@schwingen.org
*
* Configuration settings for the AcTux-3 board.
```

```
*
* See file CREDITS for list of people who contributed to this
* project.
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/actux4.h
```

```
actux4.h
```

```
/*
```

```
* (C) Copyright 2007
* Michael Schwingen, michael@schwingen.org
*
* Configuration settings for the AcTux-4 board.
```

```
*
* See file CREDITS for list of people who contributed to this
* project.
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/Adder.h
```

```
Adder.h
```

```
/*
* Copyright (C) 2004-2005 Arabella Software Ltd.
* Yuli Barcohen <yuli@arabellasw.com>
*
* Support for Analogue&Micro Adder boards family.
* Tested on AdderII and Adder87x.
*
* See file CREDITS for list of people who contributed to this
* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/AdderUSB.h
```

```
AdderUSB.h
```

```
/*
* Copyright (C) 2006 CodeHermit.
* Bryan O'Donoghue <bodonoghue@codehermit.ie>
*
* Provides support for USB console on the Analogue & Micro Adder87x
*/
```

```
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* MA 02111-1307 USA
*/
```

```
./include/configs/adp-ag101.h
```

```
adp-ag101.h
```

```
/*
* Copyright (C) 2011 Andes Technology Corporation
* Shawn Lin, Andes Technology Corporation <nobuhiro@andestech.com>
* Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
*
* See file CREDITS for list of people who contributed to this
* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./include/configs/adp-ag101p.h
```

```
adp-ag101p.h
```

```
/*
* Copyright (C) 2011 Andes Technology Corporation
* Shawn Lin, Andes Technology Corporation <nobuhiro@andestech.com>
* Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
```

```
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./include/configs/adp-ag102.h
```

```
adp-ag102.h
```

```
/*
* Copyright (C) 2011 Andes Technology Corporation
* Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
*
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./include/configs/ADS860.h
```

```
ADS860.h
```

```
/*
```

```
./include/configs/aev.h
```

```
aev.h
```

```
/*
```

```
* (C) Copyright 2003-2006
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* (C) Copyright 2004-2005
* Martin Krause, TQ-Systems GmbH, martin.krause@tqs.de
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/afeb9260.h
afeb9260.h
```

```
/*
* (C) Copyright 2008 Sergey Lapin <slapin@ossfans.org>
*
* Configuration settings for the AFEB9260 board.
* Based on configuration for AT91SAM9260-EK
*
* See file CREDITS for list of people who contributed to this
* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
```

```
*/  
  
./include/configs/Alaska8220.h  
Alaska8220.h  
/*  
* (C) Copyright 2004  
* TsiChung Liew, Freescale Software Engineering, Tsi-Chung.Liew@freescale.  
*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/alpr.h  
alpr.h  
/*  
* (C) Copyright 2006-2008  
* Stefan Roesse, DENX Software Engineering, sr@denx.de.  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA
```

```
*/  
  
./include/configs/am335x_evm.h  
am335x_evm.h  
/*  
* am335x_evm.h  
*  
* Copyright (C) 2011 Texas Instruments Incorporated - http://www.ti.com/  
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*/
```

```
./include/configs/am3517_crane.h  
am3517_crane.h  
/*  
* am3517_crane.h - Default configuration for AM3517 CraneBoard.  
*  
* Author: Srinath.R <srinath@mistralsolutions.com>  
*  
* Based on include/configs/am3517evm.h  
*  
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*/
```

```
./include/configs/am3517_evm.h  
am3517_evm.h  
/*  
* am3517_evm.h - Default configuration for AM3517 EVM board.  
*  
*/
```

```
* Author: Vaibhav Hiremath <hvaibhav@ti.com>
*
* Based on omap3_evm_config.h
*
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./include/configs/amcc-common.h
```

```
amcc-common.h
```

```
/*
* (C) Copyright 2008, 2009
* Stefan Roes, DENX Software Engineering, sr@denx.de.
*
* Common configuration options for all AMCC boards
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/ap325rxa.h
```

```
ap325rxa.h
```

```
/*
* Configuration settings for the Renesas Solutions AP-325RXA board
*
*/
```

```
* Copyright (C) 2008 Renesas Solutions Corp.
* Copyright (C) 2008 Nobuhiro Iwamatsu <iwamatsu.nobuhiro@renesas.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/APC405.h
```

```
APC405.h
```

```
/*
* (C) Copyright 2005-2008
* Matthias Fuchs, esd gmbh germany, matthias.fuchs@esd-electronics.com
*
* (C) Copyright 2001-2004
* Stefan Roese, esd gmbh germany, stefan.roese@esd-electronics.com
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

./include/configs/apx4devkit.h

apx4devkit.h

/*

* Copyright (C) 2012 Bluegiga Technologies Oy

*

* Authors:

* Veli-Pekka Peltola <veli-pekka.peltola@bluegiga.com>

* Lauri Hintsala <lauri.hintsala@bluegiga.com>

*

* Based on m28evk.h:

* Copyright (C) 2011 Marek Vasut <marek.vasut@gmail.com>

* on behalf of DENX Software Engineering GmbH

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*/

./include/configs/ap_sh4a_4a.h

ap_sh4a_4a.h

/*

* Configuration settings for the Alpha Project AP-SH4A-4A board

*

* Copyright (C) 2012 Nobuhiro Iwamatsu <nobuhiro.iwamatsu.yj@renesas.com>

*

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*

*/

./include/configs/AR405.h

AR405.h

/*

* (C) Copyright 2001-2004

```
* Stefan Roese, esd gmbh germany, stefan.roese@esd-electronics.com
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/aria.h
```

```
aria.h
```

```
/*
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* (C) Copyright 2009, DAVE Srl <www.dave.eu>
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/armadillo-800eva.h
```

```
armadillo-800eva.h
```

```
/*
* Configuration settings for the bonito board
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/ASH405.h
```

```
ASH405.h
```

```
/*
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* MA 02111-1307 USA
*/
```

```
./include/configs/aspelite.h
```

```
aspelite.h
```

```
/*
```

```
* (C) Copyright 2010
* Marvell Semiconductor <www.marvell.com>
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
* Contributor: Mahavir Jain <mjain@marvell.com>
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* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./include/configs/astro_mcf5373l.h
astro_mcf5373l.h
```

```
/*
* Configuration settings for the Sentec Cobra Board.
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/at91rm9200ek.h
at91rm9200ek.h
/*
 * Copyright (C) 2010 Andreas Bießmann <biessmann.devel@googlemail.com>
 *
 * based on previous work by
 *
 * Ulf Samuelsson <ulf@atmel.com>
 * Rick Bronson <rick@efn.org>
 *
 * Configuration settings for the AT91RM9200EK board.
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./include/configs/at91sam9260ek.h
at91sam9260ek.h
/*
 * (C) Copyright 2007-2008
 * Stelian Pop <stelian@popies.net>
 * Lead Tech Design <www.leadtechdesign.com>
 *
 * Configuration settings for the AT91SAM9260EK & AT91SAM9G20EK boards.
 *
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* MA 02111-1307 USA
*/
```

```
./include/configs/at91sam9261ek.h
at91sam9261ek.h
```

```
/*
* (C) Copyright 2007-2008
* Stelian Pop <stelian@popies.net>
* Lead Tech Design <www.leadtechdesign.com>
*
* Configuration settings for the AT91SAM9261EK board.
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/at91sam9263ek.h
at91sam9263ek.h
```

```
/*
* (C) Copyright 2007-2008
* Stelian Pop <stelian@popies.net>
* Lead Tech Design <www.leadtechdesign.com>
*
* Configuration settings for the AT91SAM9263EK board.
*
* See file CREDITS for list of people who contributed to this
* project.
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* MA 02111-1307 USA
*/
```

```
./include/configs/at91sam9m10g45ek.h
at91sam9m10g45ek.h
```

```
/*
* (C) Copyright 2007-2008
* Stelian Pop <stelian@popies.net>
* Lead Tech Design <www.leadtechdesign.com>
*
* Configuration settings for the AT91SAM9M10G45EK board(and AT91SAM9G45EKES).
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/at91sam9rlek.h
at91sam9rlek.h
```

```
/*
* (C) Copyright 2007-2008
* Stelian Pop <stelian@popies.net>
* Lead Tech Design <www.leadtechdesign.com>
*
*/
```

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*/
```

```
./include/configs/at91sam9x5ek.h
at91sam9x5ek.h
```

```
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 *
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 * MA 02111-1307 USA
*/
```

```
./include/configs/atc.h
atc.h
/*
```

```
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*/
```

```
./include/configs/atngw100.h
atngw100.h
```

```
/*
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*
* Configuration settings for the AVR32 Network Gateway
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/atngw100mkii.h
atngw100mkii.h
```

```
/*
 * Copyright (C) 2006 Atmel Corporation
 *
 * Copyright (C) 2012 Andreas Bießmann <andreas.devel@googlemail.com>
 *
 * Configuration settings for the AVR32 Network Gateway
 *
 * See file CREDITS for list of people who contributed to this
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./include/configs/atstk1002.h
atstk1002.h
```

```
/*
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 *
 * Configuration settings for the ATSTK1002 CPU daughterboard
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 * MA 02111-1307 USA
```

```
*/  
  
./include/configs/atstk1003.h  
atstk1003.h  
/*  
* Copyright (C) 2007 Atmel Corporation  
*  
* Configuration settings for the ATSTK1003 CPU daughterboard  
*  
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* MA 02111-1307 USA  
*/
```

```
./include/configs/atstk1004.h  
atstk1004.h  
/*  
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*  
* Configuration settings for the ATSTK1003 CPU daughterboard  
*  
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*/
```

```
./include/configs/atstk1006.h  
atstk1006.h
```

```
/*  
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*  
* Configuration settings for the ATSTK1002 CPU daughterboard  
*  
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* MA 02111-1307 USA  
*/
```

```
./include/configs/balloon3.h  
balloon3.h
```

```
/*  
* Balloon3 configuration file  
*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
```

```
* MA 02111-1307 USA
*/
```

```
./include/configs/bamboo.h
bamboo.h
```

```
/*
 * (C) Copyright 2005-2007
 * Stefan Roesse, DENX Software Engineering, sr@denx.de.
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./include/configs/BC3450.h
BC3450.h
```

```
/*
 * -- Version 1.1 --
 *
 * (C) Copyright 2003-2005
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
 * (C) Copyright 2004-2005
 * Martin Krause, TQ-Systems GmbH, martin.krause@tqs.de
 *
 * (C) Copyright 2005
 * Stefan Strobl, GERSYS GmbH, stefan.strobl@gersys.de.
 *
 * History:
 * 1.1 - add define CONFIG_ZERO_BOOTDELAY_CHECK
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/bct-brettl2.h
bct-brettl2.h
/*
 * U-boot - Configuration file for BF536 brettl2 board
 */
/*
 * Processor Settings
 */
```

```
./include/configs/bf506f-ezkit.h
bf506f-ezkit.h
/*
 * U-boot - Configuration file for BF506F EZ-Kit board
 */
/*
 * Processor Settings
 */
```

```
./include/configs/bf518f-ezbrd.h
bf518f-ezbrd.h
/*
 * U-boot - Configuration file for BF518F EZBrd board
 */
/*
 * Processor Settings
 */
```

```
./include/configs/bf525-ucr2.h
bf525-ucr2.h
/*
 * U-boot - Configuration file for bf525-ucr2 board
 * The board includes ADSP-BF525 rev. 0.2,
 * 32-bit SDRAM (SAMSUNG K4S561632H-UC75),
 * USB 2.0 High Speed OTG USB WIFI,
 * SPI flash (cFeon EN25Q128 16 MB),
 * Support PPI and ITU-R656,
```

```
* See http://www.ucrobotics.com/?q=cn/ucr2
*/

./include/configs/bf526-ezbrd.h
bf526-ezbrd.h
/*
 * U-boot - Configuration file for BF526 EZBrd board
 */
/*
 * Processor Settings
 */

./include/configs/bf527-ad7160-eval.h
bf527-ad7160-eval.h
/*
 * U-boot - Configuration file for BF527 AD7160-EVAL board
 */
/*
 * Processor Settings
 */

./include/configs/bf527-ezkit.h
bf527-ezkit.h
/*
 * U-boot - Configuration file for BF537 STAMP board
 */
/*
 * Processor Settings
 */

./include/configs/bf527-sdp.h
bf527-sdp.h
/*
 * U-boot - Configuration file for BF527 SDP board
 */
/*
 * Processor Settings
 */

./include/configs/bf533-ezkit.h
bf533-ezkit.h
/*
 * U-boot - Configuration file for BF533 EZKIT board
 */
/*
 * Processor Settings
 */

./include/configs/bf533-stamp.h
bf533-stamp.h
```

```

/*
 * U-boot - Configuration file for BF533 STAMP board
 */
/*
 * Processor Settings
 */

./include/configs/bf537-minotaur.h
bf537-minotaur.h
/*
 * U-boot - Configuration file for CSP Minotaur board
 *
 * Thu Oct 25 15:30:44 CEST 2007 <hackfin@section5.ch>
 *   Minotaur config, brushed up for official uClinux dist.
 *   Parallel flash support disabled, SPI flash boot command
 *   added ('run flashboot').
 *
 * Flash image map:
 *
 * 0x00000000      u-boot bootstrap
 * 0x00010000      environment
 * 0x00020000      u-boot code
 * 0x00030000      uImage.initramfs
 */

./include/configs/bf537-pnav.h
bf537-pnav.h
/*
 * U-boot - Configuration file for BF537 PNAV board
 */
/*
 * Processor Settings
 */

./include/configs/bf537-srv1.h
bf537-srv1.h
/*
 * U-boot - Configuration file for CSP Minotaur board
 *
 * Thu Oct 25 15:30:44 CEST 2007 <hackfin@section5.ch>
 *   Minotaur config, brushed up for official uClinux dist.
 *   Parallel flash support disabled, SPI flash boot command
 *   added ('run flashboot').
 *
 * Flash image map:
 *
 * 0x00000000      u-boot bootstrap
 * 0x00010000      environment
 * 0x00020000      u-boot code

```

```
* 0x00030000      uImage.initramfs
*
*/

./include/configs/bf537-stamp.h
bf537-stamp.h
/*
 * U-boot - Configuration file for BF537 STAMP board
 */
/*
 * Processor Settings
 */

./include/configs/bf538f-ezkit.h
bf538f-ezkit.h
/*
 * U-boot - Configuration file for BF538F EZ-Kit Lite board
 */
/*
 * Processor Settings
 */

./include/configs/bf548-ezkit.h
bf548-ezkit.h
/*
 * U-boot - Configuration file for BF548 STAMP board
 */
/*
 * Processor Settings
 */

./include/configs/bf561-acvilon.h
bf561-acvilon.h
/*
 * U-boot - Configuration file for BF561 Acvilon System On Module
 * For more information please go to http://www.niistt.ru/
 */
/*
 * Processor Settings
 */

./include/configs/bf561-ezkit.h
bf561-ezkit.h
/*
 * U-boot - Configuration file for BF561 EZKIT board
 */
/*
 * Processor Settings
 */
```

```
./include/configs/bfin_adi_common.h
```

```
bfin_adi_common.h
```

```
/*  
 * U-Boot - Common settings for Analog Devices boards  
 */  
/*  
 * Command Settings  
 */
```

```
./include/configs/blackstamp.h
```

```
blackstamp.h
```

```
/*  
 * U-boot - Configuration file for BlackStamp board  
 * Configuration by Ben Matthews for UR LLE using bf533-stamp.h  
 * as a template  
 * See http://blackfin.uclinux.org/gf/project/blackstamp/  
 */  
/*  
 * Debugging: Set these options if you're having problems  
 */  
/*  
 * #define CONFIG_DEBUG_EARLY_SERIAL  
 * #define DEBUG  
 * #define CONFIG_DEBUG_DUMP  
 * #define CONFIG_DEBUG_DUMP_SYMS  
 */
```

```
./include/configs/blackvme.h
```

```
blackvme.h
```

```
/* U-boot for BlackVME. (C) Wojtek Skulski 2010.  
 * The board includes ADSP-BF561 rev. 0.5,  
 * 32-bit SDRAM (2 * MT48LC16M16A2TG or MT48LC32M16A2TG),  
 * Gigabit Ether AX88180 (ASIX) + 88E1111 rev. B2 (Marvell),  
 * SPI boot flash on PF2 (M25P64 8MB, or M25P128 16 MB),  
 * FPGA boot flash on PF3 (M25P64 8MB, or M25P128 16 MB),  
 * Spartan6-LX150 (memory-mapped; both PPIs also connected).  
 * See http://www.skutek.com  
 */
```

```
./include/configs/bluestone.h
```

```
bluestone.h
```

```
/*  
 * bluestone.h - configuration for Bluestone (APM821XX)  
 *  
 * Copyright (c) 2010, Applied Micro Circuits Corporation  
 * Author: Tirumala R Marri <tmarri@apm.com>  
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/br4.h
```

```
br4.h
```

```
/*
* U-boot - Configuration file for BR4 Appliance
*
* based on bf537-stamp.h
* Copyright (c) Switchfin Org. <dpn@switchfin.org>
*/
```

```
./include/configs/BSC9131RDB.h
```

```
BSC9131RDB.h
```

```
/*
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*
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* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/bubinga.h
```

```
bubinga.h
```

```
/*
* (C) Copyright 2000-2005
```

```
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/ca9x4_ct_vxp.h  
ca9x4_ct_vxp.h
```

```
/*  
* (C) Copyright 2010 Linaro  
* Matt Waddel, <matt.waddel@linaro.org>  
*  
* Configuration for Versatile Express. Parts were derived from other ARM  
* configurations.  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/calimain.h
```

calimain.h

```
/*
 * Copyright (C) 2011 OMICRON electronics GmbH
 *
 * Based on da850evm.h. Original Copyrights follow:
 *
 * Copyright (C) 2010 Texas Instruments Incorporated - http://www.ti.com/
 * Copyright (C) 2007 Sergey Kubushyn <ksi@koi8.net>
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
 */
```

./include/configs/cam_enc_4xx.h

cam_enc_4xx.h

```
/*
 * Copyright (C) 2009 Texas Instruments Incorporated
 *
 * Copyright (C) 2011
 * Heiko Schocher, DENX Software Engineering, hs@denx.de.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./include/configs/CANBT.h

CANBT.h

```
/*
 * (C) Copyright 2001
 * Matthias Fuchs, esd gmbh germany, matthias.fuchs@esd-electronics.com
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./include/configs/canmb.h
canmb.h
```

```
/*
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./include/configs/canyonlands.h
canyonlands.h
```

```
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 * MA 02111-1307 USA
 */
```

```
./include/configs/CATcenter.h
CATcenter.h
```

```
/*
 * ueberarbeitet durch Christoph Seyfert
 *
 * (C) Copyright 2004-2005 DENX Software Engineering,
 * Wolfgang Grandegger <wg@denx.de>
 * (C) Copyright 2003
 * DAVE Srl
 *
 * http://www.dave-tech.it
 * http://www.wawnet.biz
 * mailto:info@wawnet.biz
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```

```
*/  
  
./include/configs/charon.h  
charon.h  
/*  
* (C) Copyright 2003-2005  
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*  
* (C) Copyright 2004-2006  
* Martin Krause, TQ-Systems GmbH, martin.krause@tqs.de  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/cm-bf527.h  
cm-bf527.h  
/*  
* U-boot - Configuration file for CM-BF527 board  
*/  
/*  
* Processor Settings  
*/
```

```
./include/configs/cm-bf533.h  
cm-bf533.h  
/*  
* U-boot - Configuration file for CM-BF533 board  
*/  
/*  
* Processor Settings  
*/
```

```
./include/configs/cm-bf537e.h
cm-bf537e.h
```

```
/*
 * U-boot - Configuration file for CM-BF537E board
 */
/*
 * Processor Settings
 */
```

```
./include/configs/cm-bf537u.h
cm-bf537u.h
```

```
/*
 * U-boot - Configuration file for CM-BF537U board
 */
/*
 * Processor Settings
 */
```

```
./include/configs/cm-bf548.h
cm-bf548.h
```

```
/*
 * U-boot - Configuration file for cm-bf548 board
 */
/*
 * Processor Settings
 */
```

```
./include/configs/cm-bf561.h
cm-bf561.h
```

```
/*
 * U-boot - Configuration file for CM-BF561 board
 */
/*
 * Processor Settings
 */
```

```
./include/configs/cm4008.h
cm4008.h
```

```
/*
 * (C) Copyright 2004
 * Greg Ungerer <greg.ungerer@opengear.com>.
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
 * See file CREDITS for list of people who contributed to this
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/cm41xx.h
cm41xx.h
```

```
/*
* (C) Copyright 2005
* Greg Ungerer <greg.ungerer@opengear.com>.
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/cm5200.h
cm5200.h
```

```
/*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/cmi_mpc5xx.h
```

```
cmi_mpc5xx.h
```

```
/*
* (C) Copyright 2003
* Martin Winistoerfer, martinwinistoerfer@gmx.ch.
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* Foundation,
*/
```

```
./include/configs/CMS700.h
```

```
CMS700.h
```

```
/*
* (C) Copyright 2005
* Matthias Fuchs, esd gmbh germany, matthias.fuchs@esd-electronics.com
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* See file CREDITS for list of people who contributed to this
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/cm_t35.h
```

```
cm_t35.h
```

```
/*
* (C) Copyright 2011 CompuLab, Ltd.
* Mike Rapoport <mike@compulab.co.il>
* Igor Grinberg <grinberg@compulab.co.il>
*
* Based on omap3_beagle.h
* (C) Copyright 2006-2008
* Texas Instruments.
* Richard Woodruff <r-woodruff2@ti.com>
* Syed Mohammed Khasim <x0khasim@ti.com>
*
* Configuration settings for the CompuLab CM-T35 and CM-T3730 boards
*
* See file CREDITS for list of people who contributed to this
* project.
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*/
```

```
./include/configs/cobra5272.h
```

```
cobra5272.h
```

```
/*
* Configuration settings for the Sentec Cobra Board.
*/
```

```
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/cogent_common.h
```

```
cogent_common.h
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/cogent_mpc8260.h
```

```
cogent_mpc8260.h
```

```
/*
```

```
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*/
```

```
./include/configs/cogent_mpc8xx.h
```

```
cogent_mpc8xx.h
```

```
/*
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*/
```

```
./include/configs/colibri_pxa270.h
```

```
colibri_pxa270.h
```

```
/*
```

```
* Toradex Colibri PXA270 configuration file
*
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*/
```

```
./include/configs/colibri_t20_iris.h
colibri_t20_iris.h
```

```
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*
*/
```

```
./include/configs/coreboot.h
coreboot.h
```

```
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* Graeme Russ, graeme.russ@gmail.com.
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*/
```

```
./include/configs/corenet_ds.h
corenet_ds.h
```

```
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*/
```

```
./include/configs/CPC45.h
CPC45.h
```

```
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*/
```

```
./include/configs/CPCI2DP.h
CPCI2DP.h
```

```
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```

```
./include/configs/CPCI405.h
CPCI405.h
```

```
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```

```
./include/configs/CPCI4052.h
CPCI4052.h
```

```
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*/
```

```
./include/configs/CPCI405AB.h
CPCI405AB.h
```

```
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```

```
./include/configs/CPCI405DT.h
CPCI405DT.h
```

```
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*/
```

```
./include/configs/cpci5200.h
cpci5200.h
```

```
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```

```
./include/configs/CPCI750.h
```

```
CPCI750.h
```

```
/*
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```

```
./include/configs/CPCIISER4.h
```

```
CPCIISER4.h
```

```
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*/
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* MA 02111-1307 USA  
*/
```

```
./include/configs/CPU86.h
```

```
CPU86.h
```

```
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```

```
./include/configs/CPU87.h
```

```
CPU87.h
```

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*/
```

```
./include/configs/cpu9260.h
```

```
cpu9260.h
```

```
/*
```

```
* (C) Copyright 2007-2008  
* Stelian Pop <stelian@popies.net>  
* Lead Tech Design <www.leadtechdesign.com>  
* Ilko Iliev <www.ronetix.at>
```

```
/*
```

```
* (C) Copyright 2009  
* Eric Benard <eric@eukrea.com>
```

```
/*
```

```
* Configuration settings for the Eukrea CPU9260 board.
```

```
/*
```

```
* See file CREDITS for list of people who contributed to this  
* project.
```

```
/*
```

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```

```
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```

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```
*/
```

```
./include/configs/cpuat91.h
```

```
cpuat91.h
```

```
/*
```

```
* CPUAT91 by (C) Copyright 2006-2010 Eric Benard  
* eric@eukrea.com
```

```
/*
```

```
* Configuration settings for the CPUAT91 board.
```

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./include/configs/CRAYL1.h
CRAYL1.h
/*
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* MA 02111-1307 USA
*/
/*
* board/config.h - configuration options, board specific
*/

./include/configs/csb272.h
```

csb272.h

```
/*
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 */
```

./include/configs/csb472.h

csb472.h

```
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```

./include/configs/CU824.h

CU824.h

```
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 */
```

./include/configs/da830evm.h

da830evm.h

```
/*
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 *
 * Based on davinci_dvevm.h. Original Copyrights follow:
 *
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 */
```

./include/configs/da850evm.h

da850evm.h

```
/*
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 */
```

./include/configs/davinci_dm355evm.h

davinci_dm355evm.h

```
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 * MA 02111-1307 USA
 */
```

./include/configs/davinci_dm355leopard.h

davinci_dm355leopard.h

```
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* MA 02111-1307 USA
*/
```

```
./include/configs/davinci_dm365evm.h
davinci_dm365evm.h
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/davinci_dm6467evm.h
davinci_dm6467evm.h
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/davinci_dvevm.h
```

```
davinci_dvevm.h
```

```
/*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/davinci_schmoogie.h
```

```
davinci_schmoogie.h
```

```
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* MA 02111-1307 USA
*/
```

```
./include/configs/davinci_sffsdr.h
```

davinci_sffsdr.h

```
/*
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 *
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 * Copyright (C) 2008 Philip Balister, OpenSDR <philip@opensdr.com>
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./include/configs/davinci_sonata.h

davinci_sonata.h

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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./include/configs/DB64360.h

DB64360.h

```
/*
 * (C) Copyright 2001
 * Josh Huber <huber@mcclx.com>, Mission Critical Linux, Inc.
 *
 */
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/DB64460.h
```

```
DB64460.h
```

```
/*
* (C) Copyright 2001
* Josh Huber <huber@mcrlx.com>, Mission Critical Linux, Inc.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/dbau1x00.h
```

```
dbau1x00.h
```

```
/*
* (C) Copyright 2003
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
*/
```

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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/debris.h
```

```
debris.h
```

```
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* Sangmoon Kim, Etin Systems, dogoil@etinsys.com.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/devkit3250.h
```

```
devkit3250.h
```

```
/*
* Embest/Timll DevKit3250 board configuration file
*
* Copyright (C) 2011 Vladimir Zapolskiy <vz@mleia.com>
```

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* along with this program; if not, write to the Free Software
* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
* MA 02110-1301, USA.
*/
```

```
./include/configs/devkit8000.h
devkit8000.h
```

```
/*
* (C) Copyright 2006-2008
* Texas Instruments.
* Richard Woodruff <r-woodruff2@ti.com>
* Syed Mohammed Khasim <x0khasim@ti.com>
*
* (C) Copyright 2009
* Frederik Kriewitz <frederik@kriewitz.eu>
*
* Configuration settings for the DevKit8000 board.
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/dig297.h
```

dig297.h

```
/*
 * (C) Copyright 2011 Comelit Group SpA
 * Luca Ceresoli <luca.ceresoli@comelit.it>
 *
 * Based on omap3_beagle.h:
 * (C) Copyright 2006-2008
 * Texas Instruments.
 * Richard Woodruff <r-woodruff2@ti.com>
 * Syed Mohammed Khasim <x0khasim@ti.com>
 *
 * Configuration settings for the Comelit DIG297 board.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./include/configs/digsy_mtc.h

digsy_mtc.h

```
/*
 * (C) Copyright 2003-2004
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
 * (C) Copyright 2005-2007
 * Modified for InterControl digsyMTC MPC5200 board by
 * Frank Bodammer, GCD Hard- & Software GmbH,
 * frank.bodammer@gcd-solutions.de
 *
 * (C) Copyright 2009 Semihalf
 * Optimized for digsyMTC by: Grzegorz Bernacki <gjb@semihalf.com>
 *
 * See file CREDITS for list of people who contributed to this
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* along with this program\; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/dkb.h
dkb.h
```

```
/*
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* Marvell Semiconductor <www.marvell.com>
* Written-by: Lei Wen <leiwen@marvell.com>
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./include/configs/dlvision-10g.h
dlvision-10g.h
```

```
/*
* (C) Copyright 2010
* Dirk Eibach, Guntermann & Drunck GmbH, eibach@gdsys.de
*
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*
*/
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/dlvision.h
```

```
dlvision.h
```

```
/*
* (C) Copyright 2009
* Dirk Eibach, Guntermann & Drunck GmbH, eibach@gdsys.de
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/dnp5370.h
```

```
dnp5370.h
```

```
/*
* U-boot - Configuration file for SSV DNP5370 board
*/
/* this must come first */
/*
* Processor Settings
*/
```

```
./include/configs/dns325.h
dns325.h
```

```
/*
 * Copyright (C) 2011
 * Stefan Herbrechtsmeier <stefan@code.herbrechtsmeier.net>
 *
 * Based on Kirkwood support:
 * (C) Copyright 2009
 * Marvell Semiconductor <www.marvell.com>
 * Written-by: Prafulla Wadaskar <prafulla@marvell.com>
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
 * MA 02110-1301 USA
 */
```

```
./include/configs/dockstar.h
dockstar.h
```

```
/*
 * Copyright (C) 2010 Eric C. Cooper <ecc@cmu.edu>
 *
 * Based on sheevaplug.h originally written by
 * Prafulla Wadaskar <prafulla@marvell.com>
 * (C) Copyright 2009
 * Marvell Semiconductor <www.marvell.com>
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*/
```

```
./include/configs/DP405.h
```

```
DP405.h
```

```
/*
* (C) Copyright 2001-2003
* Stefan Roese, esd gmbh germany, stefan.roese@esd-electronics.com
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* MA 02111-1307 USA
*/
```

```
./include/configs/dreamplug.h
```

```
dreamplug.h
```

```
/*
* (C) Copyright 2011
* Jason Cooper <u-boot@lakedaemon.net>
*
* Based on work by:
* Marvell Semiconductor <www.marvell.com>
* Written-by: Siddarth Gore <gores@marvell.com>
*
* See file CREDITS for list of people who contributed to this
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* MA 02110-1301 USA
*/
```

```
./include/configs/DU405.h
```

```
DU405.h
```

```
/*
* (C) Copyright 2001
* Stefan Roesse, esd gmbh germany, stefan.roesse@esd-electronics.com
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/DU440.h
```

```
DU440.h
```

```
/*
* (C) Copyright 2008
* Matthias Fuchs, esd gmbh, matthias.fuchs@esd-electronics.com
*
* based on the Sequoia board configuration by
* Stefan Roesse, Jacqueline Pira-Ferriol and Alain Saurel
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/dvlhost.h
```

```
dvlhost.h
```

```
/*
* (C) Copyright 2009
* Michael Schwingen, michael@schwingen.org
*
* Configuration settings for the
* dLAN200 AV Wireless G ("dvlhost") board.
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* MA 02111-1307 USA
*/
```

```
./include/configs/ea20.h
```

```
ea20.h
```

```
/*
* Copyright (C) 2010 Texas Instruments Incorporated - http://www.ti.com/
*
* Based on davinci_dvevm.h. Original Copyrights follow:
*
* Copyright (C) 2007 Sergey Kubushyn <ksi@koi8.net>
```

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*/

./include/configs/ebony.h
ebony.h
/*
* (C) Copyright 2002 Scott McNutt <smcnutt@artesyincp.com>
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* project.
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* MA 02111-1307 USA
*/

./include/configs/eb_cpu5282.h
eb_cpu5282.h
/*
* Configuration settings for the BuS EB+CPU5283 boards (aka EB+MCF-EV123)
*
* (C) Copyright 2005-2009 BuS Elektronik GmbH & Co.KG <esw@bus-elektronik.de>
*
* See file CREDITS for list of people who contributed to this
* project.
*

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* MA 02111-1307 USA
*/
```

```
./include/configs/eb_cpux9k2.h
eb_cpux9k2.h
```

```
/*
* (C) Copyright 2008-2009
* BuS Elektronik GmbH & Co. KG <www.bus-elektronik.de>
* Jens Scharsig <esw@bus-elektronik.de>
*
* Configuration settings for the EB+CPUx9K2 board.
*
* See file CREDITS for list of people who contributed to this
* project.
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* MA 02111-1307 USA
*/
```

```
./include/configs/eco5pk.h
eco5pk.h
```

```
/*
* Copyright (C) 2012 8D Technologies inc.
* Based on mt_ventoux.h, original banner below:
*
*/
```

```
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* Stefano Babic, DENX Software Engineering, sbabic@denx.de.
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./include/configs/ecovec.h
ecovec.h
```

```
/*
* Configuration settings for the Renesas Solutions ECOVEC board
*
* Copyright (C) 2009 - 2011 Renesas Solutions Corp.
* Copyright (C) 2009 Kuninori Morimoto <morimoto.kuninori@renesas.com>
* Copyright (C) 2010, 2011 Nobuhiro Iwamatsu <nobuhiro.iwamatsu.yj@renesas.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/edminiv2.h
edminiv2.h
```

```
/*
 * Copyright (C) 2010 Albert ARIBAUD <albert.u.boot@aribaud.net>
 *
 * Based on original Kirkwood support which is
 * (C) Copyright 2009
 * Marvell Semiconductor <www.marvell.com>
 * Written-by: Prafulla Wadaskar <prafulla@marvell.com>
 *
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 * project.
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
 * MA 02110-1301 USA
 */
```

```
./include/configs/ELPPC.h
```

```
ELPPC.h
```

```
/*
 * (C) Copyright 2002 ELTEC Elektronik AG
 * Frank Gottschling <fgottschling@eltec.de>
 *
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```

```
*/  
  
./include/configs/ELPT860.h  
ELPT860.h  
/*  
**=====**  
**  
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** The LEOX team <team@leox.org>, http://www.leox.org  
**  
** LEOX.org is about the development of free hardware and software resources  
** for system on chip.  
**  
** Description: U-Boot port on the LEOX's ELPT860 CPU board  
** ~~~~~  
**  
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** Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
** MA 02111-1307 USA  
**  
**=====**  
*/
```

```
./include/configs/enbw_cmc.h  
enbw_cmc.h  
/*  
* (C) Copyright 2011  
* Heiko Schocher, DENX Software Engineering, hs@denx.de.  
*  
* Based on:  
* Copyright (C) 2010 Texas Instruments Incorporated - http://www.ti.com/  
*  
* Based on davinci_dvevm.h. Original Copyrights follow:  
*  
* Copyright (C) 2007 Sergey Kubushyn <ksi@koi8.net>  
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./include/configs/eNET.h
eNET.h
```

```
/*
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* MA 02111-1307 USA
*/
```

```
./include/configs/ep8248.h
ep8248.h
```

```
/*
* Copyright (C) 2004 Arabella Software Ltd.
* Yuli Barcohen <yuli@arabellasw.com>
*
* U-Boot configuration for Embedded Planet EP8248 boards.
*
* See file CREDITS for list of people who contributed to this
* project.
*
*/
```

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* MA 02111-1307 USA
*/
```

```
./include/configs/ep8260.h
ep8260.h
```

```
/*
* (C) Copyright 2002
* Frank Panno <fpanno@delphintech.com>, Delphin Technology AG
*
* This file is based on similar values for other boards found in other
* U-Boot config files, and some that I found in the EP8260 manual.
*
* See file CREDITS for list of people who contributed to this
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* MA 02111-1307 USA
*/
```

```
./include/configs/ep82xxm.h
ep82xxm.h
```

```
/*
* Copyright (C) 2006 Embedded Planet, LLC.
*
* U-Boot configuration for Embedded Planet EP82xxM boards.
```

```
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* MA 02111-1307 USA
*/
```

```
./include/configs/EP88x.h
```

```
EP88x.h
```

```
/*
* Copyright (C) 2005 Arabella Software Ltd.
* Yuli Barcohen <yuli@arabellasw.com>
*
* Support for Embedded Planet EP88x boards.
* Tested on EP88xC with MPC885 CPU, 64MB SDRAM and 16MB flash.
*
* See file CREDITS for list of people who contributed to this
* project.
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* MA 02111-1307 USA
*/
```

```
./include/configs/espt.h
```

```
espt.h
```

```
/*
 * Configuration settings for the ESPT-GIGA board
 *
 * Copyright (C) 2008 Renesas Solutions Corp.
 * Copyright (C) 2008 Nobuhiro Iwamatsu <iwamatsu.nobuhiro@renesas.com>
 *
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 * MA 02111-1307 USA
 */
```

```
./include/configs/ESTEEM192E.h
ESTEEM192E.h
```

```
/*
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 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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 */
```

./include/configs/ethernut5.h

ethernut5.h

/*

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* egnite GmbH <info@egnite.de>

*

* Configuration settings for Ethernut 5 with AT91SAM9XE.

*

* See file CREDITS for list of people who contributed to this

* project.

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*/

./include/configs/EVB64260.h

EVB64260.h

/*

* (C) Copyright 2001

* Josh Huber <huber@mclx.com>, Mission Critical Linux, Inc.

*

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```
*/  
  
./include/configs/eXalion.h  
eXalion.h  
/*  
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*/
```

```
./include/configs/EXBITGEN.h  
EXBITGEN.h  
/*  
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* MA 02111-1307 USA
```

```

*/

./include/configs/exynos5250-dt.h
exynos5250-dt.h
/*
 * Copyright (C) 2012 Samsung Electronics
 *
 * Configuration settings for the SAMSUNG EXYNOS5250 board.
 *
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 * MA 02111-1307 USA
 */

./include/configs/FADS823.h
FADS823.h
/*
 * 1999-nov-26: The FADS is using the following physical memorymap:
 *
 * ff020000 -> ff02ffff : pcmcia      io remapping
 * ff010000 -> ff01ffff : BCSR        connected to CS1, setup by U-Boot
 * ff000000 -> ff00ffff : IMAP        internal in the cpu
 * e0000000 -> f3ffffff : pcmcia      memory remapping by m8xx_pcmcia
 * fe000000 -> fe1ffffff : flash      connected to CS0, setup by U-Boot
 * 00000000 -> nnnnnnnn : sdram/dram  setup by U-Boot
 */

./include/configs/FADS850SAR.h
FADS850SAR.h
/*
 * 1999-nov-26: The FADS is using the following physical memorymap:
 *
 * ff020000 -> ff02ffff : pcmcia
 * ff010000 -> ff01ffff : BCSR        connected to CS1, setup by 8xxrom
 * ff000000 -> ff00ffff : IMAP        internal in the cpu
 * fe000000 -> ffnnnnnn : flash      connected to CS0, setup by 8xxrom

```

```
* 00000000 -> nnnnnnnn : sdram/dram setup by 8xxrom
*/
```

```
./include/configs/FADS860T.h
FADS860T.h
/*
```

```
./include/configs/favr-32-ezkit.h
favr-32-ezkit.h
/*
```

```
* Copyright (C) 2008 Atmel Corporation
*
* Configuration settings for the Favr-32 EarthLCD LCD kit.
*
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* Place, Suite 330, Boston, MA 02111-1307 USA
*/
```

```
./include/configs/FLAGADM.h
FLAGADM.h
/*
```

```
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* MA 02111-1307 USA
*/
```

```
./include/configs/flea3.h
```

```
flea3.h
```

```
/*
 * (C) Copyright 2011, Stefano Babic <sbabic@denx.de>
 *
 * (C) Copyright 2008-2010 Freescale Semiconductor, Inc.
 *
 * Copyright (C) 2007, Guennadi Liakhovetski <lg@denx.de>
 *
 * Configuration for the flea3 board.
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*/
```

```
./include/configs/FPS850L.h
```

```
FPS850L.h
```

```
/*
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*/
```

```
./include/configs/FPS860L.h  
FPS860L.h
```

```
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* MA 02111-1307 USA  
*/
```

```
./include/configs/fx12mm.h  
fx12mm.h
```

```
/*  
* (C) Copyright 2008  
*  
* Ricardo Ribalda-Universidad Autonoma de Madrid-ricardo.ribalda@uam.es  
* This work has been supported by: QTechnology http://qtec.com  
*  
* Georg Schardt <schardt@team-ctech.de>  
*  
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* MA 02111-1307 USA
*/
```

```
./include/configs/G2000.h
```

```
G2000.h
```

```
/*
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* MA 02111-1307 USA
*/
```

```
./include/configs/galaxy5200.h
```

```
galaxy5200.h
```

```
/*
* (C) Copyright 2003-2005
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* (C) Copyright 2006
* Eric Schumann, Phytex Messatechnik GmbH
*
* (C) Copyright 2009
* Jon Smirl <jonsmirl@gmail.com>
*
* (C) Copyright 2009
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```
* Eric Millbrandt, DEKA Research and Development Corporation
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/gdppc440etx.h
```

```
gdppc440etx.h
```

```
/*
* (C) Copyright 2008
* Dirk Eibach, Guntermann & Drunck GmbH, eibach@gdsys.de
*
* Based on include/configs/yosemite.h
* (C) Copyright 2005-2007
* Stefan Roes, DENX Software Engineering, sr@denx.de.
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/GEN860T.h
```

```
GEN860T.h
```

```
/*  
 * (C) Copyright 2000  
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
 * Keith Outwater, keith_outwater@mvis.com  
 *  
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 * along with this program; if not, write to the Free Software  
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
 * MA 02111-1307 USA  
 */
```

```
./include/configs/GENIETV.h
```

```
GENIETV.h
```

```
/*  
 * The GENIETV is using the following physical memorymap (copied from  
 * the FADS configuration):  
 *  
 * ff020000 -> ff02ffff : pcmcia  
 * ff010000 -> ff01ffff : BCSR          connected to CS1, setup by 8xxROM  
 * ff000000 -> ff00ffff : IMAP         internal in the cpu  
 * 30000000 -> 300ffffff : flash       connected to CS0  
 * 00000000 -> nnnnnnnn : sdram        setup by U-Boot  
 *  
 * CS pins are connected as follows:  
 *  
 * CS0 -512Kb boot flash  
 * CS1 - SDRAM #1  
 * CS2 - SDRAM #2  
 * CS3 - Flash #1  
 * CS4 - Flash #2  
 * CS5 - Lon (if present)  
 * CS6 - PCMCIA #1  
 * CS7 - PCMCIA #2  
 */
```

./include/configs/gplugd.h

gplugd.h

```
/*
 * (C) Copyright 2011
 * eInfochips Ltd. <www.einfochips.com>
 * Written-by: Ajay Bhargav <ajay.bhargav@einfochips.com>
 *
 * Based on Aspenite:
 * (C) Copyright 2010
 * Marvell Semiconductor <www.marvell.com>
 * Written-by: Prafulla Wadaskar <prafulla@marvell.com>
 * Contributor: Mahavir Jain <mjain@marvell.com>
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
 * MA 02110-1301 USA
 */
```

./include/configs/grasshopper.h

grasshopper.h

```
/*
 * Copyright (C) 2011
 * Corscience GmbH & Co.KG, Andreas Bießmann <biessmann@corscience.de>
 *
 * Configuration settings for the grasshopper (ICnova AP7000) board
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/grsim.h
```

```
grsim.h
```

```
/* Configuration header file for LEON3 GRSIM, trying to be similar
```

```
* to Gaisler's GR-XC3S-1500 board.
```

```
*
```

```
* (C) Copyright 2003-2005
```

```
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
```

```
*
```

```
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```
* Daniel Hellstrom, Gaisler Research, daniel@gaisler.com.
```

```
*
```

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* See file CREDITS for list of people who contributed to this
* project.
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*
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```

```
*/
```

```
./include/configs/grsim_leon2.h
```

```
grsim_leon2.h
```

```
/* Configuration header file for LEON2 GRSIM.
```

```
*
```

```
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```

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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
```

```
*
```

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* Daniel Hellstrom, Gaisler Research, daniel@gaisler.com.
```

```
*
```

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* See file CREDITS for list of people who contributed to this
```

```
* project.  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/gr_cpci_ax2000.h
```

```
gr_cpci_ax2000.h
```

```
/* Configuration header file for Gaisler GR-CPCI-AX2000  
* AX board. Note that since the AX is removable the configuration  
* for this board must be edited below.  
*  
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
* (C) Copyright 2008  
* Daniel Hellstrom, Gaisler Research, daniel@gaisler.com.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/gr_ep2s60.h
```

gr_ep2s60.h

```
/* Configuration header file for Gaisler Research AB's Template
 * design (GPL Open Source SPARC/LEON3 96MHz) for Altera NIOS
 * Development board Stratix II edition, with the FPGA device
 * EP2S60.
 *
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 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
 * (C) Copyright 2008
 * Daniel Hellstrom, Gaisler Research, daniel@gaisler.com.
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./include/configs/gr_xc3s_1500.h

gr_xc3s_1500.h

```
/* Configuration header file for Gaisler GR-XC3S-1500
 * spartan board.
 *
 * (C) Copyright 2003-2005
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
 * (C) Copyright 2007
 * Daniel Hellstrom, Gaisler Research, daniel@gaisler.com.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/guruplug.h
```

```
guruplug.h
```

```
/*
```

```
* (C) Copyright 2009  
* Marvell Semiconductor <www.marvell.com>  
* Written-by: Siddarth Gore <gores@marvell.com>  
*  
* See file CREDITS for list of people who contributed to this  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,  
* MA 02110-1301 USA  
*/
```

```
./include/configs/gw8260.h
```

```
gw8260.h
```

```
/*
```

```
* (C) Copyright 2000  
* Murray Jensen <Murray.Jensen@cmst.csiro.au>  
*  
* (C) Copyright 2000  
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>  
* Marius Groeger <mgroeger@sysgo.de>  
*  
* (C) Copyright 2001  
* Advent Networks, Inc. <http://www.adventnetworks.com>  
* Jay Monkman <jmonkman@adventnetworks.com>
```

```
*
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* Advent Networks, Inc. <http://www.adventnetworks.com>
* Oliver Brown <obrown@adventnetworks.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./include/configs/h2200.h
h2200.h
/*
* iPAQ h2200 board configuration
*
* Copyright (C) 2012 Lukasz Dalek <luk0104@gmail.com>
*
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*/

./include/configs/h2_p2_dbg_board.h
h2_p2_dbg_board.h
/*
*
```

```
* BRIEF MODULE DESCRIPTION
*   TI H2 and P2 Debug Board hardware map
*
* Copyright (C) 2004 MPC-Data Limited. (http://www.mpc-data.co.uk)
* Author: MPC-Data Limited
*       Dave Peverley
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* 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./include/configs/hammerhead.h
hammerhead.h
```

```
/*
* Copyright (C) 2008 Miromico AG
*
* Configuration settings for the Miromico Hammerhead AVR32 board
*
* See file CREDITS for list of people who contributed to this
* project.
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```

```
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/harmony.h  
harmony.h
```

```
/*  
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* NVIDIA Corporation <www.nvidia.com>  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/hawkboard.h  
hawkboard.h
```

```
/*  
* Copyright (C) 2010 Texas Instruments Incorporated - http://www.ti.com/  
*  
* Based on davinci_dvevm.h. Original Copyrights follow:  
*  
* Copyright (C) 2007 Sergey Kubushyn <ksi@koi8.net>  
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```

* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/

./include/configs/hermes.h

hermes.h

/*

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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.

*

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* MA 02111-1307 USA

*/

./include/configs/HH405.h

HH405.h

/*

* (C) Copyright 2001-2004

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*

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* Matthias Fuchs, esd GmbH, matthias.fuchs@esd-electronics.com

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/HIDDEN_DRAGON.h
HIDDEN_DRAGON.h
```

```
/*
* (C) Copyright 2004
* Yusdi Santoso, Adaptec Inc., yusdi_santoso@adaptec.com
*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
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*/
```

```
./include/configs/highbank.h
highbank.h
```

```
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*/
```

```
./include/configs/hmi1001.h
```

```
hmi1001.h
```

```
/*  
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*/
```

```
./include/configs/HUB405.h
```

```
HUB405.h
```

```
/*  
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* Stefan Roese, esd gmbh germany, stefan.roese@esd-electronics.com  
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*/
```

```
./include/configs/HWW1U1A.h
```

```
HWW1U1A.h
```

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 * MA 02111-1307 USA
*/
```

```
./include/configs/hymod.h
```

```
hymod.h
```

```
/*
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 * Murray Jensen <Murray.Jensen@cmst.csiro.au>
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*/
```

```
./include/configs/ib62x0.h
ib62x0.h
```

```
/*
 * Copyright (C) 2011-2012
 * Gerald Kerma <dreagle@doukki.net>
 * Luka Perkov <luka@openwrt.org>
 *
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*/
```

```
./include/configs/ibf-dsp561.h
ibf-dsp561.h
```

```
/*
 * U-boot - Configuration file for IBF-DSP561 board
 */
/*
 * Processor Settings
 */
```

```
./include/configs/IceCube.h
IceCube.h
```

```
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 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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* MA 02111-1307 USA
*/
```

```
./include/configs/icon.h
```

```
icon.h
```

```
/*
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* Stefan Roesse, DENX Software Engineering, sr@denx.de.
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* MA 02111-1307 USA
*/
```

```
./include/configs/iconnect.h
```

```
iconnect.h
```

```
/*
* (C) Copyright 2009-2012
* Wojciech Dubowik <wojciech.dubowik@neratec.com>
* Luka Perkov <luka@openwrt.org>
*
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*/
```

```
./include/configs/ICU862.h
ICU862.h
```

```
/*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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* MA 02111-1307 USA
*/
```

```
./include/configs/idmr.h
idmr.h
```

```
/*
* Configuration settings for the iDMR board
*
* Based on MC5272C3, r5200 and M5271EVB board configs
* (C) Copyright 2006 Wolfgang Denk, DENX Software Engineering, wd@denx.de.
* (C) Copyright 2006 Lab X Technologies <zachary.landau@labxtechnologies.com>
* (C) Copyright 2003 Josef Baumgartner <josef.baumgartner@telex.de>
*
* See file CREDITS for list of people who contributed to this
* project.
*/
```

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* MA 02111-1307 USA
*/
```

```
./include/configs/IDS8247.h
```

```
IDS8247.h
```

```
/*
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* Heiko Schocher, DENX Software Engineering, <hs@denx.de>
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* MA 02111-1307 USA
*/
```

```
./include/configs/igep00x0.h
```

```
igep00x0.h
```

```
/*
* Common configuration settings for IGEP technology based boards
*
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* ISEE 2007 SL, <www.iseebcn.com>
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* MA 02111-1307 USA
*/
```

```
./include/configs/ima3-mx53.h
ima3-mx53.h
```

```
/*
* (C) Copyright 2012, Stefano Babic <sbabic@denx.de>
*
* Copyright (C) 2010 Freescale Semiconductor, Inc.
*
* Configuration settings for the MX53-EVK Freescale board.
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* Foundation, Inc.
*/
```

```
./include/configs/imx27lite-common.h
imx27lite-common.h
```

```
/*
* Copyright (C) 2010 Heiko Schocher <hs@denx.de>
*
* based on:
* Copyright (C) 2009 Ilya Yanok <yanok@emcraft.com>
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* MA 02111-1307 USA
*/
```

```
./include/configs/imx27lite.h
```

```
imx27lite.h
```

```
/*
* Copyright (C) 2009 Ilya Yanok <yanok@emcraft.com>
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*/
```

```
./include/configs/imx31_litekit.h
```

```
imx31_litekit.h
```

```
/*
* (C) Copyright 2004
* Texas Instruments.
* Richard Woodruff <r-woodruff2@ti.com>
* Kshitij Gupta <kshitij@ti.com>
*
* Configuration settings for the LogicPD i.MX31 Litekit board.
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/imx31_phycore.h
imx31_phycore.h
```

```
/*
* (C) Copyright 2004
* Texas Instruments.
* Richard Woodruff <r-woodruff2@ti.com>
* Kshitij Gupta <kshitij@ti.com>
*
* Configuration settings for the phyCORE-i.MX31 board.
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*/
```

```
./include/configs/incaip.h
incaip.h
```

```
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* MA 02111-1307 USA
*/
```

```
./include/configs/inka4x0.h
```

```
inka4x0.h
```

```
/*
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*
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*/
```

```
./include/configs/integrator-common.h
```

```
integrator-common.h
```

```
/*
* (C) Copyright 2012
* Linaro
```

```
* Linus Walleij <linus.walleij@linaro.org>
* Common ARM Integrator configuration settings
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/integratorap.h
integratorap.h
```

```
/*
* (C) Copyright 2003
* Texas Instruments.
* Kshitij Gupta <kshitij@ti.com>
* Configuration settings for the TI OMAP Innovator board.
*
* (C) Copyright 2004
* ARM Ltd.
* Philippe Robin, <philippe.robin@arm.com>
* Configuration for Integrator AP board.
*
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```

```
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* MA 02111-1307 USA  
*/
```

```
./include/configs/integratorcp.h  
integratorcp.h
```

```
/*  
* (C) Copyright 2003  
* Texas Instruments.  
* Kshitij Gupta <kshitij@ti.com>  
* Configuration settings for the TI OMAP Innovator board.  
*  
* (C) Copyright 2004  
* ARM Ltd.  
* Philippe Robin, <philippe.robin@arm.com>  
* Configuration for Compact Integrator board.  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/intip.h  
intip.h
```

```
/*  
* (C) Copyright 2009  
* Dirk Eibach, Guntermann & Drunck GmbH, eibach@gdsys.de  
*  
* Based on include/configs/canyonlands.h  
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* MA 02111-1307 USA
*/
```

```
./include/configs/io.h
```

```
io.h
```

```
/*
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*/
```

```
./include/configs/io64.h
```

```
io64.h
```

```
/*
* (C) Copyright 2011
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*
* based on kilauea.h
* by Stefan Roese, DENX Software Engineering, sr@denx.de.
* and Grant Erickson <gerickson@nuovations.com>
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*/
```

```
./include/configs/iocon.h
```

```
iocon.h
```

```
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* MA 02111-1307 USA
*/
```

```
./include/configs/ip04.h
```

```
ip04.h
```

```
/*
* U-boot - Configuration file for IP04 board (having BF532 processor)
*
* Copyright (c) 2006 Intratrade Ltd., Ivan Danov, idanov@gmail.com
*
* Copyright (c) 2005-2010 Analog Devices Inc.
*/
```

```
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*
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*/
```

```
./include/configs/IP860.h
```

```
IP860.h
```

```
/*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
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*/
```

```
./include/configs/ipek01.h
```

```
ipek01.h
```

```
/*
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* MicroSys GmbH
*
* (C) Copyright 2009
* Wolfgang Grandegger, DENX Software Engineering, wg@denx.de.
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*/
```

```
./include/configs/IPHASE4539.h
IPHASE4539.h
```

```
/*
* (C) Copyright 2002 Wolfgang Grandegger <wg@denx.de>
*
* This file is based on similar values for other boards found in
* other U-Boot config files, mainly tqm8260.h and mpc8260ads.h.
*
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*/
```

```
./include/configs/ISPAN.h
ISPAN.h
```

```
/*
* Copyright (C) 2004 Arabella Software Ltd.
* Yuli Barcohen <yuli@arabellasw.com>
*
* Support for Interphase iSPAN Communications Controllers
* (453x and others). Tested on 4532.
*
* Derived from iSPAN 4539 port (iphase4539) by
* Wolfgang Grandegger <wg@denx.de>
*
* See file CREDITS for list of people who contributed to this
* project.
```

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*/
```

```
./include/configs/IVML24.h
IVML24.h
```

```
/*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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*/
```

```
./include/configs/IVMS8.h
IVMS8.h
```

```
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*/
```

```
./include/configs/jadecpu.h
```

```
jadecpu.h
```

```
/*
* (C) Copyright 2010
* Matthias Weisser <weisserm@arcor.de>
*
* Configuration settings for the jadecpu board
*
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* project.
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* MA 02111-1307 USA
*/
```

```
./include/configs/jornada.h
```

```
jornada.h
```

```
/*
* Copyright 2010 (C)
* Kristoffer Ericson <kristoffer.ericson@gmail.com>
*
*/
```

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*/
```

```
./include/configs/JSE.h
```

```
JSE.h
```

```
/*
```

```
* (C) Copyright 2003 Picture Elements, Inc.
```

```
* Stephen Williams <steve@icarus.com>
```

```
*
```

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*
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```

```
*/
```

```
./include/configs/jupiter.h
```

```
jupiter.h
```

```
/*
```

```
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```

```
*
```

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*
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*/
```

```
./include/configs/KAREF.h
```

```
KAREF.h
```

```
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*/
```

```
./include/configs/katmai.h
```

```
katmai.h
```

```
/*
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*
* (C) Copyright 2004 Paul Reynolds <PaulReynolds@lhsolutions.com>
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*/
```

```
./include/configs/kilauea.h
```

```
kilauea.h
```

```
/*
* Copyright (c) 2008 Nuovation System Designs, LLC
* Grant Erickson <gerickson@nuovations.com>
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*/
```

```
./include/configs/km/keymile-common.h
```

```
keymile-common.h
```

```
/*
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```

```
./include/configs/km/km-powerpc.h
km-powerpc.h
```

```
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*/
```

```
./include/configs/km/km8309-common.h
km8309-common.h
```

```
/*
* Copyright (C) 2012 Keymile AG
* Gerlando Falauto <gerlando.falauto@keymile.com>
```

```
*
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```

```
./include/configs/km/km8321-common.h
km8321-common.h
```

```
/*
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*           Peter Barada <peterb@logicpd.com>
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*/
```

```
./include/configs/km/km83xx-common.h
km83xx-common.h
```

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```

```
./include/configs/km/km_arm.h
km_arm.h
```

```
/*
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 * Prafulla Wadaskar <prafulla@marvell.com>
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 */
```

```
./include/configs/km82xx.h
km82xx.h
```

```
/*
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*/
```

```
./include/configs/km8360.h
```

```
km8360.h
```

```
/*
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* Christian Herzig, Keymile AG Switzerland, <christian.herzig@keymile.com>
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*/
```

```
./include/configs/km_kirkwood.h
```

```
km_kirkwood.h
```

```
/*
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* Marvell Semiconductor <www.marvell.com>
* Prafulla Wadaskar <prafulla@marvell.com>
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*/
```

./include/configs/korat.h

korat.h

/*

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* Larry Johnson, lrj@acm.org

*

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*

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*/

./include/configs/KUP4K.h

KUP4K.h

/*

* Wolfgang Denk, DENX Software Engineering, wd@denx.de.

* Klaus Heydeck, Kieback & Peter GmbH & Co KG, heydeck@kieback-peter.de

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*/
```

```
./include/configs/KUP4X.h
```

```
KUP4X.h
```

```
/*
```

```
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```

```
*/
```

```
./include/configs/kvme080.h
```

```
kvme080.h
```

```
/*
```

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* Sangmoon Kim, dogoil@etinsys.com.
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```

```
./include/configs/kzm9g.h
```

```
kzm9g.h
```

```
/*
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*/
```

```
./include/configs/lacie_kw.h
```

```
lacie_kw.h
```

```
/*
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```

```
./include/configs/linkstation.h
```

```
linkstation.h
```

```
/*
```

```
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*/
```

```
./include/configs/lxsl.h
```

```
lxsl.h
```

```
/*
```

```
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* Michael Walle <michael@walle.cc>
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*/
```

```
./include/configs/luan.h
```

```
luan.h
```

```
/*
```

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* John Otken, jotken@softadvances.com
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*/
```

```
./include/configs/lubbock.h
```

```
lubbock.h
```

```
/*
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* Kyle Harris, Nexus Technologies, Inc. kharris@nexus-tech.net
*
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* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*
* Configuration settings for the LUBBOCK board.
*
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*/
```

```
./include/configs/lwmon.h
```

```
lwmon.h
```

```
/*
```

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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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```

```
*/
```

```
./include/configs/lwmon5.h
```

```
lwmon5.h
```

```
/*
```

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```
*/
```

```
./include/configs/m28evk.h
```

```
m28evk.h
```

```
/*
```

```
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*/
```

```
./include/configs/M5208EVBE.h
```

```
M5208EVBE.h
```

```
/*
* Configuration settings for the Freescale MCF5208EVBe.
*
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*/
```

```
./include/configs/M52277EVB.h
```

```
M52277EVB.h
```

```
/*
* Configuration settings for the Freescale MCF52277 EVB board.
```

```
*
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*/
```

```
./include/configs/M5235EVB.h
M5235EVB.h
```

```
/*
* Configuration settings for the Freescale MCF5329 FireEngine board.
*
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*/
```

./include/configs/M5249EVB.h

M5249EVB.h

```
/*
 * Configuration settings for the esd TASREG board.
 *
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 */
```

./include/configs/M5253DEMO.h

M5253DEMO.h

```
/*
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 * Hayden Fraser (Hayden.Fraser@freescale.com)
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 */
```

```
*/  
  
./include/configs/M5253EVBE.h  
M5253EVBE.h  
/*  
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* MA 02111-1307 USA  
*/  
  
./include/configs/M5271EVB.h  
M5271EVB.h  
/*  
* Configuration settings for the Freescale M5271EVB  
*  
* Based on MC5272C3 and r5200 board configs  
* (C) Copyright 2006 Lab X Technologies <zachary.landau@labxtechnologies.com>  
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*/
```

```
./include/configs/M5272C3.h
```

```
M5272C3.h
```

```
/*
* Configuration settings for the Motorola MC5272C3 board.
*
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*/
```

```
./include/configs/M5275EVB.h
```

```
M5275EVB.h
```

```
/*
* Configuration settings for the Motorola MC5275EVB board.
*
* By Arthur Shipkowski <art@videon-central.com>
* Copyright (C) 2005 Videon Central, Inc.
*
* Based off of M5272C3 board code by Josef Baumgartner
* <josef.baumgartner@telex.de>
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*/
```

```
./include/configs/M5282EVB.h
```

```
M5282EVB.h
```

```
/*  
* Configuration settings for the Motorola MC5282EVB board.  
*  
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*/
```

```
./include/configs/M53017EVB.h
```

```
M53017EVB.h
```

```
/*  
* Configuration settings for the Freescale MCF53017EVB.  
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*/
```

```
./include/configs/M5329EVB.h
M5329EVB.h
```

```
/*
* Configuration settings for the Freescale MCF5329 FireEngine board.
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*/
```

```
./include/configs/M5373EVB.h
M5373EVB.h
```

```
/*
* Configuration settings for the Freescale MCF5373 FireEngine board.
*
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```

```
./include/configs/M54418TWR.h
```

```
M54418TWR.h
```

```
/*  
* Configuration settings for the Freescale MCF54418 TWR board.  
*  
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*/
```

```
./include/configs/M54451EVB.h
```

```
M54451EVB.h
```

```
/*  
* Configuration settings for the Freescale MCF54451 EVB board.  
*  
*/
```

```
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```

```
./include/configs/M54455EVB.h
```

```
M54455EVB.h
```

```
/*
* Configuration settings for the Freescale MCF54455 EVB board.
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```

```
./include/configs/M5475EVB.h
```

M5475EVB.h

```
/*
 * Configuration settings for the Freescale MCF5475 board.
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```

./include/configs/M5485EVB.h

M5485EVB.h

```
/*
 * Configuration settings for the Freescale MCF5485 FireEngine board.
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*/

./include/configs/magnesium.h
magnesium.h

```
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 *
 * based on:
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 */
```

./include/configs/makalu.h
makalu.h

```
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 * Grant Erickson <gerickson@nuovations.com>
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```

```
./include/configs/manroland/common.h
common.h
```

```
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*/
```

```
./include/configs/manroland/mpc5200-common.h
mpc5200-common.h
```

```
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./include/configs/MBX.h
MBX.h
/*
* (C) Copyright 2000
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*
* Configuration settings for the MBX8xx board.
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
/*
* Changed 2002-10-01
* Added PCMCIA defines mostly taken from other U-Boot boards that
* have PCMCIA already working. If you find any bugs, incorrect assumptions
* feel free to fix them yourself and submit a patch.
* Rod Boyce <rod_boyce@stratexnet.com>
*/
/*
* board/config.h - configuration options, board specific
*/

./include/configs/MBX860T.h
MBX860T.h
/* ----- */

./include/configs/mcc200.h
mcc200.h

```

```
/*
 * (C) Copyright 2006-2008
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
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 * project.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./include/configs/mcx.h
```

```
mcx.h
```

```
/*
 * Copyright (C) 2011 Ilya Yanok, Emcraft Systems
 *
 * Based on omap3_evm_config.h
 *
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 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc.
 */
```

```
./include/configs/mecp5123.h
```

```
mecp5123.h
```

```
/*
 * (C) Copyright 2009 Wolfgang Denk <wd@denx.de>
 * (C) Copyright 2009, DAVE Srl <www.dave.eu>
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
* modifications for the MECP5123 by reinhard.arlt@esd-electronics.com
*
*/
```

```
./include/configs/mecp5200.h
mecp5200.h
```

```
/*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/medcom-wide.h
medcom-wide.h
```

```
/*
 * (C) Copyright 2010,2011
 * NVIDIA Corporation <www.nvidia.com>
 * (C) Copyright 2011-2012
 * Avionic Design GmbH <www.avionic-design.de>
 *
 * See file CREDITS for list of people who contributed to this
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./include/configs/meesc.h
meesc.h
```

```
/*
 * (C) Copyright 2007-2008
 * Stelian Pop <stelian@popies.net>
 * Lead Tech Design <www.leadtechdesign.com>
 *
 * (C) Copyright 2009-2011
 * Daniel Gorsulowski <daniel.gorsulowski@esd.eu>
 * esd electronic system design gmbh <www.esd.eu>
 *
 * Configuration settings for the esd MEESC board.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/MERGERBOX.h
```

```
MERGERBOX.h
```

```
/*
* Copyright (C) 2007 Freescale Semiconductor, Inc.
*
* Copyright (C) 2011 Matrix Vision GmbH
* Andre Schwarz <andre.schwarz@matrix-vision.de>
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/METROBOX.h
```

```
METROBOX.h
```

```
/*
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*
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```

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* MA 02111-1307 USA
*/
```

```
./include/configs/MHPC.h
```

```
MHPC.h
```

```
/*
```

```
* (C) Copyright 2001
* Frank Gottschling, ELTEC Elektronik AG, fgottschling@eltec.de
```

```
*
* (C) Copyright 2001
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
```

```
* Configuration settings for the miniHiPerCam.
```

```
*
```

```
* -----
```

```
* See file CREDITS for list of people who contributed to this
* project.
```

```
*
```

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```

```
*/
```

```
./include/configs/microblaze-generic.h
```

```
microblaze-generic.h
```

```
/*
```

```
* (C) Copyright 2007-2010 Michal Simek
```

```
*
```

```
* Michal SIMEK <monstr@monstr.eu>
```

```
*
```

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* See file CREDITS for list of people who contributed to this
* project.
```

```
*
```

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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/MigoR.h
```

```
MigoR.h
```

```
/*
* Configuration settings for the Renesas Solutions Migo-R board
*
* Copyright (C) 2007 Nobuhiro Iwamatsu <iwamatsu@nigauri.org>
*
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* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/mimc200.h
```

```
mimc200.h
```

```
/*
* Copyright (C) 2006 Atmel Corporation
*
* Configuration settings for the AVR32 Network Gateway
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/mini2440.h
```

```
mini2440.h
```

```
/*
* (C) Copyright 2002
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
* Gary Jennejohn <gj@denx.de>
* David Mueller <d.mueller@elsoft.ch>
*
* (C) Copyright 2009-2010
* Michel Pollet <buserror@gmail.com>
*
* (C) Copyright 2012
* Gabriel Huau <contact@huau-gabriel.fr>
*
* Configuration settings for the MINI2440 board.
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

./include/configs/MIP405.h

MIP405.h

```
/*
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./include/configs/m1507.h

m1507.h

```
/*
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 */
```

./include/configs/motionpro.h

motionpro.h

```
/*
 * (C) Copyright 2003-2007
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
```

```
* Based on Motion-PRO board config file by Robert McCullough, rob@promessinc.com
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/MOUSSE.h
```

```
MOUSSE.h
```

```
/*
* (C) Copyright 2000, 2001
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* (C) Copyright 2001
* James F. Dougherty (jfd@cs.stanford.edu)
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/mpc5121-common.h
```

mpc5121-common.h

```
/*
 * (C) Copyright 2010 DENX Software Engineering
 * Anatolij Gustschin <agust@denx.de>
 *
 * Common configuration options for MPC5121 based boards
 *
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 */
```

./include/configs/mpc5121ads.h

mpc5121ads.h

```
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 * MA 02111-1307 USA
 */
```

./include/configs/mpc7448hpc2.h

mpc7448hpc2.h

```
/*
```

```
* Copyright (c) 2005 Freescale Semiconductor, Inc.
*
* (C) Copyright 2006
* Alex Bounine , Tundra Semiconductor Corp.
* Roy Zang      , <tie-fei.zang@freescale.com> Freescale Corp.
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/MPC8260ADS.h
MPC8260ADS.h
```

```
/*
* (C) Copyright 2001
* Stuart Hughes <stuarth@lineo.com>
* This file is based on similar values for other boards found in other
* U-Boot config files, and some that I found in the mpc8260ads manual.
*
* Note: my board is a PILOT rev.
* Note: the mpc8260ads doesn't come with a proper Ethernet MAC address.
*
* (C) Copyright 2003-2004 Arabella Software Ltd.
* Yuli Barcohen <yuli@arabellasw.com>
* Added support for SDRAM DIMMs SPD EEPROM, MII, JFFS2.
* Ported to PQ2FADS-ZU and PQ2FADS-VR boards.
* Ported to MPC8272ADS board.
*
* Copyright (c) 2005 MontaVista Software, Inc.
* Vitaly Bordug <vbordug@ru.mvista.com>
* Added support for PCI bridge on MPC8272ADS
*
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```

```
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* MA 02111-1307 USA
*/

./include/configs/MPC8266ADS.h
MPC8266ADS.h
/*
* (C) Copyright 2001
* Stuart Hughes <stuarth@lineo.com>
* This file is based on similar values for other boards found in other
* U-Boot config files, and some that I found in the mpc8260ads manual.
*
* Note: my board is a PILOT rev.
* Note: the mpc8260ads doesn't come with a proper Ethernet MAC address.
*
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* MA 02111-1307 USA
*/

./include/configs/MPC8308RDB.h
MPC8308RDB.h
/*
```

```
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* Copyright (C) 2010 Ilya Yanok, Emcraft Systems, yanok@emcraft.com
*
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*/
```

```
./include/configs/mpc8308_p1m.h
mpc8308_p1m.h
```

```
/*
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* Copyright (C) 2010 Ilya Yanok, Emcraft Systems, yanok@emcraft.com
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/MPC8313ERDB.h
```

MPC8313ERDB.h

```
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 * MA 02111-1307 USA
 */
/*
 * mpc8313epb board configuration file
 */
```

./include/configs/MPC8315ERDB.h

MPC8315ERDB.h

```
/*
 * Copyright (C) 2007-2010 Freescale Semiconductor, Inc.
 *
 * Dave Liu <daveliu@freescale.com>
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```

```
*/

./include/configs/MPC8323ERDB.h
MPC8323ERDB.h
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 */
/*
 * High Level Configuration Options
 */

./include/configs/MPC832XEMDS.h
MPC832XEMDS.h
/*
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 */

./include/configs/MPC8349EMDS.h
MPC8349EMDS.h
/*
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*/
```

```
./include/configs/MPC8349ITX.h
```

```
MPC8349ITX.h
```

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*/
```

```
./include/configs/MPC8360EMDS.h
```

```
MPC8360EMDS.h
```

```
/*  
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* Dave Liu <daveliu@freescale.com>  
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*/
```

```
./include/configs/MPC8360ERDK.h
```

```
MPC8360ERDK.h
```

```
/*
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 *
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 *           Peter Barada <peterb@logicpd.com>
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*/
```

```
./include/configs/MPC837XEMDS.h
```

```
MPC837XEMDS.h
```

```
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 *           Dave Liu <daveliu@freescale.com>
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```

```
./include/configs/MPC837XERDB.h
```

```
MPC837XERDB.h
```

```
/*
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 * Kevin Lam <kevin.lam@freescale.com>
 * Joe D'Abbraccio <joe.d'abbraccio@freescale.com>
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 */
```

```
./include/configs/MPC8536DS.h
MPC8536DS.h
```

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 */
```

```
./include/configs/MPC8540ADS.h
MPC8540ADS.h
```

```
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```

```
* Xianghua Xiao <X.Xiao@motorola.com>
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```

```
./include/configs/MPC8541CDS.h
```

```
MPC8541CDS.h
```

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```

```
./include/configs/MPC8544DS.h
```

```
MPC8544DS.h
```

```
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```

```
./include/configs/MPC8548CDS.h
```

```
MPC8548CDS.h
```

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```

```
./include/configs/MPC8555CDS.h
```

```
MPC8555CDS.h
```

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```

```
./include/configs/MPC8560ADS.h
MPC8560ADS.h
```

```
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```
./include/configs/MPC8568MDS.h
MPC8568MDS.h
```

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```

```
./include/configs/MPC8569MDS.h
MPC8569MDS.h
```

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*/
```

```
./include/configs/MPC8572DS.h
MPC8572DS.h
```

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```

```
./include/configs/MPC8610HPCD.h
MPC8610HPCD.h
```

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/*
 * MPC8610HPCD board configuration file
 */
```

```
./include/configs/MPC8641HPCN.h
MPC8641HPCN.h
```

```
/*
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 *
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*/
```

```
./include/configs/MPC86xADS.h  
MPC86xADS.h  
/*
```

```
./include/configs/MPC885ADS.h  
MPC885ADS.h  
/*
```

```
* A collection of structures, addresses, and values associated with  
* the Motorola MPC885ADS board. Values common to all FADS family boards  
* are in board/fads/fads.h  
*  
* Copyright (C) 2003-2004 Arabella Software Ltd.  
* Yuli Barcohen <yuli@arabellasw.com>  
*/
```

```
./include/configs/mpq101.h  
mpq101.h  
/*
```

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```

```
./include/configs/mpr2.h  
mpr2.h  
/*
```

```
* Configuration settings for MPR2  
*  
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* Mark Jonas <mark.jonas@de.bosch.com>
```

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```

```
./include/configs/ms7720se.h
ms7720se.h
```

```
/*
* Configuration settings for the Hitachi Solution Engine 7720
*
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*/
```

```
./include/configs/ms7722se.h
ms7722se.h
```

```
/*
* Configuration settings for the Hitachi Solution Engine 7722
```

```
*
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```
./include/configs/ms7750se.h
```

```
ms7750se.h
```

```
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* Configuration settings for the Hitachi Solution Engine 7750
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*/
```

```
./include/configs/mt_ventoux.h
```

```
mt_ventoux.h
```

```
/*
 * Copyright (C) 2011
 * Stefano Babic, DENX Software Engineering, sbabic@denx.de.
 *
 *
 * Configuration settings for the Teejet mt_ventoux board.
 *
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```

```
./include/configs/muas3001.h
muas3001.h
```

```
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 */
```

```
./include/configs/mucmc52.h
```

mucmc52.h

```
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 */
```

./include/configs/munices.h

munices.h

```
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```
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./include/configs/MUSENKI.h  
MUSENKI.h  
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*/
```

```
./include/configs/mv-common.h  
mv-common.h  
/*  
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*/

./include/configs/mv88f6281gtw_ge.h

mv88f6281gtw_ge.h

/*

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*/

./include/configs/MVBC_P.h

MVBC_P.h

/*

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```

```
./include/configs/MVBLM7.h
```

```
MVBLM7.h
```

```
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*
* Matrix Vision mvBlueLYNX-M7 configuration file
* based on Freescale's MPC8349ITX.
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```

```
./include/configs/MVBLUE.h
```

```
MVBLUE.h
```

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```

```
./include/configs/MVS1.h
```

```
MVS1.h
```

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```

```
./include/configs/MVSMR.h
```

```
MVSMR.h
```

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*/
```

```
./include/configs/mx1ads.h
mx1ads.h
```

```
/*
* include/configs/mx1ads.h
*
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* Techware Information Technology, Inc.
* http://www.techware.com.tw/
*
* Ming-Len Wu <minglen_wu@techware.com.tw>
*
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```
./include/configs/mx25pdk.h
mx25pdk.h
```

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```

```
./include/configs/mx28evk.h
mx28evk.h
```

```
/*
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*
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```
./include/configs/mx31ads.h
mx31ads.h
```

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```

./include/configs/mx31pdk.h

mx31pdk.h

```
/*
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 *
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 * Richard Woodruff <r-woodruff2@ti.com>
 * Kshitij Gupta <kshitij@ti.com>
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```

./include/configs/mx35pdk.h

mx35pdk.h

```
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```
./include/configs/mx51evk.h
mx51evk.h
```

```
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```
./include/configs/mx51_efikamx.h
mx51_efikamx.h
```

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```
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```

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```
./include/configs/mx53evk.h
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```

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```
./include/configs/mx53loco.h
```

```
mx53loco.h
```

```
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```

```
./include/configs/mx53smd.h
```

```
mx53smd.h
```

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```

```
*/  
  
./include/configs/mx6qarm2.h  
mx6qarm2.h  
/*  
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```

```
./include/configs/mx6qsabreauto.h  
mx6qsabreauto.h  
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```

```
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mx6qsabrelite.h  
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```
./include/configs/mx6qsabresd.h
```

```
mx6qsabresd.h
```

```
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```
./include/configs/mx6qsabre_common.h
```

```
mx6qsabre_common.h
```

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```

```
./include/configs/neo.h
```

```
neo.h
```

```
/*
```

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```
./include/configs/NETPHONE.h
NETPHONE.h
```

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```

```
./include/configs/NETTA.h
NETTA.h
/*
```

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```
./include/configs/NETTA2.h
```

```
NETTA2.h
```

```
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```

```
./include/configs/NETVIA.h
```

```
NETVIA.h
```

```
/*
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/nhk8815.h
nhk8815.h
```

```
/*
* (C) Copyright 2005
* STMicroelectronics.
* Configuration settings for the "Nomadik Hardware Kit" NHK-8815,
* the evaluation board for the Nomadik 8815 System on Chip.
*
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* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/nios2-generic.h
```

nios2-generic.h

```
/*
 * (C) Copyright 2005, Psyent Corporation <www.psyent.com>
 * Scott McNutt <smcnutt@psyent.com>
 * (C) Copyright 2010, Thomas Chou <thomas@wytron.com.tw>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./include/configs/nokia_rx51.h

nokia_rx51.h

```
/*
 * (C) Copyright 2011-2012
 * Pali Rohár <pali.rohar@gmail.com>
 *
 * (C) Copyright 2010
 * Alistair Buxton <a.j.buxton@gmail.com>
 *
 * Derived from Beagle Board code:
 * (C) Copyright 2006-2008
 * Texas Instruments.
 * Richard Woodruff <r-woodruff2@ti.com>
 * Syed Mohammed Khasim <x0khasim@ti.com>
 *
 * Configuration settings for the Nokia RX-51 aka N900.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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 * the License, or (at your option) any later version.
 */
```

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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/ns9750dev.h
```

```
ns9750dev.h
```

```
/*  
* Copyright (C) 2004 by FS Forth-Systeme GmbH.  
* All rights reserved.  
* Markus Pietrek <mpietrek@fsforth.de>  
*  
* Configuration settings for the NetSilicon NS9750 DevBoard  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/NSCU.h
```

```
NSCU.h
```

```
/*  
* (C) Copyright 2000-2008  
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/NX823.h
```

```
NX823.h
```

```
/*
* (C) Copyright 2001
* Kyle Harris, Nexus Technologies, Inc. kharris@nexus-tech.net
*
* (C) Copyright 2001
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/o2d.h
```

```
o2d.h
```

```
/*
* (C) Copyright 2012
* DENX Software Engineering, Anatolij Gustschin <agust@denx.de>
*
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```

```
*/
```

```
/*
```

```
* Valid values for CONFIG_SYS_TEXT_BASE are:
* 0xFC000000 boot low boot high (standard configuration)
* 0x00100000 boot from RAM (for testing only)
```

```
*/
```

```
./include/configs/o2d300.h
```

```
o2d300.h
```

```
/*
```

```
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```
*
```

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```

```
*/
```

```
/*
```

```
* Valid values for CONFIG_SYS_TEXT_BASE are:
* 0xFC000000 boot low boot high (standard configuration)
* 0x00100000 boot from RAM (for testing only)
```

```
*/
```

```
./include/configs/o2dnt-common.h
```

```
o2dnt-common.h
```

```
/*
```

```
* Common configuration options for ifm camera boards
```

```
*
```

```
* (C) Copyright 2005
* Sebastien Cazaux, ifm electronic gmbh
```

```
*
```

```
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* DENX Software Engineering, Anatolij Gustschin <agust@denx.de>
```

```
*
```

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```

```
*
```

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*
```

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*
```

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* MA 02111-1307 USA
*/
```

```
./include/configs/o2dnt2.h
```

```
o2dnt2.h
```

```
/*
 * (C) Copyright 2012
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 * published by the Free Software Foundation; either version 2 of
 * the License, or (at your option) any later version.
 */
/*
 * Valid values for CONFIG_SYS_TEXT_BASE are:
 * 0xFC000000 boot low boot high (standard configuration)
 * 0x00100000 boot from RAM (for testing only)
 */
```

```
./include/configs/o2i.h
```

```
o2i.h
```

```
/*
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 * DENX Software Engineering, Anatolij Gustschin <agust@denx.de>
 *
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 * the License, or (at your option) any later version.
 */
/*
 * Valid values for CONFIG_SYS_TEXT_BASE are:
 * 0xFF000000 boot low boot high (standard configuration)
 * 0x00100000 boot from RAM (for testing only)
 */
```

```
./include/configs/o2mnt.h
```

```
o2mnt.h
```

```
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 * the License, or (at your option) any later version.
 */
```

```
*/
/*
* Valid values for CONFIG_SYS_TEXT_BASE are:
* 0xFF000000 boot low boot high (standard configuration)
* 0x00100000 boot from RAM (for testing only)
*/

./include/configs/o3dnt.h
o3dnt.h
/*
* (C) Copyright 2012
* DENX Software Engineering, Anatolij Gustschin <agust@denx.de>
*
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* the License, or (at your option) any later version.
*/
/*
* Valid values for CONFIG_SYS_TEXT_BASE are:
* 0xFC000000 boot low boot high (standard configuration)
* 0x00100000 boot from RAM (for testing only)
*/

./include/configs/ocotea.h
ocotea.h
/*
* (C) Copyright 2004 Paul Reynolds <PaulReynolds@lhsolutions.com>
*
* (C) Copyright 2005
* Stefan Roese, DENX Software Engineering, sr@denx.de.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

./include/configs/OCRTC.h

OCRTC.h

/*

* (C) Copyright 2001
* Stefan Roese, esd gmbh germany, stefan.roese@esd-electronics.com

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*/

./include/configs/omap1510.h

omap1510.h

/*

*

* BRIEF MODULE DESCRIPTION

* OMAP hardware map

*

* Copyright (C) 2001 RidgeRun, Inc. (<http://www.ridgerun.com>)

* Author: RidgeRun, Inc.

* Greg Lonnon (glonnon@ridgerun.com) or info@ridgerun.com

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* 675 Mass Ave, Cambridge, MA 02139, USA.

*/

./include/configs/omap1510inn.h

omap1510inn.h

/*

* (C) Copyright 2003
* Texas Instruments.
* Kshitij Gupta <kshitij@ti.com>
* Configuration settings for the TI OMAP Innovator board.

*

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* MA 02111-1307 USA

*/

./include/configs/omap2420h4.h

omap2420h4.h

/*

* (C) Copyright 2004
* Texas Instruments.
* Richard Woodruff <r-woodruff2@ti.com>
* Kshitij Gupta <kshitij@ti.com>

*

* Configuration settings for the 242x TI H4 board.

*

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* project.

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* MA 02111-1307 USA
*/
```

```
./include/configs/omap3_beagle.h
```

```
omap3_beagle.h
```

```
/*
* (C) Copyright 2006-2008
* Texas Instruments.
* Richard Woodruff <r-woodruff2@ti.com>
* Syed Mohammed Khasim <x0khasim@ti.com>
*
* Configuration settings for the TI OMAP3530 Beagle board.
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/omap3_evm.h
```

```
omap3_evm.h
```

```
/*
* Configuration settings for the TI OMAP3 EVM board.
*
* Copyright (C) 2006-2011 Texas Instruments Incorporated - http://www.ti.com/
*/
```

```
* Author :
*   Manikandan Pillai <mani.pillai@ti.com>
* Derived from Beagle Board and 3430 SDP code by
*   Richard Woodruff <r-woodruff2@ti.com>
*   Syed Mohammed Khasim <khasim@ti.com>
*
* Manikandan Pillai <mani.pillai@ti.com>
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/omap3_evm_common.h
```

```
omap3_evm_common.h
```

```
/*
* Common configuration settings for the TI OMAP3 EVM board.
*
* Copyright (C) 2006-2011 Texas Instruments Incorporated - http://www.ti.com/
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* kind, whether express or implied; without even the implied warranty
* of MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
* GNU General Public License for more details.
*/
```

```
./include/configs/omap3_evm_quick_mmc.h
```

```
omap3_evm_quick_mmc.h
```

```
/*
* Configuration settings for quick boot from MMC on OMAP3 EVM.
*
*/
```

```
* Copyright (C) 2006-2010 Texas Instruments Incorporated - http://www.ti.com/
*
* Author :
*   Sanjeev Premi <premi@ti.com>
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*/
```

```
./include/configs/omap3_evm_quick_nand.h
omap3_evm_quick_nand.h
```

```
/*
* Configuration settings for quick boot from NAND on OMAP3 EVM.
*
* Copyright (C) 2006-2010 Texas Instruments Incorporated - http://www.ti.com/
*
* Author :
*   Sanjeev Premi <premi@ti.com>
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* kind, whether express or implied; without even the implied warranty
* of MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
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*/
```

```
./include/configs/omap3_logic.h
omap3_logic.h
```

```
/*
* (C) Copyright 2011 Logic Product Development <www.logicpd.com>
*   Peter Barada <peter.barada@logicpd.com>
*
* Configuration settings for the Logic OMAP35x/DM37x SOM LV/Torpedo
* reference boards.
*
* See file CREDITS for list of people who contributed to this
* project.
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* MA 02111-1307 USA
*/
```

```
./include/configs/omap3_mvblx.h
omap3_mvblx.h
```

```
/*
 * MATRIX VISION GmbH mvBlueLYNX-X
 *
 * Derived from omap3_beagle.h:
 * (C) Copyright 2006-2008
 * Texas Instruments.
 * Richard Woodruff <r-woodruff2@ti.com>
 * Syed Mohammed Khasim <x0khasim@ti.com>
 *
 * Configuration settings for the TI OMAP3530 Beagle board.
 *
 * See file CREDITS for list of people who contributed to this
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 * MA 02111-1307 USA
*/
```

```
./include/configs/omap3_overo.h
omap3_overo.h
/*
```

```
* Configuration settings for the Gumstix Overo board.
*
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* Foundation, Inc.
*/
```

```
./include/configs/omap3_pandora.h
```

```
omap3_pandora.h
```

```
/*
* (C) Copyright 2008-2010
* Gražvydas Ignatas <notasas@gmail.com>
*
* Configuration settings for the OMAP3 Pandora.
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/omap3_sdp3430.h
```

```
omap3_sdp3430.h
```

```
/*
* (C) Copyright 2006-2009
* Texas Instruments Incorporated.
* Richard Woodruff <r-woodruff2@ti.com>
* Syed Mohammed Khasim <x0khasim@ti.com>
* Nishanth Menon <nm@ti.com>
*
*/
```

```
* Configuration settings for the 3430 TI SDP3430 board.
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/omap3_zoom1.h
```

```
omap3_zoom1.h
```

```
/*
* (C) Copyright 2006-2008
* Texas Instruments.
* Richard Woodruff <r-woodruff2@ti.com>
* Syed Mohammed Khasim <x0khasim@ti.com>
* Nishanth Menon <nm@ti.com>
*
* Configuration settings for the TI OMAP3430 Zoom MDK board.
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* MA 02111-1307 USA
*/
```

```
./include/configs/omap3_zoom2.h
omap3_zoom2.h
/*
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 * Texas Instruments.
 * Richard Woodruff <r-woodruff2@ti.com>
 * Syed Mohammed Khasim <x0khasim@ti.com>
 * Nishanth Menon <nm@ti.com>
 * Tom Rix <Tom.Rix@windriver.com>
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 * MA 02111-1307 USA
 */
```

```
./include/configs/omap4_common.h
omap4_common.h
/*
 * (C) Copyright 2010
 * Texas Instruments Incorporated.
 * Aneesh V <aneesh@ti.com>
 * Steve Sakoman <steve@sakoman.com>
 *
 * TI OMAP4 common configuration settings
 *
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 */
```

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* MA 02111-1307 USA  
*/
```

```
./include/configs/omap4_panda.h
```

```
omap4_panda.h
```

```
/*  
* (C) Copyright 2010  
* Texas Instruments Incorporated.  
* Steve Sakoman <steve@sakoman.com>  
*  
* Configuration settings for the TI OMAP4 Panda board.  
* See omap4_common.h for OMAP4 common part  
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* MA 02111-1307 USA  
*/
```

```
./include/configs/omap4_sdp4430.h
```

```
omap4_sdp4430.h
```

```
/*  
* (C) Copyright 2010  
* Texas Instruments Incorporated.  
* Aneesh V <aneesh@ti.com>  
* Steve Sakoman <steve@sakoman.com>  
*  
* Configuration settings for the TI SDP4430 board.  
* See omap4_common.h for OMAP4 common part
```

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*/
```

```
./include/configs/omap5912osk.h
omap5912osk.h
```

```
/*
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* Kshitij Gupta <kshitij@ti.com>
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* MA 02111-1307 USA
*/
```

```
./include/configs/omap5_evm.h
omap5_evm.h
/*
```

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* Sricharan R <r.sricharan@ti.com>
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/omap730.h
omap730.h
```

```
/*
*
* BRIEF MODULE DESCRIPTION
* OMAP730 hardware map
*
* Copyright (C) 2004 MPC-Data Limited. (http://www.mpc-data.co.uk)
* Author: MPC-Data Limited
* Dave Peeverley
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* 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./include/configs/omap730p2.h
```

```
omap730p2.h
```

```
/*
* (C) Copyright 2003-2004
* MPC Data Limited (http://www.mpc-data.co.uk)
* Dave Peverley <dpeverley at mpc-data.co.uk>
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/openrd.h
```

```
openrd.h
```

```
/*
* (C) Copyright 2009
* Net Insight <www.netinsight.net>
* Written-by: Simon Kagstrom <simon.kagstrom@netinsight.net>
*
* Based on sheevaplug.h:
* (C) Copyright 2009
* Marvell Semiconductor <www.marvell.com>
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
*/
```

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*/
```

```
./include/configs/openrisc-generic.h
```

```
openrisc-generic.h
```

```
/*
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*/
```

```
./include/configs/origen.h
```

```
origen.h
```

```
/*
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*
* Configuration settings for the SAMSUNG ORIGEN (EXYNOS4210) board.
*/
```

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*/
```

```
./include/configs/ORSG.h
```

```
ORSG.h
```

```
/*
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* MA 02111-1307 USA
*/
```

```
./include/configs/otc570.h
```

```
otc570.h
```

```
/*
* (C) Copyright 2010-2011
* Daniel Gorsulowski <daniel.gorsulowski@esd.eu>
* esd electronic system design gmbh <www.esd.eu>
```

```
*
* (C) Copyright 2007-2008
* Stelian Pop <stelian@popies.net>
* Lead Tech Design <www.leadtechdesign.com>
*
* Configuration settings for the esd OTC570 board.
*
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```

```
./include/configs/P1010RDB.h
```

```
P1010RDB.h
```

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*/
```

./include/configs/P1022DS.h

P1022DS.h

```
/*
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```

./include/configs/P1023RDS.h

P1023RDS.h

```
/*
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 *
 * Authors: Roy Zang <tie-fei.zang@freescale.com>
 *          Chunhe Lan <b25806@freescale.com>
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```

./include/configs/P1_P2_RDB.h

P1_P2_RDB.h

```
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```

```
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```

```
p1_p2_rdb_pc.h
```

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```

```
./include/configs/P2020COME.h
```

```
P2020COME.h
```

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```

```
./include/configs/P2020DS.h
P2020DS.h
```

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```
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P2041RDB.h
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```
./include/configs/P3041DS.h
```

```
P3041DS.h
```

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```

```
./include/configs/P3G4.h
```

```
P3G4.h
```

```
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```

```
./include/configs/p3mx.h
p3mx.h
```

```
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```
./include/configs/p3p440.h
p3p440.h
```

```
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```

```
./include/configs/P4080DS.h
P4080DS.h
```

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*/
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```
./include/configs/P5020DS.h
P5020DS.h
```

```
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*/
```

```
./include/configs/P5040DS.h
P5040DS.h
```

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*/
```

```
./include/configs/palmld.h
palmld.h
```

```
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* Palm LifeDrive configuration file
*
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*/
```

```
./include/configs/palmtc.h
palmtc.h
```

```
/*
* Palm Tungsten|C configuration file
*
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*/
```

```
./include/configs/PATI.h
PATI.h
```

```
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```
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./include/configs/paz00.h
paz00.h
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```
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pb1x00.h
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*/
```

```
./include/configs/PCI405.h
```

PCI405.h

```
/*
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 */
```

./include/configs/PCI5441.h

PCI5441.h

```
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```

```
*/  
  
./include/configs/PCIPPC2.h  
PCIPPC2.h  
/*  
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*/
```

```
./include/configs/PCIPPC6.h  
PCIPPC6.h  
/*  
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```

*/

./include/configs/pcm030.h
pcm030.h

/*

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* Eric Schumann, Phytex Messatechnik GmbH

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*/

./include/configs/pcs440ep.h
pcs440ep.h

/*

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*/
```

```
./include/configs/pdm360ng.h  
pdm360ng.h
```

```
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```

```
./include/configs/pdnb3.h  
pdnb3.h
```

```
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*  
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*/
```

```
./include/configs/pf5200.h
```

```
pf5200.h
```

```
/*
```

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```

```
*/
```

```
./include/configs/PIP405.h
```

```
PIP405.h
```

```
/*
```

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*/
```

```
./include/configs/PK1C20.h
```

```
PK1C20.h
```

```
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*/
```

```
./include/configs/PLU405.h
```

```
PLU405.h
```

```
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```

```
./include/configs/plutux.h
```

```
plutux.h
```

```
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```

```
./include/configs/PM520.h
```

```
PM520.h
```

```
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```
./include/configs/PM826.h
```

```
PM826.h
```

```
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*/
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```
./include/configs/PM828.h
```

```
PM828.h
```

```
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```

```
./include/configs/pm9261.h
pm9261.h
```

```
/*
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* Lead Tech Design <www.leadtechdesign.com>
* Ilko Iliev <www.ronetix.at>
*
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*/
```

```
./include/configs/pm9263.h
pm9263.h
```

```
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* Lead Tech Design <www.leadtechdesign.com>
* Ilko Iliev <www.ronetix.at>
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*/
```

```
./include/configs/pm9g45.h
```

```
pm9g45.h
```

```
/*
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* Asen Dimov <dimov@ronetix.at>
* Ronetix GmbH <www.ronetix.at>
*
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* MA 02111-1307 USA

*/

./include/configs/PMC405.h

PMC405.h

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*/

./include/configs/PMC405DE.h

PMC405DE.h

/*

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* Matthias Fuchs, esd gmbh germany, matthias.fuchs@esd.eu

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./include/configs/PMC440.h
PMC440.h

```
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 * Based on the sequoia configuration file.
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 * Alain Saurel, AMCC/IBM, alain.saurel@fr.ibm.com
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 * MA 02111-1307 USA
 */
```

./include/configs/PN62.h
PN62.h

```
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*/
```

```
./include/configs/pogo_e02.h
```

```
pogo_e02.h
```

```
/*  
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* David Purdy <david.c.purdy@gmail.com>  
*  
* Based on Kirkwood support:  
* (C) Copyright 2009  
* Marvell Semiconductor <www.marvell.com>  
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>  
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*/
```

```
./include/configs/PPChameleonEVB.h
```

```
PPChameleonEVB.h
```

```
/*  
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*  
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* DAVE Srl  
*  
* http://www.dave-tech.it  
* http://www.wawnet.biz  
* mailto:info@wawnet.biz  
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```

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*/
```

```
./include/configs/ppmc7xx.h
```

```
ppmc7xx.h
```

```
/*
* ppmc7xx.h
* -----
*
* Wind River PPMC 7xx/74xx board configuration file.
*
* By Richard Danter (richard.danter@windriver.com)
* Copyright (C) 2005 Wind River Systems
*/
```

```
./include/configs/ppmc8260.h
```

```
ppmc8260.h
```

```
/*
* (C) Copyright 2000
* Murray Jensen <Murray.Jensen@cmst.csiro.au>
*
* (C) Copyright 2000
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*
* (C) Copyright 2001
* Advent Networks, Inc. <http://www.adventnetworks.com>
* Jay Monkman <jtm@smoothsmoothie.com>
*
* Configuration settings for the WindRiver PPMC8260 board.
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/pr1.h
```

```
pr1.h
```

```
/*
* U-boot - Configuration file for PR1 Appliance
*
* based on bf537-stamp.h
* Copyright (c) Switchfin Org. <dpn@switchfin.org>
*/
```

```
./include/configs/pxa-common.h
```

```
pxa-common.h
```

```
/*
* Toradex Colibri PXA270 configuration file
*
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*/
```

```
./include/configs/pxa255_idp.h
```

```
pxa255_idp.h
```

```
/*
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```

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* Marius Groeger <mgroeger@sysgo.de>
*
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*
* (C) Copyright 2004
* BEC Systems <http://bec-systems.com>
* Cliff Brake <cliff.brake@gmail.com>
* Configuration settings for the Accelent/Vibren PXA255 IDP
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*/

./include/configs/qemu-mips.h

qemu-mips.h

/*

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```

```
./include/configs/qemu-mips64.h
```

```
qemu-mips64.h
```

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*/
```

```
./include/configs/qi_lb60.h
```

```
qi_lb60.h
```

```
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*/
```

```
./include/configs/qong.h
```

```
qong.h
```

```
/*
* Copyright (C) 2009, Ilya Yanok, Emcraft Systems, <yanok@emcraft.com>
*
* Configuration settings for the Dave/DENX QongEVB-LITE board.
*/
```

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*/
```

```
./include/configs/QS823.h
```

```
QS823.h
```

```
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*
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* Simple Network Magic Corporation
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*/
```

```
./include/configs/QS850.h
```

```
QS850.h
```

```
/*
```

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```

```
./include/configs/QS860T.h
QS860T.h
```

```
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*/
```

```
./include/configs/quad100hd.h
quad100hd.h
```

```
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```
./include/configs/quantum.h
quantum.h
```

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*/
```

```
./include/configs/r0p7734.h
r0p7734.h
```

```
/*
* Configuration settings for the Renesas Solutions r0p7734 board
*
* Copyright (C) 2010, 2011 Nobuhiro Iwamatsu <nobuhiro.iwamatsu.yj@renesas.com>
*
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*/
```

```
./include/configs/r2dplus.h
r2dplus.h
```

```
/*
* Command line configuration.
*/
```

```
./include/configs/R360MPI.h
R360MPI.h
```

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*/
```

```
./include/configs/r7780mp.h
```

```
r7780mp.h
```

```
/*
* Configuration settings for the Renesas R7780MP board
*
* Copyright (C) 2007,2008 Nobuhiro Iwamatsu <iwamatsu@nigauri.org>
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*/
```

```
./include/configs/Rattler.h
```

```
Rattler.h
```

```
/*
* Copyright (C) 2004 Arabella Software Ltd.
* Yuli Barcohen <yuli@arabellasw.com>
*
* U-Boot configuration for Analogue&Micro Rattler boards.
```

```
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* MA 02111-1307 USA
*/
```

```
./include/configs/RBC823.h
RBC823.h
```

```
/*
* (C) Copyright 2000, 2001
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* Modified by Udi Finkelstein udif@udif.com
* For the RBC823 board.
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*/
```

```
./include/configs/rd6281a.h
rd6281a.h
```

```
/*
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 * Marvell Semiconductor <www.marvell.com>
 * Written-by: Prafulla Wadaskar <prafulla@marvell.com>
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 */
```

```
./include/configs/redwood.h
redwood.h
```

```
/*
 * Configuration for AMCC 460SX Ref (redwood)
 *
 * (C) Copyright 2008
 * Feng Kan, Applied Micro Circuits Corp., fkan@amcc.com
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 */
```

```
./include/configs/rpi_b.h
```

```
rpi_b.h
```

```
/*
```

```
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```

```
*
```

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```

```
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```

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```

```
*/
```

```
./include/configs/RPXClassic.h
```

```
RPXClassic.h
```

```
/*
```

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```

```
*/
```

```
./include/configs/RPXlite.h
```

```
RPXlite.h
```

```
/*
```

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*/
```

```
./include/configs/RPXlite_DW.h
```

```
RPXlite_DW.h
```

```
/*
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* Sam Song, IEMC. SHU, samsongshu@yahoo.com.cn
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/RPXsuper.h
```

```
RPXsuper.h
```

```
/******
```

```
*
* These settings must match the way _your_ board is set up
```

```
*
*****/
/* for the AY-Revision which does not use the HRCW */
```

```
./include/configs/RRvision.h
RRvision.h
```

```
/*
* (C) Copyright 2000, 2001, 2002
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
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* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/rsdproto.h
rsdproto.h
```

```
/*
* (C) Copyright 2000
* Murray Jensen <Murray.Jensen@cmst.csiro.au>
*
* (C) Copyright 2000
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*
* Configuration settings for the R&S Protocol Board board.
*
* See file CREDITS for list of people who contributed to this
* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/rsk7203.h
```

```
rsk7203.h
```

```
/*
* Configuration settings for the Renesas Technology RSK 7203
*
* Copyright (C) 2008 Nobuhiro Iwamatsu
* Copyright (C) 2008 Renesas Solutions Corp.
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/rsk7264.h
```

```
rsk7264.h
```

```
/*
* Configuration settings for the Renesas RSK2+SH7264 board
*
* Copyright (C) 2011 Renesas Electronics Europe Ltd.
* Copyright (C) 2008 Nobuhiro Iwamatsu
* Copyright (C) 2008 Renesas Solutions Corp.
*
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* See the file COPYING in the root directory of the source tree for details.
*/
```

./include/configs/rsk7269.h

rsk7269.h

```
/*
 * Configuration settings for the Renesas RSK2+SH7269 board
 *
 * Copyright (C) 2012 Renesas Electronics Europe Ltd.
 * Copyright (C) 2012 Phil Edworthy
 *
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 * See the file COPYING in the root directory of the source tree for details.
 */
```

./include/configs/s5pc210_universal.h

s5pc210_universal.h

```
/*
 * Copyright (C) 2010 Samsung Electronics
 * Minkyu Kang <mk7.kang@samsung.com>
 *
 * Configuration settings for the SAMSUNG Universal (EXYNOS4210) board.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./include/configs/s5p_goni.h

s5p_goni.h

```
/*
 * Copyright (C) 2009 Samsung Electronics
 * Minkyu Kang <mk7.kang@samsung.com>
 * Kyungmin Park <kyungmin.park@samsung.com>
 *
 * Configuration settings for the SAMSUNG Universal (s5pc100) board.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
```

```
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/sacsng.h
sacsng.h
```

```
/*
* (C) Copyright 2000
* Murray Jensen <Murray.Jensen@cmst.csiro.au>
*
* (C) Copyright 2000
* Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Marius Groeger <mgroeger@sysgo.de>
*
* (C) Copyright 2001
* Advent Networks, Inc. <http://www.adventnetworks.com>
* Jay Monkman <jtm@smoothsmoothie.com>
*
* Configuration settings for the SACSng 8260 board.
*
* See file CREDITS for list of people who contributed to this
* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
```

*/

./include/configs/sandbox.h

sandbox.h

/*

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* See file CREDITS for list of people who contributed to this project.

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*/

./include/configs/Sandpoint8240.h

Sandpoint8240.h

/*

* (C) Copyright 2001-2005

* Wolfgang Denk, DENX Software Engineering, wd@denx.de.

*

* See file CREDITS for list of people who contributed to this project.

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*/

./include/configs/Sandpoint8245.h

Sandpoint8245.h

```
/*
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 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
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 * project.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./include/configs/sbc35_a9g20.h

sbc35_a9g20.h

```
/*
 * Copyright (C) 2009
 * Albin Tonnerre, Free Electrons <albin.tonnerre@free-electrons.com>
 *
 * Configuration settings for the Calao SBC35-A9G20 board
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
*/  
  
./include/configs/sbc405.h  
sbc405.h  
/*  
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*  
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* project.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/sbc8349.h  
sbc8349.h  
/*  
* WindRiver SBC8349 U-Boot configuration file.  
* Copyright (c) 2006, 2007 Wind River Systems, Inc.  
*  
* Paul Gortmaker <paul.gortmaker@windriver.com>  
* Based on the MPC8349EMDS config.  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
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```

```
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/sbc8548.h  
sbc8548.h
```

```
/*  
* Copyright 2007,2009 Wind River Systems <www.windriver.com>  
* Copyright 2007 Embedded Specialties, Inc.  
* Copyright 2004, 2007 Freescale Semiconductor.  
*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/sbc8641d.h  
sbc8641d.h
```

```
/*  
* Copyright 2007 Wind River Systems <www.windriver.com>  
* Copyright 2007 Embedded Specialties, Inc.  
* Joe Hamman <joe.hamman@embeddedspecialties.com>  
*  
* Copyright 2006 Freescale Semiconductor.  
*  
* Srikanth Srinivasan (srikanth.srinivasan@freescale.com)  
*  
* See file CREDITS for list of people who contributed to this  
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/sc3.h
```

```
sc3.h
```

```
/*
* (C) Copyright 2007
* Heiko Schocher, DENX Software Engineering, <hs@denx.de>.
*
* From:
* (C) Copyright 2003
* Juergen Beisert, EuroDesign embedded technologies, jbeisert@eurodsn.de
*
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* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/scb9328.h
```

```
scb9328.h
```

```
/*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
* Written by Peter Figuli <peposh@etc.sk>, 2003.
*
* 2003/13/06 Initial MP10 Support copied from wepep250
*/
```

```
./include/configs/sc_sps_1.h
```

```
sc_sps_1.h
```

```
/*
* SchulerControl GmbH, SC_SPS_1 module config
*
* Copyright (C) 2012 Marek Vasut <marex@denx.de>
* on behalf of DENX Software Engineering GmbH
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/seaboard.h
```

```
seaboard.h
```

```
/*
* (C) Copyright 2010,2011
* NVIDIA Corporation <www.nvidia.com>
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/sequoia.h
```

```
sequoia.h
```

```
/*
* (C) Copyright 2006-2008
* Stefan Roesse, DENX Software Engineering, sr@denx.de.
*
* (C) Copyright 2006
* Jacqueline Pira-Ferriol, AMCC/IBM, jpira-ferriol@fr.ibm.com
* Alain Saurel, AMCC/IBM, alain.saurel@fr.ibm.com
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/sh7752evb.h
```

```
sh7752evb.h
```

```
/*
* Configuration settings for the sh7752evb board
*
* Copyright (C) 2012 Renesas Solutions Corp.
*
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* project.
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* MA 02111-1307 USA
*/
```

```
./include/configs/sh7757lcr.h
sh7757lcr.h
```

```
/*
* Configuration settings for the sh7757lcr board
*
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*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/sh7763rdp.h
sh7763rdp.h
```

```
/*
* Configuration settings for the Renesas SH7763RDP board
*
* Copyright (C) 2008 Renesas Solutions Corp.
* Copyright (C) 2008 Nobuhiro Iwamatsu <iwamatsu.nobuhiro@renesas.com>
*
* See file CREDITS for list of people who contributed to this
```

```
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/sh7785lcr.h
```

```
sh7785lcr.h
```

```
/*  
* Configuration settings for the Renesas Technology R0P7785LC0011RL board  
*  
* Copyright (C) 2008 Yoshihiro Shimoda <shimoda.yoshihiro@renesas.com>  
*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/sheevaplug.h
```

```
sheevaplug.h
```

```
/*  
* (C) Copyright 2009  
* Marvell Semiconductor <www.marvell.com>  
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
```

```
*
* See file CREDITS for list of people who contributed to this
* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./include/configs/shmin.h
```

```
shmin.h
```

```
/*
* Configuration settings for shmin (T-SH7706LAN, T-SH7706LSR)
*
* Copyright (C) 2010, 2011 Nobuhiro Iwamatsu <iwamatsu@nigauri.org>
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/SIMPC8313.h
```

```
SIMPC8313.h
```

```
/*
* Copyright (C) Sheldon Instruments, Inc. 2008
```

```
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* MA 02111-1307 USA
*/
/*
* simp8313 board configuration file
*/
```

```
./include/configs/SM850.h
SM850.h
```

```
/*
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* MA 02111-1307 USA
*/
```

```
./include/configs/smdk2410.h
smdk2410.h
```

```
/*
 * (C) Copyright 2002
 * Sysgo Real-Time Solutions, GmbH <www.elinos.com>
 * Marius Groeger <mgroeger@sysgo.de>
 * Gary Jennejohn <garyj@denx.de>
 * David Mueller <d.mueller@elsoft.ch>
 *
 * Configuration settings for the SAMSUNG SMDK2410 board.
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./include/configs/smdk5250.h
smdk5250.h
```

```
/*
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 *
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 *
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```

```
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* MA 02111-1307 USA  
*/
```

```
./include/configs/smdk6400.h  
smdk6400.h
```

```
/*  
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* Sysgo Real-Time Solutions, GmbH <www.elinos.com>  
* Marius Groeger <mgroeger@sysgo.de>  
* Gary Jennejohn <garyj@denx.de>  
* David Mueller <d.mueller@elsoft.ch>  
*  
* (C) Copyright 2008  
* Guennadi Liakhovetki, DENX Software Engineering, <lg@denx.de>  
*  
* Configuration settings for the SAMSUNG SMDK6400(mDirac-III) board.  
*  
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* MA 02111-1307 USA  
*/
```

```
./include/configs/smdkc100.h  
smdkc100.h
```

```
/*  
* (C) Copyright 2009 Samsung Electronics  
* Minkyu Kang <mk7.kang@samsung.com>  
* HeungJun Kim <riverful.kim@samsung.com>  
* Inki Dae <inki.dae@samsung.com>  
*  
* Configuration settings for the SAMSUNG SMDKC100 board.  
*  
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*  
*/
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*/
```

```
./include/configs/smdkv310.h
smdkv310.h
```

```
/*
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* MA 02111-1307 USA
*/
```

```
./include/configs/snapper9260.h
snapper9260.h
```

```
/*
* Bluewater Systems Snapper 9260 and 9G20 modules
*
* (C) Copyright 2011 Bluewater Systems
* Author: Andre Renaud <andre@bluewatersys.com>
* Author: Ryan Mallon <ryan@bluewatersys.com>
```

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*/

./include/configs/snowball.h
snowball.h
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*/

./include/configs/socfpga_arria5.h
socfpga_arria5.h
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*/
```

```
./include/configs/socfpga_common.h  
socfpga_common.h
```

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*/
```

```
./include/configs/socfpga_cyclone5.h  
socfpga_cyclone5.h
```

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*/
```

```
./include/configs/socrates.h  
socrates.h
```

```
/*
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 *
 * Wolfgang Denk <wd@denx.de>
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 */

./include/configs/sorcery.h
sorcery.h
/*
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```

* MA 02111-1307 USA

*/

./include/configs/spc1920.h

spc1920.h

/*

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* Markus Klotzbuecher, DENX Software Engineering, mk@denx.de

*

* Configuration settings for the SPC1920 board.

*

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*/

./include/configs/SPD823TS.h

SPD823TS.h

/*

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```
*/

./include/configs/spear-common.h
spear-common.h
/*
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 */

./include/configs/spear3xx_evb.h
spear3xx_evb.h
/*
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```

```
*/  
  
./include/configs/spear6xx_evb.h  
spear6xx_evb.h  
/*  
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*/
```

```
./include/configs/spieval.h  
spieval.h  
/*  
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```

```
./include/configs/stamp9g20.h
stamp9g20.h
```

```
/*
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*/
```

```
./include/configs/stxgp3.h
stxgp3.h
```

```
/*
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* Dan Malek <dan@embeddededge.com>
* Copied from ADS85xx.
* Updates for Silicon Tx GP3 8560 board.
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/stxssa.h
```

```
stxssa.h
```

```
/*
```

```
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* Dan Malek <dan@embeddedalley.com>
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* Updates for Silicon Tx GP3 SSA board.
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*/
```

```
./include/configs/stxxtc.h
stxxtc.h
```

```
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```

```
./include/configs/suvd3.h
suvd3.h
```

```
/*
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 * Peter Barada <peterb@logicpd.com>
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 */
```

```
./include/configs/svm_sc8xx.h
svm_sc8xx.h
```

```
/*
```

```
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*/
```

```
./include/configs/SXNI855T.h
```

```
SXNI855T.h
```

```
/*
* U-Boot configuration for SIXNET SXNI855T CPU board.
* This board is based (loosely) on the Motorola FADS board, so this
* file is based (loosely) on config_FADS860T.h, see it for additional
* credits.
*
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* MA 02111-1307 USA
*/
```

```
*/  
  
./include/configs/t3corp.h  
t3corp.h  
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*/
```

```
./include/configs/T4240QDS.h  
T4240QDS.h  
/*  
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* along with this program; if not, write to the Free Software  
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* MA 02111-1307 USA  
*/
```

```
./include/configs/t4qds.h  
t4qds.h
```

```
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./include/configs/taihu.h
```

```
taihu.h
```

```
/*
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 *
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 */
```

./include/configs/taishan.h

taishan.h

/*

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*/

./include/configs/tam3517-common.h

tam3517-common.h

/*

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* Stefano Babic, DENX Software Engineering, sbabic@denx.de.

*

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*/

./include/configs/TASREG.h

TASREG.h

/*

* Configuration settings for the esd TASREG board.

*

```
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* Stefan Roese, esd gmbh germany, stefan.roese@esd-electronics.com
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/tb0229.h
```

```
tb0229.h
```

```
/*
* (C) Copyright 2003
* Masami Komiya <mkomiya@sonare.it>
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*/
```

```
./include/configs/TB5200.h
```

```
TB5200.h
```

```
/*
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*
*/
```

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* Martin Krause, TQ-Systems GmbH, martin.krause@tqs.de
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/tcm-bf518.h
```

```
tcm-bf518.h
```

```
/*
 * U-boot - Configuration file for Bluetechnix TCM-BF518 board
 */
/*
 * Processor Settings
 */
```

```
./include/configs/tcm-bf537.h
```

```
tcm-bf537.h
```

```
/*
 * U-boot - Configuration file for TCM-BF537 board
 */
/*
 * Processor Settings
 */
```

```
./include/configs/tec.h
```

```
tec.h
```

```
/*
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 * NVIDIA Corporation <www.nvidia.com>
 * (C) Copyright 2011-2012
 * Avionic Design GmbH <www.avionic-design.de>
 *
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*/
```

```
./include/configs/tegra-common-post.h
tegra-common-post.h
```

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*/
```

```
./include/configs/tegra20-common.h
tegra20-common.h
```

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* MA 02111-1307 USA
*/
```

```
./include/configs/tk71.h
```

```
tk71.h
```

```
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* on behalf of DENX Software Engineering GmbH
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* MA 02110-1301 USA
*/
```

```
./include/configs/TK885D.h
```

```
TK885D.h
```

```
/*
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*
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* Martin Krause, TQ-Systems GmbH, martin.krause@tqs.de
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* MA 02111-1307 USA
*/
```

```
./include/configs/tnetv107x_evm.h
tnetv107x_evm.h
```

```
/*
* Copyright (C) 2008 Texas Instruments, Inc <www.ti.com>
*
* Based on davinci_dvevm.h. Original Copyrights follow:
*
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*/
```

```
./include/configs/tny_a9260.h
tny_a9260.h
```

```
/*
* (C) Copyright 2007-2008
* Stelian Pop <stelian@popies.net>
```

```
* Lead Tech Design <www.leadtechdesign.com>
*
* Copyright (C) 2009
* Albin Tonnerre, Free Electrons <albin.tonnerre@free-electrons.com>
*
* Configuration settings for the Calao TNY-A9260 and TNY-A9G20 boards
*
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* MA 02111-1307 USA
*/
```

```
./include/configs/TOP5200.h
```

```
TOP5200.h
```

```
/*
```

```
* (C) Copyright 2003
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* modified for TOP5200-series by Reinhard Meyer, www.emk-elektronik.de
*
* TOP5200 differences from IceCube:
* 1 FLASH Bank for one Chip only, up to 64 MB in 16 MB Banks
*   bank switch controlled by TIMER_6(LSB) and TIMER_7(MSB) Pins
* 1 SDRAM/DDRAM Bank up to 256 MB
* local VPD I2C Bus is software driven and uses
*   GPIO_WKUP_6 for SDA, GPIO_WKUP_7 for SCL
* FLASH is re-located at 0xff000000
* Internal regs are at 0xf0000000
* Reset jumps to 0x00000100
*
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* MA 02111-1307 USA
*/

./include/configs/TOP860.h
TOP860.h
/*
* (C) Copyright 2003
* EMK Elektronik GmbH <www.emk-elektronik.de>
* Reinhard Meyer <r.meyer@emk-elektronik.de>
*
* Configuration settings for the TOP860 board.
*
* -----
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
/*
* TOP860 is a simple module:
* 16-bit wide FLASH on CS0      (2MB or more)
* 32-bit wide DRAM on CS2 (either 4MB or 16MB)
* FEC with Am79C874 100-Base-T and Fiber Optic
* Ports available, but we choose SMC1 for Console
* 8k I2C EEPROM at address 0xae, 6k user available, 2k factory set
* 32768Hz crystal PLL set for 49.152MHz Core and 24.576MHz Bus Clock
*

```

```
* This config has been copied from MBX.h / MBX860T.h
*/
/*
* board/config.h - configuration options, board specific
*/

./include/configs/top9000.h
top9000.h
/*
* (C) Copyright 2010
* Reinhard Meyer, EMK Elektronik, reinhard.meyer@emk-elektronik.de
*
* Configuration settings for the TOP9000 CPU module with AT91SAM9XE.
*
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* MA 02111-1307 USA
*/
/*
* top9000 with at91sam9xe256 or at91sam9xe512
*
* Initial Bootloader is in embedded flash.
* Vital Product Data, U-Boot Environment are in I2C-EEPROM.
* U-Boot is in embedded flash, a backup U-Boot can be in NAND flash.
* kernel and file system are either in NAND flash or on a micro SD card.
* NAND flash is optional.
* I2C EEPROM is never optional.
* SPI FRAM is optional.
* SPI ENC28J60 is optional.
* 16 or 32 bit wide SDRAM.
*/

./include/configs/Total5200.h
Total5200.h
/*
* (C) Copyright 2003-2004
```

```
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*  
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* Mark Jonas, Freescale Semiconductor, mark.jonas@freescale.com.  
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* MA 02111-1307 USA  
*/
```

```
./include/configs/TQM5200.h
```

```
TQM5200.h
```

```
/*  
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```

```
*/  
  
./include/configs/TQM823L.h  
TQM823L.h  
/*  
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* MA 02111-1307 USA  
*/
```

```
./include/configs/TQM823M.h  
TQM823M.h  
/*  
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* MA 02111-1307 USA
```

*/

./include/configs/TQM8260.h

TQM8260.h

/*

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*/

./include/configs/TQM8272.h

TQM8272.h

/*

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```
*/  
  
./include/configs/TQM834x.h  
TQM834x.h  
/*  
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* MA 02111-1307 USA  
*/
```

```
./include/configs/TQM850L.h  
TQM850L.h  
/*  
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```

*/

./include/configs/TQM850M.h

TQM850M.h

/*

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*/

./include/configs/TQM855L.h

TQM855L.h

/*

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./include/configs/TQM855M.h

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./include/configs/TQM860L.h

TQM860L.h

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*/

./include/configs/TQM860M.h
TQM860M.h

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*/

./include/configs/TQM862L.h
TQM862L.h

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*/

./include/configs/TQM862M.h

TQM862M.h

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*/

./include/configs/TQM866M.h

TQM866M.h

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```
*/  
  
./include/configs/TQM885D.h  
TQM885D.h  
/*  
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*  
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* MA 02111-1307 USA  
*/
```

```
./include/configs/trats.h  
trats.h  
/*  
* Copyright (C) 2011 Samsung Electronics  
* Heungjun Kim <riverful.kim@samsung.com>  
*  
* Configuration settings for the SAMSUNG TRATS (EXYNOS4210) board.  
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*/
```

```
./include/configs/tricorder.h
tricorder.h
```

```
/*
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* Texas Instruments.
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* Syed Mohammed Khasim <x0khasim@ti.com>
*
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* Thomas Weber <weber@corscience.de>
*
* Configuration settings for the Tricorder board.
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*/
```

```
./include/configs/trimslice.h
trimslice.h
```

```
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*/
```

```
./include/configs/trizepsiv.h
trizepsiv.h
```

```
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* Marius Groeger <mgroeger@sysgo.de>
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```

```
*/

./include/configs/tt01.h
tt01.h
/*
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 * (C) Copyright 2008 Magnus Lilja <lilja.magnus@gmail.com>
 *
 * Configuration settings for the HALE TT-01 board.
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 */

./include/configs/tuxx1.h
tuxx1.h
/*
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```

```
./include/configs/twister.h
twister.h
```

```
/*
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*
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*/
```

```
./include/configs/tx25.h
tx25.h
```

```
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```

```
./include/configs/u8500_href.h
u8500_href.h
```

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```

```
./include/configs/uc100.h
uc100.h
```

```
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./include/configs/uc101.h

uc101.h

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./include/configs/utx8245.h

utx8245.h

```
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 *
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```
./include/configs/v37.h
```

```
v37.h
```

```
/*
```

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*/
```

```
./include/configs/v38b.h
```

```
v38b.h
```

```
/*
```

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*/

./include/configs/v5fx30teval.h

v5fx30teval.h

/*

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*/

./include/configs/VCMA9.h

VCMA9.h

/*

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* Sysgo Real-Time Solutions, GmbH www.elinos.com
* Marius Groeger mgroeger@sysgo.de
* Gary Jennejohn garyj@denx.de
* David Mueller d.mueller@elsoft.ch

*

* Configuration settings for the MPL VCMA9 board.

*

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```
*/  
  
./include/configs/vct.h  
vct.h  
/*  
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*/
```

```
./include/configs/ve8313.h  
ve8313.h  
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*/  
/*
```

```
* ve8313 board configuration file
*/
```

```
./include/configs/ventana.h
ventana.h
```

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```

```
./include/configs/versatile.h
versatile.h
```

```
/*
 * (C) Copyright 2003
 * Texas Instruments.
 * Kshitij Gupta <kshitij@ti.com>
 * Configuration settings for the TI OMAP Innovator board.
 *
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 * ARM Ltd.
 * Philippe Robin, <philippe.robin@arm.com>
 * Configuration for Versatile PB.
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* MA 02111-1307 USA
*/
```

```
./include/configs/virtlab2.h
virtlab2.h
```

```
/*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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*/
```

```
./include/configs/vision2.h
vision2.h
```

```
/*
* Copyright (C) 2007, Guennadi Liakhovetski <lg@denx.de>
*
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*
* Configuration settings for the MX51-3Stack Freescale board.
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* MA 02111-1307 USA
*/
```

```
./include/configs/vl_ma2sc.h
```

```
vl_ma2sc.h
```

```
/*
```

```
* (C) Copyright 2009-2012
* Jens Scharsig <esw@bus-elektronik.de>
* BuS Elektronik GmbH & Co. KG
*
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*/
```

```
./include/configs/vme8349.h
```

```
vme8349.h
```

```
/*
```

```
* esd vme8349 U-Boot configuration file
* Copyright (c) 2008, 2009 esd gmbh Hannover Germany
*
* (C) Copyright 2006-2010
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* reinhard.arlt@esd-electronics.de
* Based on the MPC8349EMDS config.
*
*/
```

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*/
```

```
./include/configs/VOH405.h
```

```
VOH405.h
```

```
/*
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*/
```

```
./include/configs/VOM405.h
```

```
VOM405.h
```

```
/*
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*/
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*/
```

```
./include/configs/VoVPN-GW.h
```

```
VoVPN-GW.h
```

```
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*/
```

```
./include/configs/vpac270.h
```

```
vpac270.h
```

```
/*
* Voipac PXA270 configuration file
*
* Copyright (C) 2010 Marek Vasut <marek.vasut@gmail.com>
*/
```

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```

```
./include/configs/W7OLMC.h
```

```
W7OLMC.h
```

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```

```
./include/configs/W7OLMG.h
```

```
W7OLMG.h
```

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*/
```

```
./include/configs/walnut.h
```

```
walnut.h
```

```
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*/
```

```
./include/configs/whistler.h
```

```
whistler.h
```

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*/
```

```
./include/configs/wireless_space.h
wireless_space.h
```

```
/*
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 *
 * Based on the netSPACE_v2 code which is
 * Copyright (C) 2011 Simon Guinot <sguinot@lacie.com>
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```

```
./include/configs/woodburn.h
woodburn.h
```

```
/*
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 *
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```

```
./include/configs/woodburn_common.h
woodburn_common.h
```

```
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```

```
./include/configs/woodburn_sd.h
woodburn_sd.h
```

```
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*/
```

```
./include/configs/WUH405.h
```

```
WUH405.h
```

```
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```

```
./include/configs/x600.h
```

```
x600.h
```

```
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*/
```

```
./include/configs/xaeniah.h
xaeniah.h
```

```
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*
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```

```
*/  
  
./include/configs/xilinx-ppc.h  
xilinx-ppc.h  
/*  
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*/
```

```
./include/configs/xilinx-ppc405-generic.h  
xilinx-ppc405-generic.h  
/*  
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*/
```

```
./include/configs/xilinx-ppc405.h
```

```
xilinx-ppc405.h
```

```
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```

```
./include/configs/xilinx-ppc440-generic.h
```

```
xilinx-ppc440-generic.h
```

```
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```

```
./include/configs/xilinx-ppc440.h
xilinx-ppc440.h
```

```
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*/
```

```
./include/configs/xpedite1000.h
xpedite1000.h
```

```
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*/
```

```
./include/configs/xpedite517x.h
xpedite517x.h
```

```
/*
```

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*/
```

```
./include/configs/xpedite520x.h
```

```
xpedite520x.h
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/xpedite537x.h
```

```
xpedite537x.h
```

```
/*
```

```
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* Copyright 2007-2008 Freescale Semiconductor, Inc.
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* MA 02111-1307 USA
*/
```

```
./include/configs/xpedite550x.h
```

```
xpedite550x.h
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/yosemite.h
```

```
yosemite.h
```

```
/*
* (C) Copyright 2005-2007
```

```
* Stefan Roese, DENX Software Engineering, sr@denx.de.  
*  
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* project.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/yucca.h
```

```
yucca.h
```

```
/*  
* (C) Copyright 2004 Paul Reynolds <PaulReynolds@lhsolutions.com>  
*  
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* project.  
*  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/configs/Yukon8220.h
```

```
Yukon8220.h
```

```
/*  
* (C) Copyright 2004  
* TsiChung Liew, Freescale Software Engineering, Tsi-Chung.Liew@freescale.
```

```
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/zeus.h
zeus.h
```

```
/*
* (C) Copyright 2007
* Stefan Roes, DENX Software Engineering, sr@denx.de.
*
* See file CREDITS for list of people who contributed to this
* project.
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/zipitz2.h
zipitz2.h
```

```
/*
* Aeronix Zipit Z2 configuration file
*
*/
```

```
* Copyright (C) 2009-2010 Marek Vasut <marek.vasut@gmail.com>
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/zmx25.h
```

```
zmx25.h
```

```
/*
* (c) 2011 Graf-Syteco, Matthias Weisser
* <weisserm@arcor.de>
*
* Configuration settings for the zmx25 board
*
* See file CREDITS for list of people who contributed to this
* project.
*
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* modify it under the terms of the GNU General Public License as
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/ZPC1900.h
```

```
ZPC1900.h
```

```
/*
* Copyright (C) 2003-2005 Arabella Software Ltd.
* Yuli Barcohen <yuli@arabellasw.com>
```

```
*
* U-Boot configuration for Zephyr Engineering ZPC.1900 board.
* This port was developed and tested on Revision C board.
*
* See file CREDITS for list of people who contributed to this
* project.
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/ZUMA.h
```

```
ZUMA.h
```

```
/*
* (C) Copyright 2001
* Josh Huber <huber@mcclx.com>, Mission Critical Linux, Inc.
*
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* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/configs/zynq.h
```

```
zynq.h
```

```
/*
 * (C) Copyright 2012 Michal Simek <monstr@monstr.eu>
 *
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 * project.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./include/config_cmd_all.h
config_cmd_all.h
```

```
/*
 * Copyright 2007 Freescale Semiconductor, Inc.
 *
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 * License Version 2. This file is licensed "as is" without any
 * warranty of any kind, whether express or implied.
 */
/*
 * Alphabetical list of all possible commands.
 */
```

```
./include/config_cmd_default.h
config_cmd_default.h
```

```
/*
 * Copyright 2007 Freescale Semiconductor, Inc.
 *
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 * License Version 2. This file is licensed "as is" without any
 * warranty of any kind, whether express or implied.
 */
/*
 * Alphabetical list of all commands that are configured by default.
 * This is essentially all commands minus those that are considered
 * "non-standard" for some reason (memory hogs, requires special
 * hardware, not fully tested, etc.).
 */
```

```
./include/config_cmd_defaults.h
config_cmd_defaults.h
/*
 * config_cmd_defaults.h - sane defaults for everyone
 *
 * Copyright (c) 2010-2011 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */
```

```
./include/config_defaults.h
config_defaults.h
/*
 * config_defaults.h - sane defaults for everyone
 *
 * Copyright (c) 2009 Analog Devices Inc.
 *
 * Licensed under the GPL-2 or later.
 */
/* Support bootm-ing different OSes */
```

```
./include/config_fallbacks.h
config_fallbacks.h
/*
 * Copyright 2012 Texas Instruments
 *
 * This file is licensed under the terms of the GNU General Public
 * License Version 2. This file is licensed "as is" without any
 * warranty of any kind, whether express or implied.
 */
```

```
./include/config_phylib_all_drivers.h
config_phylib_all_drivers.h
/*
 * Enable all PHYs
 *
 * This software may be used and distributed according to the
 * terms of the GNU Public License, Version 2, incorporated
 * herein by reference.
 *
 * Copyright 2011 Freescale Semiconductor, Inc.
 * author Andy Fleming
 *
 */
```

```
./include/config_uncmd_spl.h
config_uncmd_spl.h
/*
 * (C) Copyright 2012
```

```
* Ilya Yanok, ilya.yanok@gmail.com
*
* See file CREDITS for list of people who contributed to this
* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc.
*
* We don't use any commands in SPL, but generic networking code
* has some features enabled/disabled based on CONFIG_CMD_*
* options. As we want a minimal set of features included
* into network SPL image, we undefine some config options here.
*/

./include/cpsw.h
cpsw.h
/*
* CPSW Ethernet Switch Driver
*
* Copyright (C) 2010 Texas Instruments Incorporated - http://www.ti.com/
*
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* of MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
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*/

./include/cramfs/cramfs_fs.h
cramfs_fs.h
/*
* Width of various bitfields in struct cramfs_inode.
* Primarily used to generate warnings in mkcramfs.
*/

./include/cramfs/cramfs_fs_sb.h
```

```

cramfs_fs_sb.h
/*
 * cramfs super-block data in memory
 */

./include/crc.h
crc.h
/*
 *=====
 *
 *      crc.h
 *
 *      Interface for the CRC algorithms.
 *
 *=====
 *#####ECOSGPLCOPYRIGHTBEGIN#####
 * -----
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 * Copyright (C) 2002 Andrew Lunn
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 * -----
 *#####ECOSGPLCOPYRIGHTEND#####
 *=====
 *#####DESCRIPTIONBEGIN#####
 *

```

```
* Author(s): Andrew Lunn
* Contributors: Andrew Lunn
* Date: 2002-08-06
* Purpose:
* Description:
*
* This code is part of eCos (tm).
*
*#####DESCRIPTIONEND#####
*
*=====
*/
```

```
./include/da9030.h
da9030.h
```

```
/*
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*
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* project.
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/dataflash.h
dataflash.h
```

```
/*
* (C) Copyright 2003
* Data Flash Atmel Description File
* Author : Hamid Ikdoumi (Atmel)
*
* See file CREDITS for list of people who contributed to this
* project.
*
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```

```
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./include/ddr_spd.h
ddr_spd.h
/*
* Copyright 2008 Freescale Semiconductor, Inc.
*
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* modify it under the terms of the GNU General Public License
* Version 2 as published by the Free Software Foundation.
*/
/*
* Format from "JEDEC Standard No. 21-C,
* Appendix D: Rev 1.0: SPD's for DDR SDRAM
*/

./include/dfu.h
dfu.h
/*
* dfu.h - DFU flashable area description
*
* Copyright (C) 2012 Samsung Electronics
* authors: Andrzej Pietrasiewicz <andrzej.p@samsung.com>
*          Lukasz Majewski <l.majewski@samsung.com>
*
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*/
```

```
./include/dialog_pmic.h
```

```
dialog_pmic.h
```

```
/*  
 * da9053 register declarations.  
 *  
 * Copyright(c) 2009 Dialog Semiconductor Ltd.  
 *  
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 * it under the terms of the GNU General Public License as published by  
 * the Free Software Foundation; either version 2 of the License, or  
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 * along with this program; if not, write to the Free Software  
 * Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.  
 *  
 */
```

```
./include/div64.h
```

```
div64.h
```

```
/*  
 * Copyright (C) 2003 Bernardo Innocenti <bernie@develer.com>  
 * Based on former asm-ppc/div64.h and asm-m68knommu/div64.h  
 *  
 * The semantics of do_div() are:  
 *  
 * uint32_t do_div(uint64_t *n, uint32_t base)  
 * {  
 *     uint32_t remainder = *n % base;  
 *     *n = *n / base;  
 *     return remainder;  
 * }  
 *  
 * NOTE: macro parameter n is evaluated multiple times,  
 *       beware of side effects!  
 */
```

```
./include/dm9000.h
```

```
dm9000.h
```

```
/*  
 * NOTE:          DAVICOM DM9000 ethernet driver interface  
 *  
 * Authors:       Remy Bohmer <linux@bohmer.net>  
 *
```

```
*          This program is free software; you can redistribute it and/or
*          modify it under the terms of the GNU General Public License
*          as published by the Free Software Foundation; either version
*          2 of the License, or (at your option) any later version.
*/
```

```
./include/dm9161.h
```

```
dm9161.h
```

```
/*
 * NOTE:          DAVICOM ethernet Physical layer
 *
 * Version:       @(#)DM9161.h    1.0.0    01/10/2001
 *
 * Authors:       ATMEL Rousset
```

```
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*          modify it under the terms of the GNU General Public License
*          as published by the Free Software Foundation; either version
*          2 of the License, or (at your option) any later version.
*/
```

```
./include/dp83848.h
```

```
dp83848.h
```

```
/*
 * DP83848 ethernet Physical layer
 *
 * Copyright (C) 2007 Sergey Kubushyn <ksi@koi8.net>
```

```
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* modify it under the terms of the GNU General Public License
* as published by the Free Software Foundation; either version
* 2 of the License, or (at your option) any later version.
*/
```

```
./include/ds1722.h
```

```
ds1722.h
```

```
/*
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 * project.
 *
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```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/ds4510.h
```

```
ds4510.h
```

```
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/dtt.h
```

```
dtt.h
```

```
/*  
* (C) Copyright 2001  
* Erik Theisen, Wave 7 Optics, etheisen@mindspring.com.  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
*  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
```

```
* MA 02111-1307 USA
*/
```

```
./include/dwmmc.h
dwmmc.h
```

```
/*
 * Copyright (C) 2013 Altera Corporation <www.altera.com>
 *
 * (C) Copyright 2012 SAMSUNG Electronics
 * Jaehoon Chung <jh80.chung@samsung.com>
 *
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 * modify it under the terms of the GNU General Public License as
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 * the License, or (at your option) any later version.
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 *
 * You should have received a copy of the GNU General Public License
 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
 */
```

```
./include/e500.h
e500.h
```

```
/*
 * Copyright 2003 Motorola, Inc.
 * Xianghua Xiao(x.xiao@motorola.com)
 */
```

```
./include/edid.h
edid.h
```

```
/*
 * Copyright (c) 2012 The Chromium OS Authors.
 *
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 * Petr Stetiar <ynezz@true.cz>
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*  
*/
```

```
./include/elf.h
```

```
elf.h
```

```
/*
```

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```

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*/
```

```
./include/environment.h
```

```
environment.h
```

```
/*
```

```
* (C) Copyright 2002  
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/env_attr.h
```

```
env_attr.h
```

```
/*
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* Joe Hershberger, National Instruments, joe.hershberger@ni.com
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/env_callback.h
```

```
env_callback.h
```

```
/*
* (C) Copyright 2012
* Joe Hershberger, National Instruments, joe.hershberger@ni.com
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*/
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/env_default.h
```

```
env_default.h
```

```
/*
* (C) Copyright 2000-2010
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* (C) Copyright 2001 Sysgo Real-Time Solutions, GmbH <www.elinos.com>
* Andreas Heppel <aheppel@sysgo.de>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/env_flags.h
```

```
env_flags.h
```

```
/*
```

```
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* MA 02111-1307 USA
*/
```

```
./include/errno.h
```

```
errno.h
```

```
No license information found.
```

```
./include/exports.h
```

```
exports.h
```

```
/* These are declarations of exported functions available in C code */
```

```
./include/ext4fs.h
```

```
ext4fs.h
```

```
/*
```

```
* (C) Copyright 2011 - 2012 Samsung Electronics
```

```
* EXT4 filesystem implementation in Uboot by
```

```
* Uma Shankar <uma.shankar@samsung.com>
```

```
* Manjunatha C Achar <a.manjunatha@samsung.com>
```

```
*
```

```
* Ext4 Extent data structures are taken from original ext4 fs code
```

```
* as found in the linux kernel.
```

```
*
```

```
* Copyright (c) 2003-2006, Cluster File Systems, Inc, info@clusterfs.com
```

```
* Written by Alex Tomas <alex@clusterfs.com>
```

```
*
```

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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./include/ext_common.h
```

```
ext_common.h
```

```
/*
* (C) Copyright 2011 - 2012 Samsung Electronics
* EXT4 filesystem implementation in Uboot by
* Uma Shankar <uma.shankar@samsung.com>
* Manjunatha C Achar <a.manjunatha@samsung.com>
*
* Data structures and headers for ext4 support have been taken from
* ext2 ls load support in Uboot
*
* (C) Copyright 2004
* esd gmbh <www.esd-electronics.com>
* Reinhard Arlt <reinhard.arlt@esd-electronics.com>
*
* based on code from grub2 fs/ext2.c and fs/fshelp.c by
* GRUB -- GRand Unified Bootloader
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*/
```

```
./include/faraday/ftahbc020s.h
```

```
ftahbc020s.h
```

```
/*
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* Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
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*/
```

```
./include/faraday/ftpmu010.h
```

```
ftpmu010.h
```

```
/*
```

```
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* Po-Yu Chuang <ratbert@faraday-tech.com>
```

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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./include/faraday/ftsd010.h
```

```
ftsd010.h
```

```
/*
```

```
* Faraday FTSD010 Secure Digital Memory Card Host Controller
```

```
*
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* Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
```

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*/
```

```
./include/faraday/ftsdmc020.h
```

```
ftsdmc020.h
```

```
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*/
```

```
./include/faraday/ftsdmc021.h
```

```
ftsdmc021.h
```

```
/*  
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*  
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```

```
*/  
  
./include/faraday/ftsmc020.h  
ftsmc020.h  
/*  
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*/
```

```
./include/faraday/fttmr010.h  
fttmr010.h  
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.  
*/
```

```
./include/faraday/ftwtd010_wdt.h  
ftwtd010_wdt.h  
/*  
* Watchdog driver for the FTWDT010 Watch Dog Driver  
*  
* (c) Copyright 2004 Faraday Technology Corp. (www.faraday-tech.com)
```

```
* Based on sa1100_wdt.c by Oleg Drokin <green@crimea.edu>
* Based on SoftDog driver by Alan Cox <alan@redhat.com>
*
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* Macpaul Lin, Andes Technology Corporation <macpaul@andestech.com>
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*
* 27/11/2004 Initial release, Faraday.
* 12/01/2011 Port to u-boot, Macpaul Lin.
*/
```

```
./include/fat.h
```

```
fat.h
```

```
/*
* R/O (V)FAT 12/16/32 filesystem implementation by Marcus Sundberg
*
* 2002-07-28 - rjones@nexus-tech.net - ported to ppcboot v1.1.6
* 2003-03-10 - kharris@nexus-tech.net - ported to u-boot
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
*/  
  
./include/fdc.h  
fdc.h  
/*  
* (C) Copyright 2002  
* Stäubli Faverges - <www.staubli.com>  
* Pierre AUBERT p.aubert@staubli.com  
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* MA 02111-1307 USA  
*/
```

```
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fdt.h  
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```

```
./include/fdtdec.h  
fdtdec.h  
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* MA 02111-1307 USA
*/
```

```
./include/fdt_support.h
```

```
fdt_support.h
```

```
/*
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./include/fis.h
```

```
fis.h
```

```
/*
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 * Dave Liu <daveliu@freescale.com>
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 * MA 02111-1307 USA
*/
```

```
*
*/

./include/flash.h
flash.h
/*
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
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* project.
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* MA 02111-1307 USA
*/
```

```
./include/fm_eth.h
fm_eth.h
/*
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* MA 02111-1307 USA
*/
```

```
./include/fpga.h
```

fpga.h

```
/*
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 *
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 * project.
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 * along with this program; if not, write to the Free Software
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 * MA 02111-1307 USA
 */
```

./include/fs.h

fs.h

```
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 * more details.
 *
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 * along with this program. If not, see <http://www.gnu.org/licenses/>.
 */
```

./include/fsl_diu_fb.h

fsl_diu_fb.h

```
/*
 * Copyright 2007, 2011 Freescale Semiconductor, Inc.
 * Authors: York Sun <yorksun@freescale.com>
 *          Timur Tabi <timur@freescale.com>
 */
```

```
* FSL DIU Framebuffer driver
*
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* project.
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* MA 02111-1307 USA
*/
```

```
./include/fsl_esdhc.h
fsl_esdhc.h
```

```
/*
* FSL SD/MMC Defines
*-----
*
* Copyright 2007-2008,2010-2011 Freescale Semiconductor, Inc
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*-----
*
*/
```

```
./include/fsl_mdio.h
fsl_mdio.h
```

```
/*
 * Copyright 2009-2012 Freescale Semiconductor, Inc.
 *   Jun-jie Zhang <b18070@freescale.com>
 *   Mingkai Hu <Mingkai.hu@freescale.com>
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./include/fsl_nfc.h
```

```
fsl_nfc.h
```

```
/*
 * (c) 2009 Magnus Lilja <lilja.magnus@gmail.com>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 *
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 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./include/fsl_pmic.h
```

```
fsl_pmic.h
```

```
/*
 * (C) Copyright 2010
 * Stefano Babic, DENX Software Engineering, sbabic@denx.de.
```

```
*
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*
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* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/galileo/core.h
```

```
core.h
```

```
/* Core.h - Basic core logic functions and definitions */
```

```
/* Copyright Galileo Technology. */
```

```
/*
```

```
*/
```

```
./include/galileo/gt64260R.h
```

```
gt64260R.h
```

```
/* gt64260R.h - GT64260 Internal registers definition file */
```

```
/* Copyright - Galileo technology. */
```

```
/* CPU MASTER CONTROL REGISTER */
```

```
./include/galileo/memory.h
```

```
memory.h
```

```
/* Memory.h - Memory mappings and remapping functions declarations */
```

```
/* Copyright - Galileo technology. */
```

```
/* includes */
```

```
/* defines */
```

```
./include/galileo/pci.h
```

```
pci.h
```

```
/* PCI.h - PCI functions header file */
```

```
/* Copyright - Galileo technology. */
```

```
/* includes */
```

```
/* According to PCI REV 2.1 MAX agents allowed on the bus are -21- */
```

```
./include/gdsys_fpga.h
```

gdsys_fpga.h

```
/*
 * (C) Copyright 2010
 * Dirk Eibach, Guntermann & Drunck GmbH, eibach@gdsys.de
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./include/generated/asm-offsets.h

asm-offsets.h

```
/*
 * DO NOT MODIFY.
 *
 * This file was generated by make-asm-offsets
 *
 */
```

./include/generated/generic-asm-offsets.h

generic-asm-offsets.h

```
/*
 * DO NOT MODIFY.
 *
 * This file was generated by make-asm-offsets
 *
 */
```

./include/generated/timestamp_autogenerated.h

timestamp_autogenerated.h

No license information found.

./include/generated/version_autogenerated.h

version_autogenerated.h

No license information found.

./include/g_dnl.h

g_dnl.h

```
/*
 * Copyright (C) 2012 Samsung Electronics
 * Lukasz Majewski <l.majewski@samsung.com>
 *
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 * modify it under the terms of the GNU General Public License as
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 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./include/hash.h

hash.h

```
/*
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 * project.
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./include/hush.h

hush.h

```
/*
 * (C) Copyright 2001
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
```

```
*
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* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/hwconfig.h
```

```
hwconfig.h
```

```
/*
* An interface for configuring a hardware via u-boot environment.
*
* Copyright (c) 2009 MontaVista Software, Inc.
* Copyright 2011 Freescale Semiconductor, Inc.
*
* Author: Anton Vorontsov <avorontsov@ru.mvista.com>
*
* This program is free software; you can redistribute it and/or
* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2 of
* the License, or (at your option) any later version.
*/
```

```
./include/i2c.h
```

```
i2c.h
```

```
/*
* (C) Copyright 2001
* Gerald Van Baren, Custom IDEAS, vanbaren@cideas.com.
*
* See file CREDITS for list of people who contributed to this
* project.
*
* This program is free software; you can redistribute it and/or
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* the License, or (at your option) any later version.
*/
```

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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*  
* The original I2C interface was  
* (C) 2000 by Paolo Scaffardi (arsenio@tin.it)  
* AIRVENT SAM s.p.a - RIMINI(ITALY)  
* but has been changed substantially.  
*/
```

```
./include/i2s.h
```

```
i2s.h
```

```
/*  
* Copyright (C) 2012 Samsung Electronics  
* R. Chandrasekar <rcsekar@samsung.com>  
*  
* See file CREDITS for list of people who contributed to this  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/i8042.h
```

```
i8042.h
```

```
/*  
* (C) Copyright 2002 ELTEC Elektronik AG  
* Frank Gottschling <fgottschling@eltec.de>  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
*  
*/
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/ide.h
```

```
ide.h
```

```
/*
* (C) Copyright 2000
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
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* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/image.h
```

```
image.h
```

```
/*
* (C) Copyright 2008 Semihalf
*
* (C) Copyright 2000-2005
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* See file CREDITS for list of people who contributed to this
```

```
* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*****
* NOTE: This header file defines an interface to U-Boot. Including
* this (unmodified) header file in another file is considered normal
* use of U-Boot, and does *not* fall under the heading of "derived
* work".
*****
*/
```

```
./include/input.h
```

```
input.h
/*
* Keyboard input helper functions (too small to be called a layer)
*
* Copyright (c) 2011 The Chromium OS Authors.
* See file CREDITS for list of people who contributed to this
* project.
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/iomux.h
iomux.h
/*
 * (C) Copyright 2008
 * Gary Jennejohn, DENX Software Engineering GmbH, garyj@denx.de.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./include/ioports.h
ioports.h
/*
 * definitions for MPC8260 I/O Ports
 *
 * (in addition to those provided in <asm/immap_8260.h>)
 *
 * Murray.Jensen@cmst.csiro.au, 20-Oct-00
 */
/*
 * this structure mirrors the layout of the five port registers in
 * the internal memory map - see iop8260_t in <asm/immap_8260.h>
 */
```

```
./include/ipu_pixfmt.h
ipu_pixfmt.h
/*
 * (C) Copyright 2011
 * Stefano Babic, DENX Software Engineering, sbabic@denx.de
 *
 * Based on Linux IPU driver for MX51 (ipu.h):
 *
 * (C) Copyright 2005-2010 Freescale Semiconductor, Inc.
 *
 * See file CREDITS for list of people who contributed to this
```

```

* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./include/jfffs2/compr_rubin.h
compr_rubin.h
/* Rubin encoder/decoder header      */
/* work started at   : aug  3, 1994 */
/* last modification : aug 15, 1994 */
/* $Id: compr_rubin.h,v 1.1 2002/01/16 23:34:32 nyet Exp $ */

./include/jfffs2/jfffs2.h
jfffs2.h
/*
* JFFS2 -- Journalling Flash File System, Version 2.
*
* Copyright (C) 2001 Red Hat, Inc.
*
* Created by David Woodhouse <dwmw2@cambridge.redhat.com>
*
* The original JFFS, from which the design for JFFS2 was derived,
* was designed and implemented by Axis Communications AB.
*
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* See the Licence for the specific language governing rights and
* limitations under the Licence.
*
* The Original Code is JFFS2 - Journalling Flash File System, version 2
*
* Alternatively, the contents of this file may be used under the

```

```
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* other provisions required by the GPL. If you do not delete the
* provisions above, a recipient may use your version of this file
* under either the RHEPL or the GPL.
```

```
*
* $Id: jfffs2.h,v 1.2 2002/01/17 00:53:20 nyet Exp $
*
```

```
*/
```

```
./include/jfffs2/jfffs2_1pass.h
```

```
jfffs2_1pass.h
```

```
No license information found.
```

```
./include/jfffs2/load_kernel.h
```

```
load_kernel.h
```

```
/*-----
* Filename:      load_kernel.h
* Version:       $Id: load_kernel.h,v 1.3 2002/01/25 01:34:11 nyet Exp $
* Copyright:    Copyright (C) 2001, Russ Dill
* Author:       Russ Dill <Russ.Dill@asu.edu>
* Description:  header for load kernel modules
*-----*/
```

```
/*
```

```
*
```

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* it under the terms of the GNU General Public License as published by
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```

```
*
```

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```

```
*
```

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```

```
*
```

```
*/
```

```
./include/jfffs2/mini_inflate.h
```

```
mini_inflate.h
```

```
/*-----
* Filename:      mini_inflate.h
* Version:       $Id: mini_inflate.h,v 1.2 2002/01/17 00:53:20 nyet Exp $
```

```
* Copyright:      Copyright (C) 2001, Russ Dill
* Author:         Russ Dill <Russ.Dill@asu.edu>
* Description:    Mini deflate implementation
*-----*/
/*
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*/
```

```
./include/keyboard.h
keyboard.h
No license information found.
```

```
./include/key_matrix.h
key_matrix.h
/*
* Keyboard matrix helper functions
*
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* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/kgdb.h
kgdb.h
No license information found.
```

```
./include/ks8721.h
ks8721.h
/*
 * NOTE:          MICREL ethernet Physical layer
 *
 * Version:       KS8721.h
 *
 * Authors:       Eric Benard (based on dm9161.h)
 *
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 *               modify it under the terms of the GNU General Public License
 *               as published by the Free Software Foundation; either version
 *               2 of the License, or (at your option) any later version.
 */
```

```
./include/lattice.h
lattice.h
/*
 * Porting to U-Boot:
 *
 * (C) Copyright 2010
 * Stefano Babic, DENX Software Engineering, sbabic@denx.de.
 *
 * Lattice's ispVME Embedded Tool to load Lattice's FPGA:
 *
 * Lattice Semiconductor Corp. Copyright 2009
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
*/  
  
./include/lcd.h  
lcd.h  
/*  
* MPC823 and PXA LCD Controller  
*  
* Modeled after video interface by Paolo Scaffardi  
*  
*  
* (C) Copyright 2001  
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/lcdvideo.h  
lcdvideo.h  
/*  
* MPC823 LCD and Video Controller  
* Copyright (c) 1999 Dan Malek (dmalek@jlc.net)  
*/  
/* LCD Controller Configuration Register.  
*/
```

```
./include/ld9040.h  
ld9040.h  
/*  
* ld9040 AMOLED LCD panel driver.  
*  
* Copyright (C) 2012 Samsung Electronics  
* Donghwa Lee <dh09.lee@samsung.com>  
*  
* See file CREDITS for list of people who contributed to this
```

```
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/led-display.h
```

```
led-display.h
```

```
/*  
* (C) Copyright 2005-2010  
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
* (C) Copyright 2010  
* Sergei Poselenov, Emcraft Systems, sposelenov@emcraft.com.  
*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/libata.h
```

```
libata.h
```

```
/*  
* Copyright 2003-2004 Red Hat, Inc. All rights reserved.
```

```
* Copyright 2003-2004 Jeff Garzik
* Copyright (C) 2008 Freescale Semiconductor, Inc.
*       Dave Liu <daveliu@freescale.com>
*       port from libata of linux kernel
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
```

```
./include/libfdt.h
```

```
libfdt.h
```

```
/*
 * libfdt - Flat Device Tree manipulation
 * Copyright (C) 2006 David Gibson, IBM Corporation.
 *
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 * the GPL, or the BSD license, at your option.
 *
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*      CONTRACT, STRICT LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR
*      OTHERWISE) ARISING IN ANY WAY OUT OF THE USE OF THIS SOFTWARE,
*      EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.
*/

./include/libfdt_env.h
libfdt_env.h
/*
 * libfdt - Flat Device Tree manipulation (build/run environment adaptation)
 * Copyright (C) 2007 Gerald Van Baren, Custom IDEAS, vanbaren@cideas.com
 * Original version written by David Gibson, IBM Corporation.
 *
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 *
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 * License along with this library; if not, write to the Free Software
 * Foundation, Inc., 51 Franklin St, Fifth Floor, Boston, MA 02110-1301 USA
 */

./include/libtizen.h
libtizen.h
/*

```

```

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* Donghwa Lee <dh09.lee@samsung.com>
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* aint with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/

./include/linker_lists.h
linker_lists.h
/*
* include/linker_lists.h
*
* Implementation of linker-generated arrays
*
* Copyright (C) 2012 Marek Vasut <marex@denx.de>
*
* See file CREDITS for list of people who contributed to this
* project.
*
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* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2 of
* the License, or (at your option) any later version.
*/

./include/linux/apm_bios.h
apm_bios.h
/*
* Include file for the interface to an APM BIOS
* Copyright 1994-2001 Stephen Rothwell (sfr@canb.auug.org.au)
*
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* under the terms of the GNU General Public License as published by the
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* General Public License for more details.
*/
```

```
./include/linux/bch.h
```

```
bch.h
```

```
/*
 * Generic binary BCH encoding/decoding library
 *
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 * under the terms of the GNU General Public License version 2 as published by
 * the Free Software Foundation.
 *
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 * this program; if not, write to the Free Software Foundation, Inc., 51
 * Franklin St, Fifth Floor, Boston, MA 02110-1301 USA.
 *
 * Copyright © 2011 Parrot S.A.
 *
 * Author: Ivan Djelic <ivan.djelic@parrot.com>
 *
 * Description:
 *
 * This library provides runtime configurable encoding/decoding of binary
 * Bose-Chaudhuri-Hocquenghem (BCH) codes.
 */
```

```
./include/linux/bitops.h
```

```
bitops.h
```

```
/*
 * ffs: find first bit set. This is defined the same way as
 * the libc and compiler builtin ffs routines, therefore
 * differs in spirit from the above ffz (man ffs).
 */
```

```
./include/linux/byteorder/big_endian.h
```

```
big_endian.h
```

```
No license information found.
```

```
./include/linux/byteorder/generic.h
```

```
generic.h
```

```
/*
 * linux/byteorder_generic.h
 * Generic Byte-reordering support
```

```
*
* Francois-Rene Rideau <fare@tunes.org> 19970707
*   gathered all the good ideas from all asm-foo/byteorder.h into one file,
*   cleaned them up.
*   I hope it is compliant with non-GCC compilers.
*   I decided to put __BYTEORDER_HAS_U64__ in byteorder.h,
*   because I wasn't sure it would be ok to put it in types.h
*   Upgraded it to 2.1.43
* Francois-Rene Rideau <fare@tunes.org> 19971012
*   Upgraded it to 2.1.57
*   to please Linus T., replaced huge #ifdef's between little/big endian
*   by nestedly #include'd files.
* Francois-Rene Rideau <fare@tunes.org> 19971205
*   Made it to 2.1.71; now a facelift:
*   Put files under include/linux/byteorder/
*   Split swab from generic support.
*
* TODO:
*   = Regular kernel maintainers could also replace all these manual
*   byteswap macros that remain, disseminated among drivers,
*   after some grep or the sources...
*   = Linus might want to rename all these macros and files to fit his taste,
*   to fit his personal naming scheme.
*   = it seems that a few drivers would also appreciate
*   nybble swapping support...
*   = every architecture could add their byteswap macro in asm/byteorder.h
*   see how some architectures already do (i386, alpha, ppc, etc)
*   = cpu_to_beXX and beXX_to_cpu might some day need to be well
*   distinguished throughout the kernel. This is not the case currently,
*   since little endian, big endian, and pdp endian machines needn't it.
*   But this might be the case for, say, a port of Linux to 20/21 bit
*   architectures (and F21 Linux addict around?).
*/
```

```
./include/linux/byteorder/little_endian.h
little_endian.h
No license information found.
```

```
./include/linux/byteorder/swab.h
swab.h
```

```
/*
* linux/byteorder/swab.h
* Byte-swapping, independently from CPU endianness
*   swabXX[ps]?(foo)
*
* Francois-Rene Rideau <fare@tunes.org> 19971205
*   separated swab functions from cpu_to_XX,
*   to clean up support for bizarre-endian architectures.
*
* See asm-i386/byteorder.h and suches for examples of how to provide
```

```
* architecture-dependent optimized versions
*
*/

./include/linux/compat.h
compat.h
No license information found.

./include/linux/compiler-gcc.h
compiler-gcc.h
/*
 * Common definitions for all gcc versions go here.
 */
/* Optimization barrier */
/* The "volatile" is due to gcc bugs */

./include/linux/compiler-gcc3.h
compiler-gcc3.h
No license information found.

./include/linux/compiler-gcc4.h
compiler-gcc4.h
/* GCC 4.1.[01] miscompiles __weak */

./include/linux/compiler-gcc5.h
compiler-gcc5.h
/* Mark functions as cold. gcc will assume any path leading to a call

./include/linux/compiler-gcc6.h
compiler-gcc6.h
/* Mark functions as cold. gcc will assume any path leading to a call

./include/linux/compiler.h
compiler.h
No license information found.

./include/linux/config.h
config.h
/* #include <linux/autoconf.h> */

./include/linux/crc32.h
crc32.h
/*
 * crc32.h
 * See linux/lib/crc32.c for license and changes
 */
/* #include <linux/bitrev.h> */
/* extern u32  crc32_be(u32 crc, unsigned char const *p, size_t len); */

./include/linux/crc7.h
```

crc7.h

No license information found.

./include/linux/ctype.h

ctype.h

/*

* NOTE! This ctype does not handle EOF like the standard C
* library is required to.

*/

./include/linux/edd.h

edd.h

/*

* linux/include/linux/edd.h

* Copyright (C) 2002, 2003, 2004 Dell Inc.

* by Matt Domsch <Matt_Domsch@dell.com>

*

* structures and definitions for the int 13h, ax={41,48}h

* BIOS Enhanced Disk Drive Services

* This is based on the T13 group document D1572 Revision 0 (August 14 2002)

* available at <http://www.t13.org/docs2002/d1572r0.pdf>. It is

* very similar to D1484 Revision 3 <http://www.t13.org/docs2002/d1484r3.pdf>

*

* In a nutshell, arch/{i386,x86_64}/boot/setup.S populates a scratch

* table in the boot_params that contains a list of BIOS-enumerated

* boot devices.

* In arch/{i386,x86_64}/kernel/setup.c, this information is

* transferred into the edd structure, and in drivers/firmware/edd.c, that

* information is used to identify BIOS boot disk. The code in setup.S

* is very sensitive to the size of these structures.

*

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*

*/

./include/linux/err.h

err.h

/* XXX U-BOOT XXX */

./include/linux/ethtool.h

ethtool.h

/*

* ethtool.h: Defines for Linux ethtool.

```
*
* Copyright (C) 1998 David S. Miller (davem@redhat.com)
* Copyright 2001 Jeff Garzik <jgarzik@pobox.com>
* Portions Copyright 2001 Sun Microsystems (thockin@sun.com)
* Portions Copyright 2002 Intel (eli.kupermann@intel.com,
*                               christopher.leech@intel.com,
*                               scott.feldman@intel.com)
* Portions Copyright (C) Sun Microsystems 2008
*/

./include/linux/fb.h
fb.h
/* Definitions of frame buffers                                     */

./include/linux/input.h
input.h
/*
* Copyright (c) 1999-2002 Vojtech Pavlik
*
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* under the terms of the GNU General Public License version 2 as published by
* the Free Software Foundation.
*/
/*
* Keys and buttons
*
* Most of the keys/buttons are modeled after USB HUT 1.12
* (see http://www.usb.org/developers/hidpage).
* Abbreviations in the comments:
* AC - Application Control
* AL - Application Launch Button
* SC - System Control
*/

./include/linux/ioctl.h
ioctl.h
No license information found.

./include/linux/ioport.h
ioport.h
/*
* ioport.h      Definitions of routines for detecting, reserving and
*               allocating system resources.
*
* Authors:      Linus Torvalds
*/

./include/linux/kbuild.h
kbuild.h
/*
```

```
* Copied from Linux:
* commit 37487a56523d402e25650da16c337acf4cecd13d
* Author: Christoph Lameter <clameter@sgi.com>
*/
```

```
./include/linux/linkage.h
```

```
linkage.h
```

```
/*
```

```
* U-boot - linkage.h
```

```
*
```

```
* Copyright (c) 2005-2007 Analog Devices Inc.
```

```
*
```

```
* See file CREDITS for list of people who contributed to this
* project.
```

```
*
```

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```

```
*
```

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```

```
*
```

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* MA 02110-1301 USA
```

```
*/
```

```
./include/linux/linux_string.h
```

```
linux_string.h
```

```
No license information found.
```

```
./include/linux/list.h
```

```
list.h
```

```
/*
```

```
* Simple doubly linked list implementation.
```

```
*
```

```
* Some of the internal functions ("__xxx") are useful when
* manipulating whole lists rather than single entries, as
* sometimes we already know the next/prev entries and we can
* generate better code by using them directly rather than
* using the generic single-entry routines.
```

```
*/
```

```
./include/linux/lzo.h
```

```
lzo.h
```

```
/*
```

```
* LZO Public Kernel Interface
* A mini subset of the LZO real-time data compression library
*
* Copyright (C) 1996-2005 Markus F.X.J. Oberhumer <markus@oberhumer.com>
*
* The full LZO package can be found at:
* http://www.oberhumer.com/opensource/lzo/
*
* Changed for kernel use by:
* Nitin Gupta <nitingupta910@gmail.com>
* Richard Purdie <rpurdie@openedhand.com>
*/
```

```
./include/linux/math64.h
```

```
math64.h
```

```
/**
```

```
* div_u64_rem - unsigned 64bit divide with 32bit divisor with remainder
```

```
*
```

```
* This is commonly provided by 32bit archs to provide an optimized 64bit  
* divide.
```

```
*/
```

```
./include/linux/mc146818rtc.h
```

```
mc146818rtc.h
```

```
/* mc146818rtc.h - register definitions for the Real-Time-Clock / CMOS RAM
```

```
* Copyright Torsten Duwe <duwe@informatik.uni-erlangen.de> 1993
```

```
* derived from Data Sheet, Copyright Motorola 1984 (!).
```

```
* It was written to be part of the Linux operating system.
```

```
*/
```

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```

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```

```
* at your option.
```

```
*/
```

```
./include/linux/mdio.h
```

```
mdio.h
```

```
/*
```

```
* linux/mdio.h: definitions for MDIO (clause 45) transceivers
```

```
* Copyright 2006-2009 Solarflare Communications Inc.
```

```
*
```

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```

```
*/
```

```
./include/linux/mii.h
```

```
mii.h
```

```
/*
```

```
* linux/mii.h: definitions for MII-compatible transceivers
```

```
* Originally drivers/net/sunhme.h.
```

```

*
* Copyright (C) 1996, 1999, 2001 David S. Miller (davem@redhat.com)
*/
/* Generic MII registers. */

./include/linux/mtd/bbm.h
bbm.h
/*
* linux/include/linux/mtd/bbm.h
*
* NAND family Bad Block Management (BBM) header file
* - Bad Block Table (BBT) implementation
*
* Copyright (c) 2005-2007 Samsung Electronics
* Kyungmin Park <kyungmin.park@samsung.com>
*
* Copyright (c) 2000-2005
* Thomas Gleixner <tglx@linuxtronix.de>
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston, MA 02110-1301 USA
*/

./include/linux/mtd/blktrans.h
blktrans.h
/*
* (C) 2003 David Woodhouse <dwmw2@infradead.org>
*
* Interface to Linux block layer for MTD 'translation layers'.
*
*/
/* XXX U-BOOT XXX */

./include/linux/mtd/concat.h
concat.h
/*
* MTD device concatenation layer definitions
*

```

```
* (C) 2002 Robert Kaiser <rkaiser@sysgo.de>
*
* This code is GPL
*/

./include/linux/mtd/doc2000.h
doc2000.h
/*
* Linux driver for Disk-On-Chip devices
*
* Copyright (C) 1999 Machine Vision Holdings, Inc.
* Copyright (C) 2001-2003 David Woodhouse <dwmw2@infradead.org>
* Copyright (C) 2002-2003 Greg Ungerer <gerg@snapgear.com>
* Copyright (C) 2002-2003 SnapGear Inc
*
* Released under GPL
*/

./include/linux/mtd/fsl_upm.h
fsl_upm.h
/*
* FSL UPM NAND driver
*
* Copyright (C) 2007 MontaVista Software, Inc.
*           Anton Vorontsov <avorontsov@ru.mvista.com>
*
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* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2 of
* the License, or (at your option) any later version.
*/

./include/linux/mtd/fsmc_nand.h
fsmc_nand.h
/*
* (C) Copyright 2010
* Vipin Kumar, ST Micoelectronics, vipin.kumar@st.com.
*
* See file CREDITS for list of people who contributed to this
* project.
*
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```

```
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./include/linux/mtd/inftl-user.h
inftl-user.h
/*
 * $Id: inftl-user.h,v 1.2 2005/11/07 11:14:56 gleixner Exp $
 *
 * Parts of INFTL headers shared with userspace
 *
 */

./include/linux/mtd/jffs2-user.h
jffs2-user.h
/*
 * $Id: jffs2-user.h,v 1.1 2004/05/05 11:57:54 dwmw2 Exp $
 *
 * JFFS2 definitions for use in user space only
 */
/* This file is blessed for inclusion by userspace */

./include/linux/mtd/mtd-abi.h
mtd-abi.h
/*
 * $Id: mtd-abi.h,v 1.13 2005/11/07 11:14:56 gleixner Exp $
 *
 * Portions of MTD ABI definition which are shared by kernel and user space
 */

./include/linux/mtd/mtd.h
mtd.h
/*
 * Copyright (C) 1999-2003 David Woodhouse <dwmw2@infradead.org> et al.
 *
 * Released under GPL
 */

./include/linux/mtd/nand.h
nand.h
/*
 * linux/include/linux/mtd/nand.h
 *
 * Copyright © 2000-2010 David Woodhouse <dwmw2@infradead.org>
 *                               Steven J. Hill <sjhill@realitydiluted.com>
 *                               Thomas Gleixner <tglx@linutronix.de>
 */
```

```
* This program is free software; you can redistribute it and/or modify
* it under the terms of the GNU General Public License version 2 as
* published by the Free Software Foundation.
*
* Info:
*     Contains standard defines and IDs for NAND flash devices
*
* Changelog:
*     See git changelog.
*/
```

```
./include/linux/mtd/nand_bch.h
```

```
nand_bch.h
```

```
/*
 * Copyright © 2011 Ivan Djelic <ivan.djelic@parrot.com>
 *
 * This program is free software; you can redistribute it and/or modify
 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 *
 * This file is the header for the NAND BCH ECC implementation.
 */
```

```
./include/linux/mtd/nand_ecc.h
```

```
nand_ecc.h
```

```
/*
 * drivers/mtd/nand_ecc.h
 *
 * Copyright (C) 2000 Steven J. Hill (sjhill@realitydiluted.com)
 *
 * This program is free software; you can redistribute it and/or modify
 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 *
 * This file is the header for the ECC algorithm.
 */
```

```
./include/linux/mtd/ndfc.h
```

```
ndfc.h
```

```
/*
 * linux/include/linux/mtd/ndfc.h
 *
 * Copyright (c) 2006 Thomas Gleixner <tglx@linutronix.de>
 *
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 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 *
 * Info:
 *     Contains defines, datastructures for ndfc nand controller
```

```
*
*/

./include/linux/mtd/nftl-user.h
nftl-user.h
/*
 * $Id: nftl-user.h,v 1.2 2005/11/07 11:14:56 gleixner Exp $
 *
 * Parts of NFTL headers shared with userspace
 *
 */
/* Block Control Information */

./include/linux/mtd/nftl.h
nftl.h
/*
 * (C) 1999-2003 David Woodhouse <dwmw2@infradead.org>
 */
/* these info are used in ReplUnitTable */

./include/linux/mtd/onenand.h
onenand.h
/*
 * linux/include/linux/mtd/onenand.h
 *
 * Copyright (C) 2005-2007 Samsung Electronics
 * Kyungmin Park <kyungmin.park@samsung.com>
 *
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 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 */

./include/linux/mtd/onenand_regs.h
onenand_regs.h
/*
 * linux/include/linux/mtd/onenand_regs.h
 *
 * OneNAND Register header file
 *
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 * Kyungmin Park <kyungmin.park@samsung.com>
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 * it under the terms of the GNU General Public License version 2 as
 * published by the Free Software Foundation.
 */

./include/linux/mtd/partitions.h
partitions.h
```

```
/*
 * MTD partitioning layer definitions
 *
 * (C) 2000 Nicolas Pitre <nico@cam.org>
 *
 * This code is GPL
 *
 * $Id: partitions.h,v 1.17 2005/11/07 11:14:55 gleixner Exp $
 */

./include/linux/mtd/samsung_onenand.h
samsung_onenand.h
/*
 * Copyright (C) 2005-2009 Samsung Electronics
 * Minkyu Kang <mk7.kang@samsung.com>
 * Kyungmin Park <kyungmin.park@samsung.com>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */

./include/linux/mtd/st_smi.h
st_smi.h
/*
 * (C) Copyright 2009
 * Vipin Kumar, ST Micoelectronics, vipin.kumar@st.com.
 *
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 * project.
 *
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 *
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/linux/mtd/ubi.h
```

```
ubi.h
```

```
/*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA  
*  
* Author: Artem Bitvutskiy (Битюцкий Артём)  
*/
```

```
./include/linux/netdevice.h
```

```
netdevice.h
```

```
/*  
* INET          An implementation of the TCP/IP protocol suite for the LINUX  
*              operating system. INET is implemented using the BSD Socket  
*              interface as the means of communication with the user level.  
*  
*              Definitions for the Interfaces handler.  
*  
* Version:     @(#)dev.h          1.0.10  08/12/93  
*  
* Authors:     Ross Biro  
*              Fred N. van Kempen, <waltje@uWalt.NL.Mugnet.ORG>  
*              Corey Minyard <wf-rch!minyard@relay.EU.net>  
*              Donald J. Becker, <becker@cesdis.gsfc.nasa.gov>  
*              Alan Cox, <Alan.Cox@linux.org>  
*              Bjorn Ekwall. <bjorn@blox.se>
```

```
*           Pekka Riikonen <priikone@poseidon.pspt.fi>
*
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*           modify it under the terms of the GNU General Public License
*           as published by the Free Software Foundation; either version
*           2 of the License, or (at your option) any later version.
*
*           Moved to /usr/include/linux for NET3
*/
```

```
./include/linux/poison.h
poison.h
/***** include/linux/list.h *****/
/*
 * used to verify that nobody uses non-initialized list entries.
 */
```

```
./include/linux/posix_types.h
posix_types.h
/*
 * This allows for 1024 file descriptors: if NR_OPEN is ever grown
 * beyond that you'll have to change this too. But 1024 fd's seem to be
 * enough even for such "real" unices like OSF/1, so hopefully this is
 * one limit that doesn't have to be changed [again].
 *
 * Note that POSIX wants the FD_CLEAR(fd,fdsetp) defines to be in
 * <sys/time.h> (and thus <linux/time.h>) - but this is a more logical
 * place for them. Solved by having dummy defines in <sys/time.h>.
 */
```

```
./include/linux/rbtree.h
rbtree.h
/*
```

```
./include/linux/screen_info.h
screen_info.h
/*
 * These are set up by the setup-routine at boot-time:
 */
```

```
./include/linux/stat.h
stat.h
No license information found.
```

```
./include/linux/stddef.h
stddef.h
No license information found.
```

```
./include/linux/string.h
string.h
```

No license information found.

```
./include/linux/stringify.h  
stringify.h
```

```
/* Indirect stringification. Doing two levels allows the parameter to be a  
 * macro itself. For example, compile with -DFOO=bar, __stringify(FOO)  
 * converts to "bar".  
 */
```

```
./include/linux/time.h  
time.h
```

No license information found.

```
./include/linux/types.h  
types.h
```

No license information found.

```
./include/linux/unaligned/access_ok.h  
access_ok.h
```

No license information found.

```
./include/linux/unaligned/be_byteshift.h  
be_byteshift.h
```

No license information found.

```
./include/linux/unaligned/generic.h  
generic.h
```

```
/*  
 * Cause a link-time error if we try an unaligned access other than  
 * 1,2,4 or 8 bytes long  
 */
```

```
./include/linux/unaligned/le_byteshift.h  
le_byteshift.h
```

No license information found.

```
./include/linux/usb/cdc.h  
cdc.h
```

```
/*  
 * USB Communications Device Class (CDC) definitions  
 *  
 * CDC says how to talk to lots of different types of network adapters,  
 * notably ethernet adapters and various modems. It's used mostly with  
 * firmware based USB peripherals.  
 *  
 * Ported to U-boot by: Thomas Smits <ts.smits@gmail.com> and  
 * Remy Bohmer <linux@bohmer.net>  
 */
```

```
./include/linux/usb/ch9.h
```

ch9.h

```
/*
 * This file holds USB constants and structures that are needed for
 * USB device APIs. These are used by the USB device model, which is
 * defined in chapter 9 of the USB 2.0 specification and in the
 * Wireless USB 1.0 (spread around). Linux has several APIs in C that
 * need these:
 *
 * - the master/host side Linux-USB kernel driver API;
 * - the "usbfs" user space API; and
 * - the Linux "gadget" slave/device/peripheral side driver API.
 *
 * USB 2.0 adds an additional "On The Go" (OTG) mode, which lets systems
 * act either as a USB master/host or as a USB slave/device. That means
 * the master and slave side APIs benefit from working well together.
 *
 * There's also "Wireless USB", using low power short range radios for
 * peripheral interconnection but otherwise building on the USB framework.
 *
 * Note all descriptors are declared '__attribute__((packed))' so that:
 *
 * [a] they never get padded, either internally (USB spec writers
 *     probably handled that) or externally;
 *
 * [b] so that accessing bigger-than-a-bytes fields will never
 *     generate bus errors on any platform, even when the location of
 *     its descriptor inside a bundle isn't "naturally aligned", and
 *
 * [c] for consistency, removing all doubt even when it appears to
 *     someone that the two other points are non-issues for that
 *     particular descriptor type.
 */
```

./include/linux/usb/composite.h

composite.h

```
/*
 * composite.h -- framework for usb gadgets which are composite devices
 *
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 *
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 */
```

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* along with this program; if not, write to the Free Software
* Foundation, Inc., 51 Franklin St, Fifth Floor, Boston, MA 02110-1301 USA
*/
```

```
./include/linux/usb/gadget.h
```

```
gadget.h
```

```
/*
 * <linux/usb/gadget.h>
 *
 * We call the USB code inside a Linux-based peripheral device a "gadget"
 * driver, except for the hardware-specific bus glue. One USB host can
 * master many USB gadgets, but the gadgets are only slaved to one host.
 *
 *
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 * All Rights Reserved.
 *
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 *
 * Ported to U-boot by: Thomas Smits <ts.smits@gmail.com> and
 * Remy Bohmer <linux@bohmer.net>
 */
```

```
./include/linux/usb/musb.h
```

```
musb.h
```

```
/*
 * This is used to for host and peripheral modes of the driver for
 * Inventra (Multidrop) Highspeed Dual-Role Controllers: (M)HDCR.
 *
 * Board initialization should put one of these into dev->platform_data,
 * probably on some platform_device named "musb-hdrc". It encapsulates
 * key configuration differences between boards.
 */
```

```
./include/linux_logo.h
```

```
linux_logo.h
```

```
/* $Id: linux_logo.h,v 1.5 1998/07/30 16:30:58 jj Exp $
 * include/linux/linux_logo.h: This is a linux logo
 * to be displayed on boot.
 *
 * Copyright (C) 1996 Larry Ewing (lewing@isc.tamu.edu)
 * Copyright (C) 1996,1998 Jakub Jelinek (jj@sunsite.mff.cuni.cz)
 *
 * You can put anything here, but:
 * LINUX_LOGO_COLORS has to be less than 224
 * image size has to be 80x80
 * values have to start from 0x20
 * (i.e. RGB(linux_logo_red[0],
 * linux_logo_green[0],
```

```

*          linux_logo_blue[0]) is color 0x20)
* BW image has to be 80x80 as well, with MS bit
* on the left
* Serial_console ascii image can be any size,
* but should contain %s to display the version
*/

./include/lmb.h
lmb.h
/*
* Logical memory blocks.
*
* Copyright (C) 2001 Peter Bergner, IBM Corp.
*
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* modify it under the terms of the GNU General Public License
* as published by the Free Software Foundation; either version
* 2 of the License, or (at your option) any later version.
*/

./include/logbuff.h
logbuff.h
/*
* (C) Copyright 2002-2007
* Detlev Zundel, dzu@denx.de.
*
* See file CREDITS for list of people who contributed to this
* project.
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./include/lxt971a.h
lxt971a.h
/*****
*
* Copyright (C) 2004 by FS Forth-Systeme GmbH.

```

```
* All rights reserved.
*
* $Id: ns9750_eth.h,v 1.2 2004/02/24 13:25:39 mpietrek Exp $
* @Author: Markus Pietrek
* @References: [1] NS9750 Hardware Reference, December 2003
*              [2] Intel LXT971 Datasheet #249414 Rev. 02
*              [3] NS7520 Linux Ethernet Driver
*
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* MA 02111-1307 USA
*
*****/
```

```
./include/lynxkdi.h
```

```
lynxkdi.h
```

```
/*
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* Orbacom Systems, Inc.
*
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* project.
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* MA 02111-1307 USA
*/
```

```
./include/lzma/LzmaDec.h
```

```
LzmaDec.h
```

```
/*
```

```
* Fake include for LzmaDec.h
```

```
*
```

```
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```

```
* Luigi 'Comio' Mantellini (luigi.mantellini@idf-hit.com)
```

```
*
```

```
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* project.
```

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*
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*
```

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*
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* MA 02111-1307 USA
```

```
*/
```

```
./include/lzma/LzmaTools.h
```

```
LzmaTools.h
```

```
/*
```

```
* Fake include for LzmaTools.h
```

```
*
```

```
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```

```
* Luigi 'Comio' Mantellini (luigi.mantellini@idf-hit.com)
```

```
*
```

```
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* project.
```

```
*
```

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```

```
*
```

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* MA 02111-1307 USA  
*/
```

```
./include/lzma/LzmaTypes.h  
LzmaTypes.h
```

```
/*  
* Fake include for Types.h  
*  
* Copyright (C) 2007-2009 Industrie Dial Face S.p.A.  
* Luigi 'Comio' Mantellini (luigi.mantellini@idf-hit.com)  
*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/malloc.h  
malloc.h
```

```
/*  
* VERSION 2.6.6 Sun Mar 5 19:10:03 2000 Doug Lea (dl at gee)  
* Why use this malloc?
```

```
./include/mb862xx.h  
mb862xx.h
```

```
/*  
* (C) Copyright 2007  
* DENX Software Engineering, Anatolij Gustschin, agust@denx.de  
*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/mc13783.h
mc13783.h
```

```
/*
* (C) Copyright 2011
* Helmut Raiger, HALE electronic GmbH, helmut.raiger@hale.at
*
* See file CREDITS for list of people who contributed to this
* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/mc13892.h
mc13892.h
```

```
/*
* (C) Copyright 2010
* Stefano Babic, DENX Software Engineering, sbabic@denx.de.
*
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*
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* MA 02111-1307 USA
*/
```

```
./include/mc34704.h
mc34704.h
```

```
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* the License, or (at your option) any later version.
*
*/
```

```
./include/mc9sdz60.h
mc9sdz60.h
```

```
/*
* Copyright (C) 2009 Marc Kleine-Budde <mkl@pengutronix.de>
*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
```

```
*
*/

./include/MCD_dma.h
MCD_dma.h
/*
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 *
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 * project.
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 */

./include/MCD_progCheck.h
MCD_progCheck.h
/*
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 *
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 * project.
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 */
```

./include/MCD_tasksInit.h

MCD_tasksInit.h

```
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 * GNU General Public License for more details.
 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./include/menu.h

menu.h

```
/*
 * Copyright 2010-2011 Calxeda, Inc.
 *
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 * under the terms of the GNU General Public License as published by the Free
 * Software Foundation; either version 2 of the License, or (at your option)
 * any later version.
 *
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 * ANY WARRANTY; without even the implied warranty of MERCHANTABILITY or
 * FITNESS FOR A PARTICULAR PURPOSE. See the GNU General Public License for
 * more details.
 *
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 * this program. If not, see <http://www.gnu.org/licenses/>.
 */
```

./include/micrel.h

micrel.h

No license information found.

./include/miiphy.h

miiphy.h

```
/*-----+
```

```
./include/mii_phy.h
```

```
mii_phy.h
```

```
No license information found.
```

```
./include/mk48t59.h
```

```
mk48t59.h
```

```
/*
```

```
* (C) Copyright 2001 Sysgo Real-Time Solutions, GmbH <www.elinos.com>
```

```
* Andreas Heppel <aheppel@sysgo.de>
```

```
*
```

```
* See file CREDITS for list of people who contributed to this  
* project.
```

```
*
```

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* the License, or (at your option) any later version.
```

```
*
```

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```

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*
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA
```

```
*/
```

```
./include/mmc.h
```

```
mmc.h
```

```
/*
```

```
* Copyright 2008,2010 Freescale Semiconductor, Inc
```

```
* Andy Fleming
```

```
*
```

```
* Based (loosely) on the Linux code
```

```
*
```

```
* See file CREDITS for list of people who contributed to this  
* project.
```

```
*
```

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```

```
*
```

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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/mpc106.h
```

```
mpc106.h
```

```
/*  
* (C) Copyright 2001 Sysgo Real-Time Solutions, GmbH <www.elinos.com>  
* Andreas Heppel <aheppel@sysgo.de>  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/mpc5xx.h
```

```
mpc5xx.h
```

```
/*  
* (C) Copyright 2003  
* Martin Winistoerfer, martinwinistoerfer@gmx.ch.  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
*  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/mpc5xxx.h
```

```
mpc5xxx.h
```

```
/*  
* include/asm-ppc/mpc5xxx.h  
*  
* Prototypes, etc. for the Motorola MPC5xxx  
* embedded cpu chips  
*  
* 2003 (c) MontaVista, Software, Inc.  
* Author: Dale Farnsworth <dfarnsworth@mvista.com>  
*  
* 2003 (C) Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
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*  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/mpc5xxx_sdma.h
```

```
mpc5xxx_sdma.h
```

```
/*  
* (C) Copyright 2003  
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
*  
* This file is based on code  
* (C) Copyright Motorola, Inc., 2000  
*  
* odin smartdma header file
```

```
*/  
  
./include/mpc8220.h  
mpc8220.h  
/*  
* include/mpc8220.h  
*  
* Prototypes, etc. for the Motorola MPC8220  
* embedded cpu chips  
*  
* 2004 (c) Freescale, Inc.  
* Author: TsiChung Liew <Tsi-Chung.Liew@freescale.com>  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/mpc824x.h  
mpc824x.h  
/*  
* Copyright Rob Taylor, Flying Pig Systems Ltd. 2000.  
* Copyright (C) 2001, James Dougherty, jfd@cs.stanford.edu  
*  
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* project.  
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```

```

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* MA 02111-1307 USA
*/

./include/mpc8260.h
mpc8260.h
/*
* (C) Copyright 2000, 2001
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
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* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./include/mpc8260_irq.h
mpc8260_irq.h
/*****
/* most of this was ripped out of include/asm-ppc/irq.h from the Linux/PPC */
/* source. There was no copyright information in the file. */
/*
* this is the # irq's for all ppc arch's (pmac/chrp/prep)
* so it is the max of them all
*
* [let's just worry about 8260 for now - mjj]
*/

./include/mpc83xx.h
mpc83xx.h
/*
* Copyright (C) 2004-2007, 2010 Freescale Semiconductor, Inc.
*
* See file CREDITS for list of people who contributed to this

```

```
* project.
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* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2 of
* the License, or (at your option) any later version.
*/

./include/mpc85xx.h
mpc85xx.h
/*
* Copyright 2004, 2007 Freescale Semiconductor.
* Copyright(c) 2003 Motorola Inc.
*/
/* define for common ppc_asm.tmpl */

./include/mpc86xx.h
mpc86xx.h
/*
* Copyright 2006 Freescale Semiconductor.
* Jeffrey Brown
* Srikanth Srinivasan (srikanth.srinivasan@freescale.com)
*/

./include/mpc8xx.h
mpc8xx.h
/*
* (C) Copyright 2000-2004
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
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* project.
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* MA 02111-1307 USA
*/

./include/mpc8xx_irq.h
```

mpc8xx_irq.h

```
/* The MPC8xx cores have 16 possible interrupts. There are eight
 * possible level sensitive interrupts assigned and generated internally
 * from such devices as CPM, PCMCIA, RTC, PIT, TimeBase and Decrementer.
 * There are eight external interrupts (IRQs) that can be configured
 * as either level or edge sensitive.
 *
 * On some implementations, there is also the possibility of an 8259
 * through the PCI and PCI-ISA bridges.
 *
 * We don't support the 8259 (yet).
 */
```

./include/mtd/cfi_flash.h

cfi_flash.h

```
/*
 * (C) Copyright 2009
 * Stefan Roesse, DENX Software Engineering, sr@denx.de.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
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 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./include/mtd/ubi-user.h

ubi-user.h

```
/*
 * Copyright (c) International Business Machines Corp., 2006
 *
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 * it under the terms of the GNU General Public License as published by
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*
* Author: Artem Bityutskiy (Битюцкий Артём)
*/
```

```
./include/mtd_node.h
mtd_node.h
/*
* Info we use to search for a flash node in DTB.
*/
```

```
./include/mv88e6352.h
mv88e6352.h
/*
* (C) Copyright 2012
* Valentin Lontgchamp, Keymile AG, valentin.longchamp@keymile.com
*
* See file CREDITS for list of people who contributed to this
* project.
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
* MA 02110-1301 USA
*/
```

```
./include/mvmfp.h
mvmfp.h
/*
* (C) Copyright 2010
* Marvell Semiconductor <www.marvell.com>
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
*
* See file CREDITS for list of people who contributed to this
```

```
* project.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,  
* MA 02110-1301 USA  
*/
```

```
./include/nand.h
```

```
nand.h
```

```
/*  
* (C) Copyright 2005  
* 2N Telekomunikace, a.s. <www.2n.cz>  
* Ladislav Michl <michl@2n.cz>  
*  
* See file CREDITS for list of people who contributed to this  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/net.h
```

```
net.h
```

```
/*  
* LiMon Monitor (LiMon) - Network.  
*  
* Copyright 1994 - 2000 Neil Russell.  
* (See License)
```

```
*
*
* History
*   9/16/00   bor   adapted to TQM823L/STK8xxL board, RARP/TFTP boot added
*/
```

```
./include/netdev.h
```

```
netdev.h
```

```
/*
* (C) Copyright 2008
* Benjamin Warren, biggerbadderben@gmail.com
*
* See file CREDITS for list of people who contributed to this
* project.
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/nios2-epcs.h
```

```
nios2-epcs.h
```

```
/*
* (C) Copyright 2004, Psyent Corporation <www.psyent.com>
* Scott McNutt <smcnutt@psyent.com>
*
* See file CREDITS for list of people who contributed to this
* project.
*
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*/
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/nios2-io.h
```

```
nios2-io.h
```

```
/*
 * (C) Copyright 2004, Psyent Corporation <www.psyent.com>
 * Scott McNutt <smcnutt@psyent.com>
 *
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 *
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 *
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./include/nios2-yanu.h
```

```
nios2-yanu.h
```

```
/*
 * (C) Copyright 2006, Imagos S.a.s <www.imagos.it>
 * Renato Andreola <renato.andreola@imagos.it>
 *
 * See file CREDITS for list of people who contributed to this
 * project.
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*/
```

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* MA 02111-1307 USA
*/
```

```
./include/nios2.h
```

```
nios2.h
```

```
/*
 * (C) Copyright 2004, Psyent Corporation <www.psyent.com>
 * Scott McNutt <smcnutt@psyent.com>
 *
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 * project.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./include/nomadik.h
```

```
nomadik.h
```

```
/* Collection of constants used to access Nomadik registers */
/* Base addresses of our peripherals */
```

```
./include/ns16550.h
```

```
ns16550.h
```

```
/*
 * NS16550 Serial Port
 * originally from linux source (arch/powerpc/boot/ns16550.h)
 *
 * Cleanup and unification
 * (C) 2009 by Detlev Zundel, DENX Software Engineering GmbH
 *
 * modified slightly to
 * have addresses as offsets from CONFIG_SYS_ISA_BASE
 * added a few more definitions
 * added prototypes for ns16550.c
 * reduced no of com ports to 2
*/
```

```
* modifications (c) Rob Taylor, Flying Pig Systems. 2000.  
*  
* added support for port on 64-bit bus  
* by Richard Danter (richard.danter@windriver.com), (C) 2005 Wind River Systems  
*/
```

```
./include/ns87308.h
```

```
ns87308.h
```

```
/*  
* (C) Copyright 2000  
* Rob Taylor, Flying Pig Systems. robt@flyingpig.com.  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/ns9750_bbus.h
```

```
ns9750_bbus.h
```

```
/*  
*  
* Copyright (C) 2004 by FS Forth-Systeme GmbH.  
* All rights reserved.  
*  
* $Id: ns9750_bbus.h,v 1.1 2004/02/16 10:37:20 mpietrek Exp $  
* @Author: Markus Pietrek  
* @Descr: Definitions for BBus usage  
* @References: [1] NS9750 Hardware Reference Manual/December 2003 Chap. 10  
*  
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* MA 02111-1307 USA

*
*
*****/

./include/ns9750_mem.h

ns9750_mem.h

/******

*
* Copyright (C) 2004 by FS Forth-Systeme GmbH.
* All rights reserved.
*
* \$Id: ns9750_mem.h,v 1.1 2004/02/16 10:37:20 mpietrek Exp \$
* @Author: Markus Pietrek
* @Descr: Definitions for Memory Control Module
* @References: [1] NS9750 Hardware Reference Manual/December 2003 Chap. 5

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* MA 02111-1307 USA

*
*****/

./include/ns9750_ser.h

ns9750_ser.h

/******

*
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*
* \$Id: ns9750_ser.h,v 1.1 2004/02/16 10:37:20 mpietrek Exp \$
* @Author: Markus Pietrek
* @References: [1] NS9750 Hardware Reference, December 2003

```

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*
*****/

./include/ns9750_sys.h
ns9750_sys.h
/*****
*
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*
* $Id: ns9750_sys.h,v 1.1 2004/02/16 10:37:20 mpietrek Exp $
* @Author: Markus Pietrek
* @Descr: Definitions for SYS Control Module
* @References: [1] NS9750 Hardware Reference Manual/December 2003 Chap. 4
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* MA 02111-1307 USA
*
*****/

./include/onenand_uboot.h
onenand_uboot.h
/*

```

```
* Header file for OneNAND support for U-Boot
*
* Adaptation from kernel to U-Boot
*
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* Kyungmin Park <kyungmin.park@samsung.com>
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*/

./include/os.h
os.h
/*
* Operating System Interface
*
* This provides access to useful OS routines for the sandbox architecture.
* They are kept in a separate file so we can include system headers.
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* MA 02111-1307 USA
*/

./include/part.h
part.h
/*
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* MA 02111-1307 USA
*/
```

```
./include/part_efi.h
part_efi.h
```

```
/*
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* Richard Retanubun <RichardRetanubun@RuggedCom.com>
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* MA 02111-1307 USA
*/
```

```
./include/pca953x.h
pca953x.h
```

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* MA 02111-1307 USA
*/
```

```
./include/pca9564.h
```

```
pca9564.h
```

```
/*
* File:          include/pca9564.h
* Author:
*
* Created:       2009-06-23
* Description:   PCA9564 i2c bridge driver
*
* Modified:
*               Copyright 2009 CJSC "NII STT", http://www.niistt.ru/
*
* Bugs:
*
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*/
```

```
./include/pca9698.h
```

```
pca9698.h
```

```
/*
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* MA 02111-1307 USA
*/
```

```
./include/pci.h
```

```
pci.h
```

```
/*
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* Andreas Heppel <aheppel@sysgo.de>
*
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* MA 02111-1307 USA
*/
```

```
./include/pci_ids.h
```

```
pci_ids.h
```

```
/*
* PCI Class, Vendor and Device IDs
*
* Please keep sorted.
*/
```

```
*      Do not add new entries to this file unless the definitions
*      are shared between multiple drivers.
*/
/* Device classes and subclasses */

./include/pcmcia/cirrus.h
cirrus.h
/*
* cirrus.h 1.4 1999/10/25 20:03:34
*
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*/

./include/pcmcia/i82365.h
i82365.h
/*
* i82365.h 1.21 2001/08/24 12:15:33
*
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* under either the MPL or the GPL.
*/
```

```
./include/pcmcia/ss.h
```

```
ss.h
```

```
/*
```

```
* ss.h 1.31 2001/08/24 12:16:13
```

```
*
```

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```

```
*/
```

```
./include/pcmcia/ti113x.h
```

```
ti113x.h
```

```
/*
 * ti113x.h 1.31 2002/05/12 18:19:47
 *
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 */
```

```
./include/pcmcia/yenta.h
yenta.h
```

```
/*
 * yenta.h 1.20 2001/08/24 12:15:34
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*/
```

```
./include/pcmcia.h
```

```
pcmcia.h
```

```
/*
```

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```
*
```

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```

```
*/
```

```
./include/pc_keyb.h
```

```
pc_keyb.h
```

```
/*
```

```
* include/linux/pc_keyb.h
```

```
*
```

```
* PC Keyboard And Keyboard Controller
```

```
*
```

```
* (c) 1997 Martin Mares <mj@atrey.karlin.mff.cuni.cz>
```

```
*/
```

```
/*
```

```
* Configuration Switches
```

```
*/
```

```
./include/phy.h
```

```
phy.h
```

```

/*
 * Copyright 2011 Freescale Semiconductor, Inc.
 *   Andy Fleming <afleming@freescale.com>
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 * MA 02111-1307 USA
 *
 * This file pretty much stolen from Linux's mii.h/ethtool.h/phy.h
 */

./include/phymem.h
phymem.h
/*
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 *
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 * GNU General Public License ("GPL") version 2 as published by the Free
 * Software Foundation.
 */

/*
 * These functions work like memset but operate on physical memory which may
 * not be accessible directly.
 *
 * @param s      The physical address to start setting memory at.
 * @param c      The character to set each byte of the region to.
 * @param n      The number of bytes to set.
 *
 * @return       The physical address of the memory which was set.
 */

./include/pl330.h
pl330.h
/*
 * Copyright (C) 2013 Altera Corporation <www.altera.com>
 *
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```

```
*      Jaswinder Singh <jassi.bran@samsung.com>
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
```

```
./include/post.h
```

```
post.h
```

```
/*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/power/battery.h
```

```
battery.h
```

```
/*
* Copyright (C) 2012 Samsung Electronics
```

```
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*/
```

```
./include/power/fg_battery_cell_params.h
```

```
fg_battery_cell_params.h
```

```
/*
* Copyright (C) 2012 Samsung Electronics
* Lukasz Majewski <l.majewski@samsung.com>
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*/
```

```
./include/power/max17042_fg.h
```

```
max17042_fg.h
```

```
/*
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```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
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*/
```

```
./include/power/max77686_pmic.h
max77686_pmic.h
```

```
/*
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*/
```

```
./include/power/max8997_muic.h
max8997_muic.h
```

```
/*
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```
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```

```
./include/power/max8997_pmic.h
max8997_pmic.h
```

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* MA 02111-1307 USA
*/
```

```
./include/power/max8998_pmic.h
max8998_pmic.h
```

```
/*
* Copyright (C) 2011 Samsung Electronics
```

```
* Lukasz Majewski <l.majewski@samsung.com>
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*/
```

```
./include/power/pmic.h
```

```
pmic.h
```

```
/*
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*/
```

```
./include/power/power_chrg.h
```

```
power_chrg.h
```

```
/*
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```
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* MA 02111-1307 USA
*/
```

```
./include/ppc_defs.h
```

```
ppc_defs.h
```

```
/*
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* MA 02111-1307 USA
*/
```

```
./include/ps2mult.h
```

```
ps2mult.h
```

```
No license information found.
```

./include/pwm.h

pwm.h

```
/*
 * header file for pwm driver.
 *
 * Copyright (c) 2011 samsung electronics
 * Donghwa Lee <dh09.lee@samsung.com>
 *
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 * MA 02111-1307 USA
 */
```

./include/radeon.h

radeon.h

No license information found.

./include/reiserfs.h

reiserfs.h

```
/*
 * Copyright 2000-2002 by Hans Reiser, licensing governed by reiserfs/README
 *
 * GRUB -- GRand Unified Bootloader
 * Copyright (C) 2000, 2001 Free Software Foundation, Inc.
 *
 * (C) Copyright 2003 Sysgo Real-Time Solutions, AG <www.elinos.com>
 * Pavel Bartusek <pba@sysgo.de>
 *
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```

```
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*/
```

```
./include/rtc.h
```

```
rtc.h
```

```
/*
```

```
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* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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* MA 02111-1307 USA
*/
```

```
./include/s6e63d6.h
```

```
s6e63d6.h
```

```
/*
```

```
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```

```
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* MA 02111-1307 USA  
*/
```

```
./include/SA-1100.h  
SA-1100.h
```

```
/*  
* FILE SA-1100.h  
*  
* Version 1.2  
* Author Copyright (c) Marc A. Viredaz, 1998  
* DEC Western Research Laboratory, Palo Alto, CA  
* Date January 1998 (April 1997)  
* System StrongARM SA-1100  
* Language C or ARM Assembly  
* Purpose Definition of constants related to the StrongARM  
* SA-1100 microprocessor (Advanced RISC Machine (ARM)  
* architecture version 4). This file is based on the  
* StrongARM SA-1100 data sheet version 2.2.  
*  
* Language-specific definitions are selected by the  
* macro "LANGUAGE", which should be defined as either  
* "C" (default) or "Assembly".  
*/
```

```
./include/sata.h  
sata.h
```

No license information found.

```
./include/scsi.h  
scsi.h
```

```
/*  
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```

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* MA 02111-1307 USA  
*  
*/
```

```
./include/sdhci.h
```

```
sdhci.h
```

```
/*
```

```
* Copyright 2011, Marvell Semiconductor Inc.
```

```
* Lei Wen <leiwen@marvell.com>
```

```
*
```

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* project.
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```

```
* MA 02111-1307 USA
```

```
*
```

```
* Back ported to the 8xx platform (from the 8260 platform) by
```

```
* Murray.Jensen@cmst.csiro.au, 27-Jan-01.
```

```
*/
```

```
./include/search.h
```

```
search.h
```

```
/*
```

```
* Declarations for System V style searching functions.
```

```
* Copyright (C) 1995-1999, 2000 Free Software Foundation, Inc.
```

```
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```

```
*
```

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*/
```

```
./include/sed13806.h
sed13806.h
```

```
/*
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* Pierre AUBERT p.aubert@staubli.com
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
/* Video support for Epson SED13806 chipset */
```

```
./include/sed156x.h
sed156x.h
```

```
/*
* (C) Copyright 2004
*
* Pantelis Antoniou <panto@intracom.gr>
* Intracom S.A.
*
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* project.
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* MA 02111-1307 USA
*/
```

```
./include/serial.h
serial.h
No license information found.
```

```
./include/sha1.h
sha1.h
/**
 * \file sha1.h
 * based from http://xyssl.org/code/source/sha1/
 * FIPS-180-1 compliant SHA-1 implementation
 *
 * Copyright (C) 2003-2006 Christophe Devine
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 * License along with this library; if not, write to the Free Software
 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
 * MA 02110-1301 USA
 */
/*
 * The SHA-1 standard was published by NIST in 1993.
 *
 * http://www.itl.nist.gov/fipspubs/fip180-1.htm
 */
```

```
./include/sha256.h
sha256.h
/* Reset watchdog each time we process this many bytes */
```

```
./include/sh_pfc.h
sh_pfc.h
/*
```

```
* SuperH Pin Function Controller Support
* Copy from Linux kernel. (include/linux/sh_pfc.h)
*
* Copyright (c) 2008 Magnus Damm
*
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*/
```

```
./include/sh_tmu.h
```

```
sh_tmu.h
```

```
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* MA 02111-1307 USA
*/
```

```
./include/sja1000.h
```

```
sja1000.h
```

```
/*
* Copyright 2009, Matthias Fuchs <matthias.fuchs@esd.eu>
*
* SJA1000 register layout for basic CAN mode
*
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*/
```

```
./include/sm501.h
```

```
sm501.h
```

```
/*
```

```
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* Stäubli Faverges - <www.staubli.com>
* Pierre AUBERT p.aubert@staubli.com
*
```

```
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* Martin Krause TQ-Systems GmbH martin.krause@tqs.de
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*/
```

```
./include/smiLynxEM.h
```

```
smiLynxEM.h
```

```
/*
```

```
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* Frank Gottschling <fgottschling@eltec.de>
*
```

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* MA 02111-1307 USA
*/
```

```
./include/sound.h
```

```
sound.h
```

```
/*
* Copyright (C) 2012 Samsung Electronics
* R. Chandrasekar <rcsekar@samsung.com>
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* MA 02111-1307 USA
*/
```

```
./include/spartan2.h
```

```
spartan2.h
```

```
/*
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* MA 02111-1307 USA
*
*/
```

```
./include/spartan3.h
spartan3.h
```

```
/*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
```

```
./include/spd.h
spd.h
```

```
/*
* Copyright (C) 2003 Arabella Software Ltd.
* Yuli Barcohen <yuli@arabellasw.com>
*
* Serial Presence Detect (SPD) EEPROM format according to the
* Intel's PC SDRAM Serial Presence Detect (SPD) Specification,
* revision 1.2B, November 1999
```

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*/
```

```
./include/spd_sdram.h
spd_sdram.h
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```

```
./include/spi.h
spi.h
/*
* (C) Copyright 2001
* Gerald Van Baren, Custom IDEAS, vanbaren@cideas.com.
*
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* MA 02111-1307 USA
*/
```

```
./include/spi_flash.h
spi_flash.h
/*
* Interface to SPI flash
*
```

```
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*/
```

```
./include/spl.h
```

```
spl.h
```

```
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* MA 02111-1307 USA
*/
```

```
./include/status_led.h
```

```
status_led.h
```

```
/*
* (C) Copyright 2000-2004
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
```

```
*
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* MA 02111-1307 USA
*/
```

```
./include/stdio_dev.h
stdio_dev.h
```

```
/*
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* Paolo Scaffardi, AIRVENT SAM s.p.a - RIMINI(ITALY), arsenio@tin.it
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/stratixII.h
stratixII.h
```

```
/*
* (C) Copyright 2007
* Eran Liberty, Extricom, eran.liberty@gmail.com
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
```

```
./include/sym53c8xx.h
```

```
sym53c8xx.h
```

```
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* Denis Peter, MPL AG Switzerland
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* MA 02111-1307 USA
*
*
* Most of these definitions are derived from
* linux/drivers/scsi/sym53c8xx_defs.h
*
*/
```

```
./include/synopsys/dwccdr21mctl.h  
dwccdr21mctl.h
```

```
/*  
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 * Macpaul Lin <macpaul@andestech.com>  
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 * Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.  
 */
```

```
./include/systemace.h  
systemace.h
```

```
/*  
 * Copyright (c) 2004 Picture Elements, Inc.  
 * Stephen Williams (steve@picture1.com)  
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 * along with this program; if not, write to the Free Software  
 * Foundation, Inc., 59 Temple Place - Suite 330, Boston, MA 02111-1307, USA  
 */
```

```
./include/s_record.h  
s_record.h
```

```
/*  
 * (C) Copyright 2000  
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.  
 *  
 */
```

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* MA 02111-1307 USA
*/

./include/tegra-kbc.h
tegra-kbc.h
/*
* Copyright (c) 2011 The Chromium OS Authors. All rights reserved.
* Use of this source code is governed by a BSD-style license that can be
* found in the LICENSE file.
*/

./include/timestamp.h
timestamp.h
/*
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*
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* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/tpm.h
```

```
tpm.h
```

```
/*
```

```
* Copyright (c) 2011 The Chromium OS Authors.
```

```
*
```

```
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* project.
```

```
*
```

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```

```
*
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* MA 02111-1307 USA
```

```
*/
```

```
./include/tps6586x.h
```

```
tps6586x.h
```

```
/*
```

```
* (C) Copyright 2010,2011
```

```
* NVIDIA Corporation <www.nvidia.com>
```

```
*
```

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```

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*
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```

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*
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* MA 02111-1307 USA
```

```
*/
```

./include/tsec.h

tsec.h

```
/*
 * tsec.h
 *
 * Driver for the Motorola Triple Speed Ethernet Controller
 *
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 * terms of the GNU Public License, Version 2, incorporated
 * herein by reference.
 *
 * Copyright 2004, 2007, 2009, 2011 Freescale Semiconductor, Inc.
 * (C) Copyright 2003, Motorola, Inc.
 * maintained by Xianghua Xiao (x.xiao@motorola.com)
 * author Andy Fleming
 */
```

./include/tsi108.h

tsi108.h

```
/*
 * (C) Copyright 2003; Tundra Semiconductor Corp.
 * (C) Copyright 2006; Freescale Semiconductor Corp.
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 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

./include/tsi148.h

tsi148.h

```
/*
 * (C) Copyright 2009 Reinhard Arlt, reinhard.arlt@esd-electronics.com
 *
 * base on universe.h by
 *
 * (C) Copyright 2003 Stefan Roese, stefan.roese@esd-electronics.com
 *
 * See file CREDITS for list of people who contributed to this
```

```
* project.
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./include/twl4030.h
twl4030.h
/*
* Copyright (c) 2009 Wind River Systems, Inc.
* Tom Rix <Tom.Rix at windriver.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
* Derived from code on omapzoom, git://git.omapzoom.com/repo/u-boot.git
*
* Copyright (C) 2007-2009 Texas Instruments, Inc.
*/

./include/twl6030.h
twl6030.h
/*
* (C) Copyright 2010
* Texas Instruments, <www.ti.com>
*
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/twl6035.h
```

```
twl6035.h
```

```
/*
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* Texas Instruments, <www.ti.com>
*
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* project.
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/tws.h
```

```
tws.h
```

```
/*
* (C) Copyright 2009
* Detlev Zundel, DENX Software Engineering, dzu@denx.de.
*
*/
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
```

```
./include/u-boot/crc.h
```

```
crc.h
```

```
/*
```

```
* (C) Copyright 2009
* Marvell Semiconductor <www.marvell.com>
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
```

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* Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
* MA 02110-1301 USA
```

```
*/
```

```
./include/u-boot/md5.h
```

```
md5.h
```

```
/*
```

```
* This file was transplanted with slight modifications from Linux sources
```

```
* (fs/cifs/md5.h) into U-Boot by Bartlomiej Sieka <tur@semihalf.com>.
*/
```

```
./include/u-boot/u-boot.lds.h
```

```
u-boot.lds.h
```

```
/*
```

```
* Linker script helper macros
```

```
*
```

```
* Copyright (c) 2009 Analog Devices Inc.
```

```
*
```

```
* Licensed under the GPL-2 or later.
```

```
*/
```

```
/* See if the linker version is at least the specified version */
```

```
./include/u-boot/zlib.h
```

```
zlib.h
```

```
/*
```

```
* This file is derived from zlib.h and zconf.h from the zlib-1.2.3
```

```
* distribution by Jean-loup Gailly and Mark Adler, with some additions
```

```
* by Paul Mackerras to aid in implementing Deflate compression and
```

```
* decompression for PPP packets.
```

```
*/
```

```
./include/ubi_uboot.h
```

```
ubi_uboot.h
```

```
/*
```

```
* Header file for UBI support for U-Boot
```

```
*
```

```
* Adaptation from kernel to U-Boot
```

```
*
```

```
* Copyright (C) 2005-2007 Samsung Electronics
```

```
* Kyungmin Park <kyungmin.park@samsung.com>
```

```
*
```

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```

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```

```
*/
```

```
./include/universe.h
```

```
universe.h
```

```
/*
```

```
* (C) Copyright 2003 Stefan Roese, stefan.roese@esd-electronics.com
```

```
*
```

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* See file CREDITS for list of people who contributed to this
```

```
* project.
```

```
*
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* MA 02111-1307 USA
*/
```

```
./include/usb/designware_udc.h
designware_udc.h
```

```
/*
* (C) Copyright 2009
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/usb/ehci-fsl.h
ehci-fsl.h
```

```
/*
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* Copyright (c) 2008 Excito Elektronik i Sk=E5ne AB
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* MA 02111-1307 USA
*/
```

```
./include/usb/lin_gadget_compat.h
```

```
lin_gadget_compat.h
```

```
/*
 * Copyright (c) 2011 Samsung Electronics
 * Lukasz Majewski <l.majewski@samsung.com>
 *
 * This is a Linux kernel compatibility layer for USB Gadget
 *
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./include/usb/mpc8xx_udc.h
```

```
mpc8xx_udc.h
```

```
/*
 * Copyright (C) 2006 Bryan O'Donoghue, CodeHermit
 * bodonoghue@codehermit.ie
 *
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*/
```

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* 59 Temple Place - Suite 330, Boston, MA 02111-1307, USA.
*
*/
```

```
./include/usb/musb_udc.h
```

```
musb_udc.h
```

```
/*
* Copyright (c) 2009 Wind River Systems, Inc.
* Tom Rix <Tom.Rix@windriver.com>
*
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* MA 02111-1307 USA
*/
```

```
./include/usb/mv_udc.h
```

```
mv_udc.h
```

```
/*
* Copyright 2011, Marvell Semiconductor Inc.
* Lei Wen <leiwen@marvell.com>
*
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* MA 02111-1307 USA
```

```
*/  
  
./include/usb/omap1510_udc.h  
omap1510_udc.h  
/*  
* (C) Copyright 2003  
* Gerry Hamel, geh@ti.com, Texas Instruments  
*  
* Based on  
* linux/drivers/usb/device/bi/omap.h  
* Register definitions for TI OMAP1510 USB bus interface driver  
*  
* Author: MontaVista Software, Inc.  
*       source@mvista.com  
*  
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* the terms of the GNU General Public License version 2. This program  
* is licensed "as is" without any warranty of any kind, whether express  
* or implied.  
*/
```

```
./include/usb/pxa27x_udc.h  
pxa27x_udc.h  
/*  
* PXA27x register declarations and HCD data structures  
*  
* Copyright (C) 2007 Rodolfo Giometti <giometti@linux.it>  
* Copyright (C) 2007 Eurotech S.p.A. <info@eurotech.it>  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./include/usb/s3c_udc.h  
s3c_udc.h  
/*  
* drivers/usb/gadget/s3c_udc.h  
* Samsung S3C on-chip full/high speed USB device controllers
```

```
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*
*/
```

```
./include/usb/ulpi.h
```

```
ulpi.h
```

```
/*
 * Generic ULPI interface.
 *
 * Copyright (C) 2011 Jana Rapava <fermata7@gmail.com>
 * Copyright (C) 2011 CompuLab, Ltd. <www.compulab.co.il>
 *
 * Authors: Jana Rapava <fermata7@gmail.com>
 *          Igor Grinberg <grinberg@compulab.co.il>
 *
 * Register offsets taken from:
 * linux/include/linux/usb/ulpi.h
 *
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 * version 2 of that License.
 */
```

```
./include/usb.h
```

```
usb.h
```

```
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 * (C) Copyright 2001
 * Denis Peter, MPL AG Switzerland
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
* Note: Part of this code has been derived from linux
*
*/
```

```
./include/usbdescriptors.h
usbdescriptors.h
```

```
/*
* (C) Copyright 2003
* Gerry Hamel, geh@ti.com, Texas Instruments
*
* Based on
* linux/drivers/usbd/usb-function.h - USB Function
*
* Copyright (c) 2000, 2001, 2002 Lineo
* Copyright (c) 2001 Hewlett Packard
*
* By:
*   Stuart Lynne <sl@lineo.com>,
*   Tom Rushworth <tbr@lineo.com>,
*   Bruce Balden <balden@lineo.com>
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*
*/
```

./include/usbdevice.h

usbdevice.h

/*

* (C) Copyright 2003
* Gerry Hamel, geh@ti.com, Texas Instruments

*

* Based on linux/drivers/usbd/usbd.h

*

* Copyright (c) 2000, 2001, 2002 Lineo
* Copyright (c) 2001 Hewlett Packard

*

* By:

* Stuart Lynne <sl@lineo.com>,
* Tom Rushworth <tbr@lineo.com>,
* Bruce Balden <balden@lineo.com>

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*

*/

./include/usb_cdc_acm.h

usb_cdc_acm.h

/*

* (C) Copyright 2006
* Bryan O'Donoghue, deckard@codehermit.ie, CodeHermit

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*
*/
```

```
./include/usb_defs.h
```

```
usb_defs.h
```

```
/*
```

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```

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* Denis Peter, MPL AG Switzerland
```

```
*
```

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```

```
*
```

```
* Note: Part of this code has been derived from linux
```

```
*
```

```
*/
```

```
./include/usb_ether.h
```

```
usb_ether.h
```

```
/*
```

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* MA 02111-1307 USA
*/
```

```
./include/version.h
```

```
version.h
```

```
/*
 * (C) Copyright 2000-2006
 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
*/
```

```
./include/video.h
```

```
video.h
```

```
/*
** MPC823 Video Controller
** =====
** (C) 2000 by Paolo Scaffardi (arsenio@tin.it)
** AIRVENT SAM s.p.a - RIMINI(ITALY)
**
*/
/* Video functions */
```

```
./include/video_ad7176.h
```

```
video_ad7176.h
```

```
/*
 * (C) Copyright 2000
 * Paolo Scaffardi, AIRVENT SAM s.p.a - RIMINI(ITALY), arsenio@tin.it
 *
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* MA 02111-1307 USA
*/
```

```
./include/video_ad7177.h
video_ad7177.h
```

```
/*
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* Paolo Scaffardi, AIRVENT SAM s.p.a - RIMINI(ITALY), arsenio@tin.it
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* MA 02111-1307 USA
*/
```

```
./include/video_ad7179.h
video_ad7179.h
```

```
/*
* (C) Copyright 2003 Wolfgang Grandegger <wg@denx.de>
*
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*
*/
```

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./include/video_easylogo.h
```

```
video_easylogo.h
```

```
/*
** video easylogo
** =====
** (C) 2000 by Paolo Scaffardi (arsenio@tin.it)
** AIRVENT SAM s.p.a - RIMINI(ITALY)
**
** This utility is still under construction!
*/
```

```
./include/video_fb.h
```

```
video_fb.h
```

```
/*
* (C) Copyright 1997-2002 ELTEC Elektronik AG
* Frank Gottschling <fgottschling@eltec.de>
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```

```
*/  
  
./include/video_font.h  
video_font.h  
/*  
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* MA 02111-1307 USA  
*/
```

```
./include/video_font_data.h  
video_font_data.h  
/*  
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* MA 02111-1307 USA
```

```

*/

./include/video_logo.h
video_logo.h
/* */
/* Generated by EasyLogo, (C) 2000 by Paolo Scaffardi */
/* */
/* To use this, include it and call: easylogo_plot(screen,&u_boot_logo, width,x,y)
*/
/* */
/* Where:      'screen'      is the pointer to the frame buffer */
/*             'width' is the screen width */
/*             'x'         is the horizontal position */
/*             'y'         is the vertical position */
/* */

./include/virtex2.h
virtex2.h
/*
* (C) Copyright 2002
* Rich Ireland, Enterasys Networks, rireland@enterasys.com.
* Keith Outwater, keith_outwater@mvis.com
*
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*
*/

./include/vsc7385.h
vsc7385.h
/*
* Header file for vsc7385.c
*
* Author: Timur Tabi <timur@freescale.com>
*

```

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*/
```

```
./include/vsprintf.h
```

```
vsprintf.h
```

```
/*
```

```
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```

```
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
```

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*
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```

```
*/
```

```
./include/vxworks.h
```

```
vxworks.h
```

```
/*
```

```
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* Niklaus Giger, niklaus.giger@member.fsf.org
```

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*/
```

```
./include/w83c553f.h
w83c553f.h
```

```
/*
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 * MA 02111-1307 USA
*/
```

```
./include/watchdog.h
watchdog.h
```

```
/*
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 * Erik Theisen, Wave 7 Optics, etheisen@mindspring.com.
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*/
```

```
./include/xilinx.h
```

```
xilinx.h
```

```
/*
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 */
```

```
./include/xyzModem.h
```

```
xyzModem.h
```

```
/*
 *=====
 *
 *      xyzModem.h
 *
 *      RedBoot stream handler for xyzModem protocol
 *
 *=====
 *#####ECOSGPLCOPYRIGHTBEGIN#####
 * -----
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* -----
*####ECOSGPLCOPYRIGHTEND####
*=====
*#####DESCRIPTIONBEGIN#####
*
* Author(s): gthomas
* Contributors: gthomas
* Date: 2000-07-14
* Purpose:
* Description:
*
* This code is part of RedBoot (tm).
*
*#####DESCRIPTIONEND#####
*=====
*/

```

```
./include/zfs/dmu.h
```

```
dmu.h
```

```

/*
* GRUB -- GRand Unified Bootloader
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*/
/*
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*/

./include/zfs/dmu_objset.h
dmu_objset.h
/*
* GRUB -- GRand Unified Bootloader
* Copyright (C) 1999,2000,2001,2002,2003,2004 Free Software Foundation, Inc.
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*/
/*
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*/

./include/zfs/dnode.h
dnode.h
/*
* GRUB -- GRand Unified Bootloader
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*/
/*
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*/

./include/zfs/dsl_dataset.h
dsl_dataset.h
/*
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*/

./include/zfs/dsl_dir.h
dsl_dir.h
/*
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./include/zfs/sa_impl.h
sa_impl.h
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spa.h
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```

```
./include/zfs/uberblock_impl.h
uberblock_impl.h
```

```
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```

```
./include/zfs/vdev_impl.h
vdev_impl.h
```

```
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./include/zfs/zap_impl.h
zap_impl.h
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./include/zfs/zap_leaf.h
zap_leaf.h
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zfs.h
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*/

./include/zfs/zfs_acl.h
zfs_acl.h
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./include/zfs/zfs_znode.h
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zil.h
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```

```
./include/zfs/zio.h
```

```
zio.h
```

```
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*/
/*
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*/
```

```
./include/zfs/zio_checksum.h
```

```
zio_checksum.h
```

```
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```

```
./include/zfs_common.h
zfs_common.h
```

```
/*
* ZFS filesystem port for Uboot by
* Jorgen Lundman <lundman at lundman.net>
*
* zfsfs support
* made from existing GRUB Sources by Sun, GNU and others.
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*/
```

```
./include/_exports.h
_exports.h
```

```
/*
* You do not need to use #ifdef around functions that may not exist
* in the final configuration (such as i2c).
*/
```

```
./lib/addr_map.c
addr_map.c
```

```
/*
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./lib/aes.c
```

```
aes.c
```

```
/*
* Copyright (c) 2011 The Chromium OS Authors.
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*
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* project.
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* MA 02111-1307 USA
*/
```

```
./lib/asm-offsets.c
```

```
asm-offsets.c
```

```
/*
* Adapted from Linux v2.6.36 kernel: arch/powerpc/kernel/asm-offsets.c
*
* This program is used to generate definitions needed by
* assembly language modules.
*
* We use the technique used in the OSF Mach kernel code:
* generate asm statements containing #defines,
* compile this file to assembler, and then extract the
* #defines from the assembly-language output.
*
*/
```

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*/
```

```
./lib/asm-offsets.s
asm-offsets.s
No license information found.
```

```
./lib/bch.c
bch.c
```

```
/*
 * Generic binary BCH encoding/decoding library
 *
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 * Franklin St, Fifth Floor, Boston, MA 02110-1301 USA.
 *
 * Copyright © 2011 Parrot S.A.
 *
 * Author: Ivan Djelic <ivan.djelic@parrot.com>
 *
 * Description:
 *
 * This library provides runtime configurable encoding/decoding of binary
 * Bose-Chaudhuri-Hocquenghem (BCH) codes.
 *
 * Call init_bch to get a pointer to a newly allocated bch_control structure for
 * the given m (Galois field order), t (error correction capability) and
 * (optional) primitive polynomial parameters.
 *
 * Call encode_bch to compute and store ecc parity bytes to a given buffer.
 * Call decode_bch to detect and locate errors in received data.
 *
 * On systems supporting hw BCH features, intermediate results may be provided
 * to decode_bch in order to skip certain steps. See decode_bch() documentation
 * for details.
 *
 * Option CONFIG_BCH_CONST_PARAMS can be used to force fixed values of
 * parameters m and t; thus allowing extra compiler optimizations and providing
```

```

* better (up to 2x) encoding performance. Using this option makes sense when
* (m,t) are fixed and known in advance, e.g. when using BCH error correction
* on a particular NAND flash device.
*
* Algorithmic details:
*
* Encoding is performed by processing 32 input bits in parallel, using 4
* remainder lookup tables.
*
* The final stage of decoding involves the following internal steps:
* a. Syndrome computation
* b. Error locator polynomial computation using Berlekamp-Massey algorithm
* c. Error locator root finding (by far the most expensive step)
*
* In this implementation, step c is not performed using the usual Chien search.
* Instead, an alternative approach described in [1] is used. It consists in
* factoring the error locator polynomial using the Berlekamp Trace algorithm
* (BTA) down to a certain degree (4), after which ad hoc low-degree polynomial
* solving techniques [2] are used. The resulting algorithm, called BTZ, yields
* much better performance than Chien search for usual (m,t) values (typically
* m >= 13, t < 32, see [1]).
*
* [1] B. Biswas, V. Herbert. Efficient root finding of polynomials over fields
* of characteristic 2, in: Western European Workshop on Research in Cryptology
* - WEWoRC 2009, Graz, Austria, LNCS, Springer, July 2009, to appear.
* [2] [Zin96] V.A. Zinoviev. On the solution of equations of degree 10 over
* finite fields GF(2^q). In Rapport de recherche INRIA no 2829, 1996.
*/

```

```
./lib/bzlib.c
```

```
bzlib.c
```

```

/*
 * This file is a modified version of bzlib.c from the bzip2-1.0.2
 * distribution which can be found at http://sources.redhat.com/bzip2/
 */
/*-----*/
/*--- Library top-level functions. ---*/
/*--- bzlib.c ---*/
/*-----*/

```

```
./lib/bzlib_crctable.c
```

```
bzlib_crctable.c
```

```

/*-----*/
/*--- Table for doing CRCs ---*/
/*--- crctable.c ---*/
/*-----*/
/*--

```

```
./lib/bzlib_decompress.c
```

```
bzlib_decompress.c
```

```

/*-----*/
/*--- Decompression machinery ---*/
/*--- decompress.c ---*/
/*-----*/
/*--

./lib/bzlib_huffman.c
bzlib_huffman.c
/*-----*/
/*--- Huffman coding low-level stuff ---*/
/*--- huffman.c ---*/
/*-----*/
/*--

./lib/bzlib_private.h
bzlib_private.h
/*
 * This file is a modified version of bzlib_private.h from the bzip2-1.0.2
 * distribution which can be found at http://sources.redhat.com/bzip2/
 */
/*-----*/
/*--- Private header file for the library. ---*/
/*--- bzlib_private.h ---*/
/*-----*/
/*--

./lib/bzlib_randtable.c
bzlib_randtable.c
/*-----*/
/*--- Table for randomising repetitive blocks ---*/
/*--- randtable.c ---*/
/*-----*/
/*--

./lib/circbuf.c
circbuf.c
/*
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 * Gerry Hamel, geh@ti.com, Texas Instruments
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*
*/
```

```
./lib/crc16.c
```

```
crc16.c
```

```
/*
```

```
*=====
```

```
*
```

```
*     crc16.c
```

```
*
```

```
*     16 bit CRC with polynomial  $x^{16}+x^{12}+x^5+1$ 
```

```
*
```

```
*=====
```

```
*#####ECOSGPLCOPYRIGHTBEGIN#####
```

```
* -----
```

```
* This file is part of eCos, the Embedded Configurable Operating System.
```

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* Copyright (C) 1998, 1999, 2000, 2001, 2002 Red Hat, Inc.
```

```
* Copyright (C) 2002 Gary Thomas
```

```
*
```

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```

```
*#####ECOSGPLCOPYRIGHTEND#####
```

```
*=====
```

```
#####DESCRIPTIONBEGIN#####
*
* Author(s):    gthomas
* Contributors: gthomas,asl
* Date:        2001-01-31
* Purpose:
* Description:
*
* This code is part of eCos (tm).
*
#####DESCRIPTIONEND#####
*
*=====
*/
```

```
./lib/crc32.c
crc32.c
/*
* This file is derived from crc32.c from the zlib-1.1.3 distribution
* by Jean-loup Gailly and Mark Adler.
*/
/* crc32.c -- compute the CRC-32 of a data stream
* Copyright (C) 1995-1998 Mark Adler
* For conditions of distribution and use, see copyright notice in zlib.h
*/
```

```
./lib/crc7.c
crc7.c
/*
*      crc7.c
*
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* Version 2. See the file COPYING for more details.
*/
/* Table for CRC-7 (polynomial  $x^7 + x^3 + 1$ ) */
```

```
./lib/ctype.c
ctype.c
/*
* (C) Copyright 2000
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
*
* See file CREDITS for list of people who contributed to this
* project.
*
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* MA 02111-1307 USA  
*/
```

```
./lib/display_options.c
```

```
display_options.c
```

```
/*  
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*  
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* MA 02111-1307 USA  
*/
```

```
./lib/div64.c
```

```
div64.c
```

```
/*  
* Copyright (C) 2003 Bernardo Innocenti <bernie@develer.com>  
*  
* Based on former do_div() implementation from asm-parisc/div64.h:  
* Copyright (C) 1999 Hewlett-Packard Co  
* Copyright (C) 1999 David Mosberger-Tang <davidm@hpl.hp.com>  
*  
*  
* Generic C version of 64bit/32bit division and modulo, with  
* 64bit result and 32bit remainder.  
*  
* The fast case for (n>>32 == 0) is handled inline by do_div().
```

```
*
* Code generated for this function might be very inefficient
* for some CPUs. __div64_32() can be overridden by linking arch-specific
* assembly versions such as arch/powerpc/lib/div64.S and arch/sh/lib/div64.S.
*/
```

```
./lib/errno.c
```

```
errno.c
```

```
No license information found.
```

```
./lib/fdtdec.c
```

```
fdtdec.c
```

```
/*
```

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```

```
*/
```

```
./lib/fdtdec_test.c
```

```
fdtdec_test.c
```

```
/*
```

```
* Some very basic tests for fdtdec, accessed through test_fdtdec command.
* They are easiest to use with sandbox.
```

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* MA 02111-1307 USA
*/
```

```
./lib/gunzip.c
```

```
gunzip.c
```

```
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* MA 02111-1307 USA
*/
```

```
./lib/gzip.c
```

```
gzip.c
```

```
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*/
```

```
./lib/hashtable.c
hashtable.c
```

```
/*
* This implementation is based on code from uClibc-0.9.30.3 but was
* modified and extended for use within U-Boot.
*
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* 02111-1307 USA.
*/
```

```
./lib/ldiv.c
ldiv.c
```

```
/* Copyright (C) 1992, 1997 Free Software Foundation, Inc.
```

```
./lib/libfdt/fdt.c
fdt.c
```

```
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* libfdt - Flat Device Tree manipulation
* Copyright (C) 2006 David Gibson, IBM Corporation.
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*/

./lib/libfdt/fdt_empty_tree.c
fdt_empty_tree.c
/*
* libfdt - Flat Device Tree manipulation
```

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*/

./lib/libfdt/fdt_ro.c

fdt_ro.c

```
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```

```
*/
./lib/libfdt/fdt_rw.c
fdt_rw.c
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*/
```

```
./lib/libfddt/fddt_sw.c
```

```
fddt_sw.c
```

```
/*
```

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*/
```

```
./lib/libfdt/fdt_wip.c
```

```
fdt_wip.c
```

```
/*
```

```
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```

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*/
```

```
./lib/libfdt/libfdt_internal.h  
libfdt_internal.h
```

```
/*
```

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*/
```

```
./lib/linux_string.c
linux_string.c
/*
 *  linux/lib/string.c
 *
 *  Copyright (C) 1991, 1992  Linus Torvalds
 */
```

```
./lib/lmb.c
lmb.c
/*
 *  Procedures for maintaining information about logical memory blocks.
 *
 *  Peter Bergner, IBM Corp.      June 2001.
 *  Copyright (C) 2001 Peter Bergner.
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 */
```

```
./lib/lzma/LzmaDec.c
LzmaDec.c
/* LzmaDec.c -- LZMA Decoder
```

```
./lib/lzma/LzmaDec.h
LzmaDec.h
/* LzmaDec.h -- LZMA Decoder
/* #define _LZMA_PROB32 */
/* _LZMA_PROB32 can increase the speed on some CPUs,
```

```
./lib/lzma/LzmaTools.c
```

LzmaTools.c

```
/*
 * Useful routines based on the LzmaTest.c file from LZMA SDK 4.65
 *
 * Copyright (C) 2007-2009 Industrie Dial Face S.p.A.
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```

./lib/lzma/LzmaTools.h

LzmaTools.h

```
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 *
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*/

./lib/lzma/Types.h
Types.h
/* Types.h -- Basic types

./lib/lzo/lzo1x_decompress.c
lzo1x_decompress.c
/*
* LZ01X Decompressor from MiniLZO
*
* Copyright (C) 1996-2005 Markus F.X.J. Oberhumer <markus@oberhumer.com>
*
* The full LZ0 package can be found at:
* http://www.oberhumer.com/opensource/lzo/
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* Nitin Gupta <nitingupta910@gmail.com>
* Richard Purdie <rpurdie@openedhand.com>
*/

./lib/lzo/lzodefs.h
lzodefs.h
/*
* lzodefs.h -- architecture, OS and compiler specific defines
*
* Copyright (C) 1996-2005 Markus F.X.J. Oberhumer <markus@oberhumer.com>
*
* The full LZ0 package can be found at:
* http://www.oberhumer.com/opensource/lzo/
*
* Changed for kernel use by:
* Nitin Gupta <nitingupta910@gmail.com>
* Richard Purdie <rpurdie@openedhand.com>
*/

./lib/md5.c
md5.c
/*
* This file was transplanted with slight modifications from Linux sources
* (fs/cifs/md5.c) into U-Boot by Bartlomiej Sieka <tur@semihalf.com>.
*/
/*
* This code implements the MD5 message-digest algorithm.
* The algorithm is due to Ron Rivest. This code was
```

```
* written by Colin Plumb in 1993, no copyright is claimed.
* This code is in the public domain; do with it what you wish.
*
* Equivalent code is available from RSA Data Security, Inc.
* This code has been tested against that, and is equivalent,
* except that you don't need to include two pages of legalese
* with every copy.
*
* To compute the message digest of a chunk of bytes, declare an
* MD5Context structure, pass it to MD5Init, call MD5Update as
* needed on buffers full of bytes, and then call MD5Final, which
* will fill a supplied 16-byte array with the digest.
*/
```

```
./lib/net_utils.c
```

```
net_utils.c
```

```
/*
 * Generic network code. Moved from net.c
 *
 * Copyright 1994 - 2000 Neil Russell.
 * Copyright 2000 Roland Borde
 * Copyright 2000 Paolo Scaffardi
 * Copyright 2000-2002 Wolfgang Denk, wd@denx.de
 * Copyright 2009 Dirk Behme, dirk.behme@googlemail.com
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
 * This program is free software; you can redistribute it and/or
 * modify it under the terms of the GNU General Public License as
 * published by the Free Software Foundation; either version 2 of
 * the License, or (at your option) any later version.
 *
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 * but WITHOUT ANY WARRANTY; without even the implied warranty of
 * MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
 * GNU General Public License for more details.
 *
 * You should have received a copy of the GNU General Public License
 * along with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./lib/phymem.c
```

```
phymem.c
```

```
/*
 * Copyright (c) 2012 The Chromium OS Authors. All rights reserved.
 * Use of this source code is governed by a BSD-style license that can be
 * found in the LICENSE file.
```

```
*
* Alternatively, this software may be distributed under the terms of the
* GNU General Public License ("GPL") version 2 as published by the Free
* Software Foundation.
*/
```

```
./lib/qsort.c
```

```
qsort.c
```

```
/*
* Code adapted from uClibc-0.9.30.3
*
* It is therefore covered by the GNU LESSER GENERAL PUBLIC LICENSE
* Version 2.1, February 1999
*
* Wolfgang Denk <wd@denx.de>
*/
/* This code is derived from a public domain shell sort routine by
* Ray Gardner and found in Bob Stout's snippets collection. The
* original code is included below in an #if 0/#endif block.
*
* I modified it to avoid the possibility of overflow in the wgap
* calculation, as well as to reduce the generated code size with
* bcc and gcc. */
```

```
./lib/rand.c
```

```
rand.c
```

```
/*
* Simple xorshift PRNG
* see http://www.jstatsoft.org/v08/i14/paper
*
* Copyright (c) 2012 Michael Walle
* Michael Walle <michael@walle.cc>
*
* See file CREDITS for list of people who contributed to this
* project.
*
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* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2 of
* the License, or (at your option) any later version.
*
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* but WITHOUT ANY WARRANTY; without even the implied warranty of
* MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
```

```
*/

./lib/rbtree.c
rbtree.c
/*

./lib/sha1.c
sha1.c
/*
 * Heiko Schocher, DENX Software Engineering, hs@denx.de.
 * based on:
 * FIPS-180-1 compliant SHA-1 implementation
 *
 * Copyright (C) 2003-2006 Christophe Devine
 *
 * This library is free software; you can redistribute it and/or
 * modify it under the terms of the GNU Lesser General Public
 * License, version 2.1 as published by the Free Software Foundation.
 *
 * This library is distributed in the hope that it will be useful,
 * but WITHOUT ANY WARRANTY; without even the implied warranty of
 * MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the GNU
 * Lesser General Public License for more details.
 *
 * You should have received a copy of the GNU Lesser General Public
 * License along with this library; if not, write to the Free Software
 * Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston,
 * MA 02110-1301 USA
 */
/*
 * The SHA-1 standard was published by NIST in 1993.
 *
 * http://www.itl.nist.gov/fipspubs/fip180-1.htm
 */

./lib/sha256.c
sha256.c
/*
 * FIPS-180-2 compliant SHA-256 implementation
 *
 * Copyright (C) 2001-2003 Christophe Devine
 *
 * This program is free software; you can redistribute it and/or modify
 * it under the terms of the GNU General Public License as published by
 * the Free Software Foundation; either version 2 of the License, or
 * (at your option) any later version.
 *
 * This program is distributed in the hope that it will be useful,
 * but WITHOUT ANY WARRANTY; without even the implied warranty of
 * MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
```

```
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*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
*/
```

```
./lib/string.c
```

```
string.c
```

```
/*
```

```
* linux/lib/string.c
```

```
*
```

```
* Copyright (C) 1991, 1992 Linus Torvalds
```

```
*/
```

```
/*
```

```
* stupid library routines.. The optimized versions should generally be found
* as inline code in <asm-xx/string.h>
```

```
*
```

```
* These are buggy as well..
```

```
*
```

```
* * Fri Jun 25 1999, Ingo Oeser <ioe@informatik.tu-chemnitz.de>
```

```
* - Added strsep() which will replace strtok() soon (because strsep() is
```

```
* reentrant and should be faster). Use only strsep() in new code, please.
```

```
*/
```

```
./lib/strmh.c
```

```
strmh.c
```

```
/*
```

```
* (C) Copyright 2002-2006
```

```
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
```

```
*
```

```
* See file CREDITS for list of people who contributed to this
* project.
```

```
*
```

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* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2 of
* the License, or (at your option) any later version.
```

```
*
```

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```

```
*
```

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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
```

```
*/
```

./lib/time.c

time.c

/*

* (C) Copyright 2000-2009

* Wolfgang Denk, DENX Software Engineering, wd@denx.de.

*

* See file CREDITS for list of people who contributed to this
* project.

*

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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA

*/

./lib/tizen/tizen.c

tizen.c

/*

* (C) Copyright 2012 Samsung Electronics

* Donghwa Lee <dh09.lee@samsung.com>

*

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* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2 of
* the License, or (at your option) any later version.

*

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*

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* aint with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA

*

*/

./lib/tizen/tizen_hd_logo.h

tizen_hd_logo.h

```
/*
 * (C) Copyright 2012 Samsung Electronics
 * Donghwa Lee <dh09.lee@samsung.com>
 *
 * This program is free software; you can redistribute it and/or
 * modify it under the terms of the GNU General Public License as
 * published by the Free Software Foundation; either version 2 of
 * the License, or (at your option) any later version.
 *
 * This program is distributed in the hope that it will be useful,
 * but WITHOUT ANY WARRANTY; without even the implied warranty of
 * MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
 * GNU General Public License for more details.
 *
 * You should have received a copy of the GNU General Public License
 * aint with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 *
 */
```

```
./lib/tizen/tizen_hd_logo_data.h
tizen_hd_logo_data.h
```

```
/*
 * (C) Copyright 2012 Samsung Electronics
 * Donghwa Lee <dh09.lee@samsung.com>
 *
 * This program is free software; you can redistribute it and/or
 * modify it under the terms of the GNU General Public License as
 * published by the Free Software Foundation; either version 2 of
 * the License, or (at your option) any later version.
 *
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 * but WITHOUT ANY WARRANTY; without even the implied warranty of
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 * GNU General Public License for more details.
 *
 * You should have received a copy of the GNU General Public License
 * aint with this program; if not, write to the Free Software
 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 *
 */
```

```
./lib/uuid.c
uuid.c
```

```
/*
 * Copyright 2011 Calxeda, Inc.
 *
 * See file CREDITS for list of people who contributed to this
```

```

* project.
*
* This program is free software; you can redistribute it and/or
* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2 of
* the License, or (at your option) any later version.
*
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* but WITHOUT ANY WARRANTY; without even the implied warranty of
* MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
* GNU General Public License for more details.
*
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/

./lib/vsprintf.c
vsprintf.c
/*
 * linux/lib/vsprintf.c
 *
 * Copyright (C) 1991, 1992 Linus Torvalds
 */
/* vsprintf.c -- Lars Wirzenius & Linus Torvalds. */
/*
 * Wirzenius wrote this portably, Torvalds fucked it up :-)
 *
 * from hush: simple_itoa() was lifted from boa-0.93.15
 */

./lib/zlib/adler32.c
adler32.c
/* adler32.c -- compute the Adler-32 checksum of a data stream
 * Copyright (C) 1995-2004 Mark Adler
 * For conditions of distribution and use, see copyright notice in zlib.h
 */
/* @(#) $Id$ */
/* NMAX is the largest n such that 255n(n+1)/2 + (n+1)(BASE-1) <= 2^32-1 */

./lib/zlib/deflate.c
deflate.c
/* deflate.c -- compress data using the deflation algorithm
 * Copyright (C) 1995-2010 Jean-loup Gailly and Mark Adler
 * For conditions of distribution and use, see copyright notice in zlib.h
 */
/*
 * ALGORITHM
 *

```

```
* The "deflation" process depends on being able to identify portions
* of the input text which are identical to earlier input (within a
* sliding window trailing behind the input currently being processed).
*
* The most straightforward technique turns out to be the fastest for
* most input files: try all possible matches and select the longest.
* The key feature of this algorithm is that insertions into the string
* dictionary are very simple and thus fast, and deletions are avoided
* completely. Insertions are performed at each input character, whereas
* string matches are performed only when the previous match ends. So it
* is preferable to spend more time in matches to allow very fast string
* insertions and avoid deletions. The matching algorithm for small
* strings is inspired from that of Rabin & Karp. A brute force approach
* is used to find longer strings when a small match has been found.
* A similar algorithm is used in comic (by Jan-Mark Wams) and freeze
* (by Leonid Broukhis).
* A previous version of this file used a more sophisticated algorithm
* (by Fiala and Greene) which is guaranteed to run in linear amortized
* time, but has a larger average cost, uses more memory and is patented.
* However the F&G algorithm may be faster for some highly redundant
* files if the parameter max_chain_length (described below) is too large.
```

```
* ACKNOWLEDGEMENTS
```

```
* The idea of lazy evaluation of matches is due to Jan-Mark Wams, and
* I found it in 'freeze' written by Leonid Broukhis.
* Thanks to many people for bug reports and testing.
```

```
* REFERENCES
```

```
* Deutsch, L.P., "DEFLATE Compressed Data Format Specification".
* Available in http://www.ietf.org/rfc/rfc1951.txt
```

```
* A description of the Rabin and Karp algorithm is given in the book
* "Algorithms" by R. Sedgewick, Addison-Wesley, p252.
```

```
* Fiala, E.R., and Greene, D.H.
* Data Compression with Finite Windows, Comm.ACM, 32,4 (1989) 490-595
```

```
*/
```

```
./lib/zlib/deflate.h
```

```
deflate.h
```

```
/* deflate.h -- internal compression state
```

```
 * Copyright (C) 1995-2010 Jean-loup Gailly
```

```
 * For conditions of distribution and use, see copyright notice in zlib.h
```

```
*/
```

```
/* WARNING: this file should *not* be used by applications. It is
```

```
*/
```

```
/* @(#) $Id$ */
```

```
./lib/zlib/inffast.c
inffast.c
/* inffast.c -- fast decoding
 * Copyright (C) 1995-2004 Mark Adler
 * For conditions of distribution and use, see copyright notice in zlib.h
 */
/* U-boot: we already included these
 */
```

```
./lib/zlib/inffast.h
inffast.h
/* inffast.h -- header to use inffast.c
 * Copyright (C) 1995-2003 Mark Adler
 * For conditions of distribution and use, see copyright notice in zlib.h
 */
/* WARNING: this file should *not* be used by applications. It is
 */
```

```
./lib/zlib/inffixed.h
inffixed.h
No license information found.
```

```
./lib/zlib/inflate.c
inflate.c
/* inflate.c -- zlib decompression
 * Copyright (C) 1995-2005 Mark Adler
 * For conditions of distribution and use, see copyright notice in zlib.h
 */
```

```
./lib/zlib/inflate.h
inflate.h
/* inflate.h -- internal inflate state definition
 * Copyright (C) 1995-2004 Mark Adler
 * For conditions of distribution and use, see copyright notice in zlib.h
 */
/* WARNING: this file should *not* be used by applications. It is
 */
/* define NO_GZIP when compiling if you want to disable gzip header and
```

```
./lib/zlib/inftrees.c
inftrees.c
/* inftrees.c -- generate Huffman trees for efficient decoding
 * Copyright (C) 1995-2005 Mark Adler
 * For conditions of distribution and use, see copyright notice in zlib.h
 */
/* U-boot: we already included these
 */
/*
```

```
./lib/zlib/inftrees.h
inftrees.h
/* inftrees.h -- header to use inftrees.c
 * Copyright (C) 1995-2005 Mark Adler
 * For conditions of distribution and use, see copyright notice in zlib.h
 */
/* WARNING: this file should *not* be used by applications. It is
 */
/* Structure for decoding tables. Each entry provides either the
```

```
./lib/zlib/trees.c
trees.c
/* trees.c -- output deflated data using Huffman coding
 * Copyright (C) 1995-2010 Jean-loup Gailly
 * detect_data_type() function provided freely by Cosmin Truta, 2006
 * For conditions of distribution and use, see copyright notice in zlib.h
 */
/*
 * ALGORITHM
 *
 * The "deflation" process uses several Huffman trees. The more
 * common source values are represented by shorter bit sequences.
 *
 * Each code tree is stored in a compressed form which is itself
 * a Huffman encoding of the lengths of all the code strings (in
 * ascending order by source values). The actual code strings are
 * reconstructed from the lengths in the inflate process, as described
 * in the deflate specification.
 *
 * REFERENCES
 *
 * Deutsch, L.P., "Deflate" Compressed Data Format Specification".
 * Available in ftp.uu.net:/pub/archiving/zip/doc/deflate-1.1.doc
 *
 * Storer, James A.
 * Data Compression: Methods and Theory, pp. 49-50.
 * Computer Science Press, 1988. ISBN 0-7167-8156-5.
 *
 * Sedgewick, R.
 * Algorithms, p290.
 * Addison-Wesley, 1983. ISBN 0-201-06672-6.
 */
```

```
./lib/zlib/trees.h
trees.h
/* header created automatically with -DGEN_TREES_H */
```

```
./lib/zlib/zlib.c
zlib.c
/*
```

```

* This file is derived from various .h and .c files from the zlib-1.2.3
* distribution by Jean-loup Gailly and Mark Adler, with some additions
* by Paul Mackerras to aid in implementing Deflate compression and
* decompression for PPP packets. See zlib.h for conditions of
* distribution and use.
*
* Changes that have been made include:
* - changed functions not used outside this file to "local"
* - added minCompression parameter to deflateInit2
* - added Z_PACKET_FLUSH (see zlib.h for details)
* - added inflateIncomp
*/

./lib/zlib/zlib.h
zlib.h
/* Glue between u-boot and upstream zlib */
/* avoid conflicts */

./lib/zlib/zutil.c
zutil.c
/* zutil.c -- target dependent utility functions for the compression library
 * Copyright (C) 1995-2005 Jean-loup Gailly.
 * For conditions of distribution and use, see copyright notice in zlib.h
 */
/* @(#) $Id$ */

./lib/zlib/zutil.h
zutil.h
/* zutil.h -- internal interface and configuration of the compression library
 * Copyright (C) 1995-2005 Jean-loup Gailly.
 * For conditions of distribution and use, see copyright notice in zlib.h
 */
/* WARNING: this file should *not* be used by applications. It is
 */
/* @(#) $Id$ */

./nand_spl/board/amcc/bamboo/sdram.c
sdram.c
/*
 * (C) Copyright 2007
 * Stefan Roese, DENX Software Engineering, sr@denx.de.
 *
 * See file CREDITS for list of people who contributed to this
 * project.
 *
 * This program is free software; you can redistribute it and/or
 * modify it under the terms of the GNU General Public License as
 * published by the Free Software Foundation; either version 2 of
 * the License, or (at your option) any later version.
 */

```

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* MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the  
* GNU General Public License for more details.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./nand_spl/board/amcc/canyonlands/ddr2_fixed.c  
ddr2_fixed.c
```

```
/*  
* (C) Copyright 2008-2009  
* Stefan Roesse, DENX Software Engineering, sr@denx.de.  
*  
* See file CREDITS for list of people who contributed to this  
* project.  
*  
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* the License, or (at your option) any later version.  
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* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./nand_spl/board/freescale/mpc8536ds/nand_boot.c  
nand_boot.c
```

```
/*  
* Copyright 2009 Freescale Semiconductor, Inc.  
*  
* This program is free software; you can redistribute it and/or  
* modify it under the terms of the GNU General Public License as  
* published by the Free Software Foundation; either version 2 of  
* the License, or (at your option) any later version.  
*  
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* MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the  
*  
*/
```

```
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
```

```
./nand_spl/board/freescale/mpc8569mds/nand_boot.c
nand_boot.c
```

```
/*
* Copyright 2009 Freescale Semiconductor, Inc.
*
* This program is free software; you can redistribute it and/or
* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2 of
* the License, or (at your option) any later version.
*
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*
* You should have received a copy of the GNU General Public License
* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*
*/
```

```
./nand_spl/board/freescale/mpc8572ds/nand_boot.c
nand_boot.c
```

```
/*
* Copyright 2009-2010 Freescale Semiconductor, Inc.
*
* This program is free software; you can redistribute it and/or
* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2 of
* the License, or (at your option) any later version.
*
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* MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
*
* GNU General Public License for more details.
*
* You should have received a copy of the GNU General Public License
* along with this program; if not, write to the Free Software
```

```
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*  
*/
```

```
./nand_spl/board/freescale/p1010rdb/nand_boot.c  
nand_boot.c
```

```
/*  
* Copyright 2011 Freescale Semiconductor, Inc.  
*  
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* modify it under the terms of the GNU General Public License as  
* published by the Free Software Foundation; either version 2 of  
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*  
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* GNU General Public License for more details.  
*  
* You should have received a copy of the GNU General Public License  
* along with this program; if not, write to the Free Software  
* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*  
*/
```

```
./nand_spl/board/freescale/p1023rds/nand_boot.c  
nand_boot.c
```

```
/*  
* Copyright 2010-2011 Freescale Semiconductor, Inc.  
* Author: Roy Zang <tie-fei.zang@freescale.com>  
*  
* This program is free software; you can redistribute it and/or  
* modify it under the terms of the GNU General Public License as  
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* the License, or (at your option) any later version.  
*  
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* MA 02111-1307 USA  
*  
*/
```

```
*/  
  
./nand_spl/board/freescale/p1_p2_rdb/nand_boot.c  
nand_boot.c  
/*  
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* MA 02111-1307 USA  
*  
*/
```

```
./nand_spl/nand_boot.c  
nand_boot.c  
/*  
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* Stefan Roesse, DENX Software Engineering, sr@denx.de.  
*  
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./nand_spl/nand_boot_fsl_elbc.c  
nand_boot_fsl_elbc.c  
/*
```

```
* NAND boot for Freescale Enhanced Local Bus Controller, Flash Control Machine
*
* (C) Copyright 2006-2008
* Stefan Roesse, DENX Software Engineering, sr@denx.de.
*
* Copyright (c) 2008 Freescale Semiconductor, Inc.
* Author: Scott Wood <scottwood@freescale.com>
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./nand_spl/nand_boot_fsl_ifc.c
nand_boot_fsl_ifc.c
/*
```

```
* NAND boot for FSL Integrated Flash Controller, NAND Flash Control Machine
*
* Copyright 2011 Freescale Semiconductor, Inc.
* Author: Dipen Dudhat <dipen.dudhat@freescale.com>
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* MA 02111-1307 USA
*/
```

```
./nand_spl/nand_boot_fsl_nfc.c
nand_boot_fsl_nfc.c
```

```
/*
 * (C) Copyright 2009
 * Magnus Lilja <lilja.magnus@gmail.com>
 *
 * (C) Copyright 2008
 * Maxim Artamonov, <scn1874 at yandex.ru>
 *
 * (C) Copyright 2006-2008
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 * Foundation, Inc., 59 Temple Place, Suite 330, Boston,
 * MA 02111-1307 USA
 */
```

```
./net/arp.c
```

```
arp.c
```

```
/*
 * Copied from Linux Monitor (LiMon) - Networking.
 *
 * Copyright 1994 - 2000 Neil Russell.
 * (See License)
 * Copyright 2000 Roland Borde
 * Copyright 2000 Paolo Scaffardi
 * Copyright 2000-2002 Wolfgang Denk, wd@denx.de
 */
```

```
./net/arp.h
```

```
arp.h
```

```
/*
 * Copied from Linux Monitor (LiMon) - Networking.
 *
 * Copyright 1994 - 2000 Neil Russell.
 * (See License)
 * Copyright 2000 Roland Borde
 * Copyright 2000 Paolo Scaffardi
 * Copyright 2000-2002 Wolfgang Denk, wd@denx.de
 */
```

./net/bootp.c

bootp.c

```
/*
 *      Based on LiMon - BOOTP.
 *
 *      Copyright 1994, 1995, 2000 Neil Russell.
 *      (See License)
 *      Copyright 2000 Roland Borde
 *      Copyright 2000 Paolo Scaffardi
 *      Copyright 2000-2004 Wolfgang Denk, wd@denx.de
 */
```

./net/bootp.h

bootp.h

```
/*
 *      Copied from LiMon - BOOTP.
 *
 *      Copyright 1994, 1995, 2000 Neil Russell.
 *      (See License)
 *      Copyright 2000 Paolo Scaffardi
 */
```

./net/cdp.c

cdp.c

```
/*
 *      Copied from Linux Monitor (LiMon) - Networking.
 *
 *      Copyright 1994 - 2000 Neil Russell.
 *      (See License)
 *      Copyright 2000 Roland Borde
 *      Copyright 2000 Paolo Scaffardi
 *      Copyright 2000-2002 Wolfgang Denk, wd@denx.de
 */
```

./net/cdp.h

cdp.h

```
/*
 *      Copied from Linux Monitor (LiMon) - Networking.
 *
 *      Copyright 1994 - 2000 Neil Russell.
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 *      Copyright 2000 Roland Borde
 *      Copyright 2000 Paolo Scaffardi
 *      Copyright 2000-2002 Wolfgang Denk, wd@denx.de
 */
```

./net/dns.c

dns.c

```
/*
 * DNS support driver
```

```
*
* Copyright (c) 2008 Pieter Voorthuijsen <pieter.voorthuijsen@prodrive.nl>
* Copyright (c) 2009 Robin Getz <rgetz@blackfin.uclinux.org>
*
* This is a simple DNS implementation for U-Boot. It will use the first IP
* in the DNS response as NetServerIP. This can then be used for any other
* network related activities.
*
* The packet handling is partly based on TADNS, original copyrights
* follow below.
*
*/
```

```
./net/dns.h
```

```
dns.h
```

```
/*
* (C) Masami Komiya <mkomiya@sonare.it> 2005
* Copyright 2009, Robin Getz <rgetz@blackfin.uclinux.org>
*
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* modify it under the terms of the GNU General Public License as
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* your option) any later version.
*/
```

```
./net/eth.c
```

```
eth.c
```

```
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*/
```

./net/link_local.c

link_local.c

```
/*
 * RFC3927 ZeroConf IPv4 Link-Local addressing
 * (see <http://www.zeroconf.org/>)
 *
 * Copied from BusyBox - networking/zcip.c
 *
 * Copyright (C) 2003 by Arthur van Hoff (avh@strangeberry.com)
 * Copyright (C) 2004 by David Brownell
 * Copyright (C) 2010 by Joe Hershberger
 *
 * Licensed under the GPL v2 or later
 */
```

./net/link_local.h

link_local.h

```
/*
 * RFC3927 ZeroConf IPv4 Link-Local addressing
 * (see <http://www.zeroconf.org/>)
 *
 * Copied from BusyBox - networking/zcip.c
 *
 * Copyright (C) 2003 by Arthur van Hoff (avh@strangeberry.com)
 * Copyright (C) 2004 by David Brownell
 *
 * Licensed under the GPL v2 or later
 */
```

./net/net.c

net.c

```
/*
 * Copied from Linux Monitor (LiMon) - Networking.
 *
 * Copyright 1994 - 2000 Neil Russell.
 * (See License)
 * Copyright 2000 Roland Borde
 * Copyright 2000 Paolo Scaffardi
 * Copyright 2000-2002 Wolfgang Denk, wd@denx.de
 */
/*
 * General Description:
 *
 * The user interface supports commands for BOOTP, RARP, and TFTP.
 * Also, we support ARP internally. Depending on available data,
 * these interact as follows:
 *
 * BOOTP:
 *
 * Prerequisites: - own ethernet address
```



```
*      We want:          - load the boot file
*      Next step:        none
*
* SNTP:
*
*      Prerequisites:    - own ethernet address
*                        - own IP address
*      We want:          - network time
*      Next step:        none
*/
```

./net/net_rand.h

net_rand.h

```
/*
*      Copied from LiMon - BOOTP.
*
*      Copyright 1994, 1995, 2000 Neil Russell.
*      (See License)
*      Copyright 2000 Paolo Scaffardi
*/
```

./net/nfs.c

nfs.c

```
/*
* NFS support driver - based on etherboot and U-BOOT's tftp.c
*
* Masami Komiya <mkomiya@sonare.it> 2004
*
*/
/* NOTE: the NFS code is heavily inspired by the NetBSD netboot code (read:
* large portions are copied verbatim) as distributed in OSKit 0.97. A few
* changes were necessary to adapt the code to Etherboot and to fix several
* inconsistencies. Also the RPC message preparation is done "by hand" to
* avoid adding netsprintf() which I find hard to understand and use. */
```

./net/nfs.h

nfs.h

```
/*
* (C) Masami Komiya <mkomiya@sonare.it> 2004
*
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* modify it under the terms of the GNU General Public License as
* published by the Free Software Foundation; either version 2, or (at
* your option) any later version.
*/
```

./net/ping.c

ping.c

```
/*
*      Copied from Linux Monitor (LiMon) - Networking.
```

```
*
* Copyright 1994 - 2000 Neil Russell.
* (See License)
* Copyright 2000 Roland Borde
* Copyright 2000 Paolo Scaffardi
* Copyright 2000-2002 Wolfgang Denk, wd@denx.de
*/
```

```
./net/ping.h
```

```
ping.h
```

```
/*
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* Copyright 2000 Roland Borde
* Copyright 2000 Paolo Scaffardi
* Copyright 2000-2002 Wolfgang Denk, wd@denx.de
*/
```

```
./net/rarp.c
```

```
rarp.c
```

```
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*/
```

```
./net/rarp.h
```

```
rarp.h
```

```
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*/

./net/sntp.c
sntp.c
/*
* SNTP support driver
*
* Masami Komiya <mkomiya@sonare.it> 2005
*
*/

./net/sntp.h
sntp.h
/*
* (C) Masami Komiya <mkomiya@sonare.it> 2005
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* your option) any later version.
*/

./net/tftp.c
tftp.c
/*
* Copyright 1994, 1995, 2000 Neil Russell.
* (See License)
* Copyright 2000, 2001 DENX Software Engineering, Wolfgang Denk, wd@denx.de
* Copyright 2011 Comelit Group SpA,
* Luca Ceresoli <luca.ceresoli@comelit.it>
*/
```

./net/tftp.h

tftp.h

```
/*
 *      LiMon - BOOTP/TFTP.
 *
 *      Copyright 1994, 1995, 2000 Neil Russell.
 *      Copyright 2011 Comelit Group SpA
 *              Luca Ceresoli <luca.ceresoli@comelit.it>
 *      (See License)
 */
/*****/
/*
 *      Global functions and variables.
 */
```

./post/board/lwmon/sysmon.c

sysmon.c

```
/*
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 * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
 *
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 */
```

./post/board/lwmon5/dsp.c

dsp.c

```
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 *
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*/
```

```
./post/board/lwmon5/dspic.c
dspic.c
```

```
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*/
```

```
./post/board/lwmon5/fpga.c
fpga.c
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./post/board/lwmon5/gdc.c
```

```
gdc.c
```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,  
* MA 02111-1307 USA  
*/
```

```
./post/board/lwmon5/sysmon.c
```

```
sysmon.c
```

```
/*  
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```

```
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* Foundation, Inc., 59 Temple Place, Suite 330, Boston,
* MA 02111-1307 USA
*/
```

```
./post/board/lwmon5/watchdog.c
watchdog.c
```

```
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*/
```

```
./post/board/netta/codec.c
codec.c
```

```
/*
* (C) Copyright 2004
```

```
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* MA 02111-1307 USA
*/
```

```
./post/board/netta/dsp.c
```

```
dsp.c
```

```
/*
```

```
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*/
```

```
./post/board/pdm360ng/coproc_com.c
```

```
coproc_com.c
```

```
/*
```

```
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```

```
* Anatolij Gustschin, agust@denx.de.  
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* MA 02111-1307 USA  
*/
```

```
./post/cpu/mpc83xx/ecc.c
```

```
ecc.c
```

```
/*  
* (C) Copyright 2010  
* Eastman Kodak Company, <www.kodak.com>  
* Michael Zaidman, <michael.zaidman@kodak.com>  
*  
* The code is based on the cpu/mpc83xx/ecc.c written by  
* Dave Liu <daveliu@freescale.com>  
*  
* See file CREDITS for list of people who contributed to this  
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* MA 02111-1307 USA  
*/
```

./post/cpu/mpc8xx/cache.c

cache.c

/*

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*/

./post/cpu/mpc8xx/ether.c

ether.c

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./post/cpu/mpc8xx/spr.c

spr.c

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./post/cpu/mpc8xx/uart.c

uart.c

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./post/cpu/mpc8xx/usb.c

usb.c

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./post/cpu/mpc8xx/watchdog.c

watchdog.c

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 */
```

./post/cpu/ppc4xx/cache.c

cache.c

/*

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*

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*/

./post/cpu/ppc4xx/denali_ecc.c

denali_ecc.c

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```
./post/cpu/ppc4xx/ether.c  
ether.c
```

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*/
```

```
./post/cpu/ppc4xx/fpu.c  
fpu.c
```

```
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```

```
./post/cpu/ppc4xx/ocm.c
```

```
ocm.c
```

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*/
```

```
./post/cpu/ppc4xx/spr.c
```

```
spr.c
```

```
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*/
```

```
./post/cpu/ppc4xx/uart.c
```

```
uart.c
```

```
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*/
```

```
./post/cpu/ppc4xx/watchdog.c
```

```
watchdog.c
```

```
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*/
```

```
./post/drivers/flash.c
flash.c
```

```
/*
* Parallel NOR Flash tests
*
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```
./post/drivers/i2c.c
i2c.c
```

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./post/drivers/memory.c
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./post/drivers/rtc.c
rtc.c

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```
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./post/lib_powerpc/andi.c  
andi.c  
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```
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b.c  
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*/

./post/lib_powerpc/cmp.c

cmp.c

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./post/lib_powerpc/cmpi.c

cmpi.c

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```
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complex.c  
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cpu.c  
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```
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cpu_asm.h  
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```
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cr.c  
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```
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20001122-1.c  
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*/  
/*  
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```
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20010114-2.c  
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20010226-1.c
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/*
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./post/lib_powerpc/fpu/980619-1.c
980619-1.c
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* MA 02111-1307 USA  
*/  
/*  
* This file is originally a part of the GCC testsuite.  
*/
```

```
./post/lib_powerpc/fpu/acc1.c  
acc1.c
```

```
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*/  
/*  
* This file is originally a part of the GCC testsuite.  
*/
```

```
./post/lib_powerpc/fpu/compare-fp-1.c  
compare-fp-1.c
```

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* MA 02111-1307 USA
*/
/*
* Test for correctness of composite floating-point comparisons.
* Written by Paolo Bonzini, 26th May 2004.
* This file is originally a part of the GCC testsuite.
*/

./post/lib_powerpc/fpu/darwin-ldouble.c
darwin-ldouble.c
/*
* Borrowed from GCC 4.2.2 (which still was GPL v2+)
*/
/* 128-bit long double support routines for Darwin.

./post/lib_powerpc/fpu/fpu.c
fpu.c
/*
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*/
```

```
./post/lib_powerpc/fpu/mul-subnormal-single-1.c
mul-subnormal-single-1.c
```

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 * MA 02111-1307 USA
*/
/*
 * This file is originally a part of the GCC testsuite.
 * Check that certain subnormal numbers (formerly known as denormalized
 * numbers) are rounded to within 0.5 ulp. PR other/14354.
*/
```

```
./post/lib_powerpc/load.c
load.c
```

```
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*/
```

```
./post/lib_powerpc/multi.c
```

```
multi.c
```

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*/
```

```
./post/lib_powerpc/rlwimi.c
```

```
rlwimi.c
```

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*/
```

```
./post/lib_powerpc/rlwinm.c
```

```
rlwinm.c
```

```
/*
```

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*/
```

```
./post/lib_powerpc/rlwnm.c
```

```
rlwnm.c
```

```
/*
```

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*/
```

```
./post/lib_powerpc/srawi.c
```

```
srawi.c
```

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*/
```

```
./post/lib_powerpc/store.c
```

```
store.c
```

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*/
```

```
./post/lib_powerpc/string.c
```

```
string.c
```

```
/*
```

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```
./post/lib_powerpc/three.c
```

```
three.c
```

```
/*
```

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*/
```

```
./post/lib_powerpc/threei.c
```

```
threei.c
```

```
/*
```

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*/
```

```
./post/lib_powerpc/threex.c
```

```
threex.c
```

```
/*
```

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*/
```

```
./post/lib_powerpc/two.c
```

```
two.c
```

```
/*
```

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```

```
./post/lib_powerpc/twox.c
```

```
twox.c
```

```
/*
```

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```
./post/post.c
```

```
post.c
```

```
/*
```

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```

```
./post/tests.c
```

```
tests.c
```

```
/*
```

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*  
* Be sure to mark tests to be run before relocation as such with the  
* CONFIG_SYS_POST_PREREL flag so that logging is done correctly if the  
* logbuffer support is enabled.  
*/
```

```
./test/command_ut.c  
command_ut.c
```

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*/
```

```
./tools/aisimage.c  
aisimage.c
```

```
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```
./tools/aisimage.h
```

```
aisimage.h
```

```
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*/
```

```
./tools/bin2header.c
```

```
bin2header.c
```

```
/* bin2header.c - program to convert binary file into a C structure
* definition to be included in a header file.
*
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*/
```

```
./tools/bmp_logo.c
bmp_logo.c
No license information found.
```

```
./tools/default_image.c
default_image.c
/*
* (C) Copyright 2008 Semihalf
*
* (C) Copyright 2000-2004
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* Wolfgang Denk, wd@denx.de
*
* Updated-by: Prafulla Wadaskar <prafulla@marvell.com>
*             default_image specific code abstracted from mkimage.c
*             some functions added to address abstraction
*
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*/
```

```
./tools/easylogo/easylogo.c
```

```
easylogo.c
```

```
/*
```

```
** Easylogo TGA->header converter
```

```
** =====
```

```
** (C) 2000 by Paolo Scaffardi (arsenio@tin.it)
```

```
** AIRVENT SAM s.p.a - RIMINI(ITALY)
```

```
** (C) 2007-2008 Mike Frysinger <vapier@gentoo.org>
```

```
**
```

```
** This is still under construction!
```

```
*/
```

```
./tools/env/fw_env.c
```

```
fw_env.c
```

```
/*
```

```
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```

```
*
```

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* project.
```

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```
*/
```

```
./tools/env/fw_env.h
```

```
fw_env.h
```

```
/*
```

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```

```
* Wolfgang Denk, DENX Software Engineering, wd@denx.de.
```

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*
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* project.
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*/
```

```
./tools/env/fw_env_main.c
```

```
fw_env_main.c
```

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```

```
./tools/envcrc.c
```

```
envcrc.c
```

```
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```

```
./tools/fdt_host.h
fdt_host.h
```

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*/
```

```
./tools/fit_image.c
fit_image.c
```

```
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* Wolfgang Denk, wd@denx.de
*
* Updated-by: Prafulla Wadaskar <prafulla@marvell.com>
*             FIT image specific code abstracted from mkimage.c
*             some functions added to address abstraction
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```

```
./tools/gdb/error.c
```

```
error.c
```

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error.h
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```
./tools/gdb/gdbcont.c
gdbcont.c
```

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```
./tools/gdb/gdbsend.c
gdbsend.c
```

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```

```
./tools/gdb/remote.c
```

```
remote.c
```

```
/*
* taken from gdb/remote.c
*
* I am only interested in the write to memory stuff - everything else
* has been ripped out
*
* all the copyright notices etc have been left in
*/
/* enough so that it will compile */
```

```
./tools/gdb/remote.h
```

```
remote.h
```

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```

```
./tools/gdb/serial.c  
serial.c
```

```
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```

```
./tools/gdb/serial.h  
serial.h
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```

```
./tools/gen_eth_addr.c  
gen_eth_addr.c
```

```
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* MA 02111-1307 USA  
*/
```

```
./tools/getline.c  
getline.c  
/* getline.c -- Replacement for GNU C library function getline
```

```
./tools/getline.h  
getline.h  
No license information found.
```

```
./tools/img2srec.c  
img2srec.c  
/*****  
|*****
```

```
./tools/imls/imls.c  
imls.c  
/*  
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*/
```

```
./tools/imximage.c
```

```
imximage.c
```

```
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* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
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```

```
./tools/imximage.h
```

```
imximage.h
```

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*/
```

```
./tools/kernel-doc/docproc.c
docproc.c
```

```
/*
* docproc is a simple preprocessor for the template files
* used as placeholders for the kernel internal documentation.
* docproc is used for documentation-frontend and
* dependency-generator.
* The two usages have in common that they require
* some knowledge of the .tmpl syntax, therefore they
* are kept together.
*
* documentation-frontend
* Scans the template file and call kernel-doc for
* all occurrences of ![EIF]file
* Beforehand each referenced file is scanned for
* any symbols that are exported via these macros:
* EXPORT_SYMBOL(), EXPORT_SYMBOL_GPL(), &
* EXPORT_SYMBOL_GPL_FUTURE()
* This is used to create proper -function and
* -nofunction arguments in calls to kernel-doc.
* Usage: docproc doc file.tmpl
*
* dependency-generator:
* Scans the template file and list all files
* referenced in a format recognized by make.
* Usage: docproc depend file.tmpl
* Writes dependency information to stdout
* in the following format:
* file.tmpl src.c src2.c
* The filenames are obtained from the following constructs:
* !Efilename
* !Ifilename
* !Dfilename
```

```
*          !Ffilename
*          !Pfilename
*
*/
```

```
./tools/kwbimage.c
```

```
kwbimage.c
```

```
/*
```

```
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* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
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```

```
./tools/kwbimage.h
```

```
kwbimage.h
```

```
/*
```

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```

```
./tools/kwboot.c
```

```
kwboot.c
```

```
/*
* Boot a Marvell Kirkwood SoC, with Xmodem over UART0.
*
* (c) 2012 Daniel Stodden <daniel.stodden@gmail.com>
*
* References: marvell.com, "88F6180, 88F6190, 88F6192, and 88F6281
* Integrated Controller: Functional Specifications" December 2,
* 2008. Chapter 24.2 "BootROM Firmware".
*/
```

```
./tools/mingw_support.c
```

```
mingw_support.c
```

```
/*
* Copyright 2008 Extreme Engineering Solutions, Inc.
*
* mmap/munmap implementation derived from:
* Clamav Native Windows Port : mmap win32 compatibility layer
* Copyright (c) 2005-2006 Gianluigi Tiesi <sherpya@netfarm.it>
* Parts by Kees Zeelenberg <kzlg@users.sourceforge.net> (LibGW32C)
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```

```
./tools/mingw_support.h
```

```
mingw_support.h
```

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```

```
./tools/mkenvimage.c
```

```
mkenvimage.c
```

```
/*
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* David Wagner <david.wagner@free-electrons.com>
*
* Inspired from envcrc.c:
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* Paolo Scaffardi, AIRVENT SAM s.p.a - RIMINI(ITALY), arsenio@tin.it
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```

```
./tools/mkexynossp1.c
```

```
mkexynossp1.c
```

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```

```
./tools/mkimage.c
```

```
mkimage.c
```

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```

```
./tools/mkimage.h
```

```
mkimage.h
```

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*/
```

```
./tools/mpc86x_clk.c
mpc86x_clk.c
```

```
/*
* (C) Copyright 2003 Intracom S.A.
* Pantelis Antoniou <panto@intracom.gr>
*
* This little program makes an exhaustive search for the
* correct terms of pdf, mfi, mfn, mfd, s, dbrmo, in PLPRCR.
* The goal is to produce a gclk2 from a xin input, while respecting
* all the restrictions on their combination.
*
* Generally you select the first row of the produced table.
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*/
```

```
./tools/mxsboot.c
mxsboot.c
```

```
/*
```

```
* Freescale i.MX28 image generator
*
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*/
```

```
./tools/ncb.c
```

```
ncb.c
```

```
No license information found.
```

```
./tools/omap/clocks_get_m_n.c
```

```
clocks_get_m_n.c
```

```
/*
```

```
* Program for finding M & N values for DPLLs
* To be run on Host PC
*
* (C) Copyright 2010
* Texas Instruments, <www.ti.com>
*
* Aneesh V <aneesh@ti.com>
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*/
```

```
./tools/omapimage.c
```

```
omapimage.c
```

```
/*  
* (C) Copyright 2010  
* Linaro LTD, www.linaro.org  
* Author: John Rigby <john.rigby@linaro.org>  
* Based on TI's signGP.c  
*  
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*  
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* Marvell Semiconductor <www.marvell.com>  
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>  
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*/
```

```
./tools/omapimage.h
```

```
omapimage.h
```

```
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* Author John Rigby <john.rigby@linaro.org>  
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*/
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```

```
./tools/os_support.c
```

```
os_support.c
```

```
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```
./tools/os_support.h
```

```
os_support.h
```

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```

```
./tools/pblimage.c
```

```
pblimage.c
```

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```

```
./tools/pblimage.h
```

```
pblimage.h
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```

```
./tools/ublimage.c
```

```
ublimage.c
```

```
/*
```

```
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* Heiko Schocher, DENX Software Engineering, hs@denx.de.
*
* Based on:
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* Stefano Babic, DENX Software Engineering, sbabic@denx.de.
*
```

```
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* Marvell Semiconductor <www.marvell.com>
* Written-by: Prafulla Wadaskar <prafulla@marvell.com>
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```

```
./tools/ublimage.h
```

```
ublimage.h
```

```
/*
```

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```

```
./tools/ubsha1.c
```

```
ubsha1.c
```

```
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*/
```

```
./tools/updater/cmd_flash.c
```

```
cmd_flash.c
```

```
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*
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```

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```

./tools/updater/ctype.c

ctype.c

```
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*/
```

./tools/updater/dummy.c

dummy.c

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./tools/updater/flash.c

flash.c

```
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```

```
./tools/updater/flash_hw.c
flash_hw.c
```

```
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 */
```

```
./tools/updater/string.c
string.c
/*
 * linux/lib/string.c
 *
 * Copyright (C) 1991, 1992 Linus Torvalds
 */
/*
 * stupid library routines.. The optimized versions should generally be found
 * as inline code in <asm-xx/string.h>
 *
 * These are buggy as well..
 */
```

```
./tools/updater/update.c
update.c
No license information found.
```

```
./tools/updater/utils.c
utils.c
No license information found.
```

```
./tools/xway-swap-bytes.c
xway-swap-bytes.c
No license information found.
```