

```

./Avalon/altera_avalon_pio_regs.h
altera_avalon_pio_regs.h
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altera_avalon_spi_regs.h
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./Avalon/FpgaSystem.h
FpgaSystem.h
// In NIOS version of code, these values were all defined in the System.h file
output by Quartus Tool
// Now come out in some html files, in handoff directory
// defines from System.h in NIOS2 version, don't know where they go in CycloneV
version
// PA drivers

./Avalon/io.h
io.h
// printf("writel: addr 0x%x val:0x%x\n", base + (reg<<2), val);

./HardwareLibrary/alt_16550_uart.c
alt_16550_uart.c
/*****
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./HardwareLibrary/alt_address_space.c
alt_address_space.c
/*****
*
* alt_address_space.c - API for the Altera SoC FPGA address space.
*
*****/
/*****
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alt_bridge_manager.c
/*****
*
* alt_bridge_manager.c - API for the Altera SoC FPGA bridge manager.
*
*****/
/*****
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./HardwareLibrary/alt_cache.c
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alt_cache.c
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alt\_clock\_manager.c

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./HardwareLibrary/alt\_dma.c

alt\_dma.c

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/*****
*
* alt_fpga_manager.c - API for the Altera SoC FPGA manager.
*
*****/
/*****

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alt_generalpurpose_io.c
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./HardwareLibrary/alt\_globaltmr.c

alt\_globaltmr.c

/\*\*\*\*\*

\*

\* alt\_globaltmr.c - API for the Altera SoC FPGA global timer.

\*

\*\*\*\*\*/

/\*\*\*\*\*

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alt\_i2c.c

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 */
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```

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alt\_interrupt.c

```
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 * alt_interrupt.c - API for the Altera SoC FPGA interrupts
 *
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 * *****/
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alt_mmu.c
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```
./HardwareLibrary/alt_nand.c
```

```

alt_nand.c
/*****
*
* alt_nand.c - API for the Altera SoC FPGA NAND device.
*
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*
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alt_reset_manager.c
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*
* alt_reset_manager.c - API for the Altera SoC FPGA reset manager.
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\*\*\*\*\*/

./HardwareLibrary/alt\_spi.c

alt\_spi.c

\*\*\*\*\*/

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*****/

./HardwareLibrary/alt_system_manager.c
alt_system_manager.c
/*****
*
* alt_system_manager.c - API for the Altera SoC FPGA system manager
*
*****/
/*****
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*
* alt_watchdog.c - API for the Altera SoC FPGA watchdog timers.
*
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*****/

./HardwareLibrary/include/alt_16550_uart.h
alt_16550_uart.h
/*
* Altera - SoC UART Manager
*/
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./HardwareLibrary/include/alt_address_space.h
alt_address_space.h
/*! \file
*   Altera - SoC FPGA Address Space Manager
*/
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*****/

./HardwareLibrary/include/alt_bridge_manager.h
alt_bridge_manager.h
/*! \file
 *   Altera - SoC FPGA FPGA/HPS Bridge Management
 */
/*****
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./HardwareLibrary/include/alt_clock_manager.h
alt_clock_manager.h
/*! \file
*   Contains definitions for the Altera Hardware Libraries Clock Manager
*   Application Programming Interface
*/
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```
./HardwareLibrary/include/alt_dma_common.h
```

```
alt_dma_common.h
```

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/*****
```

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./HardwareLibrary/include/alt_ecc.h
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/*! \file
* Altera - SoC FPGA ECC Management
*/
/*****
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./HardwareLibrary/include/alt\_fpga\_manager.h  
alt\_fpga\_manager.h

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*****/

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/*! \file
*   Altera - GPIO Module
*/
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/*! \file
*   Altera - Module Description
*/
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```
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alt_hwlibs_ver.h
```

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alt\_i2c.h

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./HardwareLibrary/include/alt\_interrupt.h

alt\_interrupt.h

/\*!

\* Altera - SoC Interrupt Manager - Secure interface

\*/

/\*\*\*\*\*

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alt_interrupt_common.h
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./HardwareLibrary/include/alt\_mmu.h

alt\_mmu.h

/\*\*\*\*\*/

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./HardwareLibrary/include/alt\_mpu\_registers.h

alt\_mpu\_registers.h

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./HardwareLibrary/include/alt\_nand\_flash.h

alt\_nand\_flash.h

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./HardwareLibrary/include/alt\_nand\_private.h

alt\_nand\_private.h

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./HardwareLibrary/include/alt\_printf.h

alt\_printf.h

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./HardwareLibrary/include/alt\_qspi.h



```

alt_qspi.h
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./HardwareLibrary/include/alt_qspi_private.h
alt_qspi_private.h
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\*\*\*\*\*/

./HardwareLibrary/include/alt\_reset\_manager.h  
alt\_reset\_manager.h

/\*! \file

\* Altera - SoC Reset Manager

\*/

/\*\*\*\*\*/

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./HardwareLibrary/include/alt_spi.h
alt_spi.h
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./HardwareLibrary/include/alt\_system\_manager.h  
alt\_system\_manager.h

/\*! \file

\* Altera - SoC FPGA System Manager

\*/

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./HardwareLibrary/include/alt\_timers.h

```

alt_timers.h
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 *   Altera - Module Description
 */
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hwlib.h
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./HardwareLibrary/include/socal/alt_acpidmap.h
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```
alt_acpidmap.h
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./HardwareLibrary/include/socal/alt_can.h
alt_can.h

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./HardwareLibrary/include/socal/alt_clkmgr.h
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alt_clkmgr.h
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```
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alt_dap.h
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/*****
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alt_dmanonsecure.h
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```

```
./HardwareLibrary/include/socal/alt_dmasecure.h
alt_dmasecure.h
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*
```

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```

```

./HardwareLibrary/include/socal/alt_emac.h

```

```

alt_emac.h
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```
./HardwareLibrary/include/socal/alt_f2h.h
```

```
alt_f2h.h
```

```
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```
./HardwareLibrary/include/socal/alt_fpgamgr.h
```

```
alt_fpgamgr.h
```

```
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```

```

./HardwareLibrary/include/socal/alt_fpgamgrdata.h
alt_fpgamgrdata.h

```

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```

./HardwareLibrary/include/socal/alt_gpio.h
alt_gpio.h

```

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```

```
./HardwareLibrary/include/socal/alt_h2f.h
```

```
alt_h2f.h
```

```

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./HardwareLibrary/include/socal/alt_i2c.h
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```
./HardwareLibrary/include/socal/alt_l4wd.h
alt_l4wd.h
```

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```
./HardwareLibrary/include/socal/alt_lwfpgaslvs.h
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```

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```
./HardwareLibrary/include/socal/alt_nand.h
```

```
alt_nand.h
```

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```
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```
alt_nanddata.h
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```

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./HardwareLibrary/include/socal/alt\_qspidata.h  
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```
./HardwareLibrary/include/socal/alt_rom.h
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```
alt_rom.h
```

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```

```
./HardwareLibrary/include/socal/alt_rstmgr.h
```

```
alt_rstmgr.h
```

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```

./HardwareLibrary/include/socal/alt_scanmgr.h
alt_scanmgr.h

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```

```

./HardwareLibrary/include/socal/alt_sdmmc.h
alt_sdmmc.h

```

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```

./HardwareLibrary/include/socal/alt_sdr.h
alt_sdr.h

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```
./HardwareLibrary/include/socal/alt_spim.h
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alt_spim.h
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```
./HardwareLibrary/include/socal/alt_spis.h
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alt_spis.h
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```

./HardwareLibrary/include/socal/alt_stm.h

```

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alt_stm.h

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```

./HardwareLibrary/include/socal/alt_sysmgr.h

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alt_sysmgr.h

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```

```
./HardwareLibrary/include/socal/alt_tmr.h
```

```
alt_tmr.h
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```
./HardwareLibrary/include/socal/alt_uart.h
```

```
alt_uart.h
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```
./HardwareLibrary/include/socal/alt_usb.h
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```
alt_usb.h
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```

./HardwareLibrary/include/socal/hps.h
hps.h

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./HardwareLibrary/include/socal/socal.h
socal.h

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```
./SoCsupport/cache_support.c
cache_support.c
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./SoCSupport/fpga_support.c  
fpga_support.c
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mmu\_support.h

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./SoCSupport/include/uart0\_support.h

uart0\_support.h

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\*\*\*\*\*/

./SoCSupport/mmu\_support.c

mmu\_support.c

/\*\*\*\*\*

\*/

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\*\*\*\*\*/

./SoCSupport/uart0\_support.c

uart0\_support.c

/\*\*\*\*\*

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\*

\*\*\*\*\*/

./CycloneMCU\_App/src/driverlib/adc.c

adc.c

//\*\*\*\*\*

//

// FILE: adc.c

//

// TITLE: C28x ADC driver.

//

//\*\*\*\*\*

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// \$Release Date: Fri May 24 03:33:32 CDT 2019 \$

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// $
//#####

./CycloneMCU_App/src/driverlib/adc.h
adc.h
//#####
//
// FILE:   adc.h
//
// TITLE:  C28x ADC driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/asysctl.c
asysctl.c
//#####
//
// FILE:   asysctl.c
//
// TITLE:  C28x Driver for Analog System Control.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/asysctl.h
asysctl.h
//#####
//
// FILE:   asysctl.h
//
// TITLE:  C28x driver for Analog System Control.
//
//#####
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// $
//#####

```

```

./CycloneMCU_App/src/driverlib/can.c
can.c
//#####
//
// FILE:    can.c
//
// TITLE:   C28x CAN driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/can.h
can.h
//#####
//
// FILE:    can.h
//
// TITLE:   C28x CAN driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/cla.c
cla.c
//#####
//
// FILE:   cla.c
//
// TITLE:  CLA Driver Implementation File
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/cla.h
cla.h
//#####
//
// FILE:   cla.h
//
// TITLE:  CLA Driver Implementation File
//
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//#####

./CycloneMCU_App/src/driverlib/clapromcrc.c
clapromcrc.c
//#####
//
// FILE:    clapromcrc.c
//
// TITLE:   C28x CLAPROMCRC driver.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/clapromcrc.h
clapromcrc.h
//#####
//
// FILE:    clapromcrc.h
//
// TITLE:   C28x and CLA CLAPROMCRC driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/cmpss.c
cmpss.c
//#####
//
// FILE:    cmpss.c
//

```

```

// TITLE: C28x CMPSS driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/cmpss.h
cmpss.h
//#####
//
// FILE: cmpss.h
//
// TITLE: C28x CMPSS driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/cpu.h
cpu.h
//#####
//
// FILE:    cpu.h
//
// TITLE:   Useful C28x CPU defines.
//
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// $
//#####

./CycloneMCU_App/src/driverlib/cputimer.c
cputimer.c
//#####
//
// FILE:    cputimer.c
//
// TITLE:    C28x CPU timer Driver
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/cputimer.h
cputimer.h
//#####
//
// FILE:    cputimer.h
//
// TITLE:    C28x CPU timer Driver
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/dac.c
dac.c

```

```

//#####
//
// FILE:   dac.c
//
// TITLE:  C28x DAC driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/dac.h
dac.h
//#####
//
// FILE:   dac.h
//
// TITLE:  C28x DAC driver.
//
//#####
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```

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// $
//#####

./CycloneMCU_App/src/driverlib/dcc.c
dcc.c
//#####
//
// FILE:    dcc.c
//
// TITLE:   C28x DCC driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/dcc.h
dcc.h
//#####
//
// FILE:    dcc.h
//
// TITLE:   C28x DCC driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/dcsn.c
dcsm.c
//#####
//
// FILE:    dcsn.c
//
// TITLE:   C28x Driver for the DCSM security module.
//
//#####
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// $

```

```

//#####

./CycloneMCU_App/src/driverlib/dcsn.h
dcsm.h
//#####
//
// FILE:    dcsn.h
//
// TITLE:   C28x Driver for the DCSM security module.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/debug.h
debug.h
//#####
//
// FILE:    debug.h
//
// TITLE:   Assert definition macro for debug.
//

```

```
//#####  
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// $  
//#####
```

```
./CycloneMCU_App/src/driverlib/device.h  
device.h
```

```
//#####  
//  
// FILE:    device.h  
//  
// TITLE:   Device setup for examples.  
//  
//#####  
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// $
//#####

./CycloneMCU_App/src/driverlib/dma.c
dma.c
//#####
//
// FILE:    dma.c
//
// TITLE:   C28x DMA driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/dma.h
dma.h
//#####
//
// FILE: dma.h
//
// TITLE: C28x DMA driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/driverlib.h
driverlib.h
//#####
//
// FILE:    driverlib.h
//
// TITLE:   C28x Driverlib Header File
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $
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// $
//#####

./CycloneMCU_App/src/driverlib/ecap.c
ecap.c
//#####
//

```

```

// FILE:   ecap.c
//
// TITLE: C28x ECAP driver
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/ecap.h
ecap.h
//#####
//
// FILE: ecap.h
//
// TITLE: C28x ECAP driver
//
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./CycloneMCU_App/src/driverlib/epwm.c
epwm.c
//#####
//
// FILE:   epwm.c
//
// TITLE:  C28x EPWM driver.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/epwm.h
epwm.h
//#####
//
// FILE:    epwm.h
//
// TITLE:    C28x EPWM Driver
//
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// $
//#####

./CycloneMCU_App/src/driverlib/eqep.c
eqep.c
//#####
//
// FILE:    eqep.c
//
// TITLE:   C28x eQEP driver.
//
//#####
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// $
//#####

```

```

./CycloneMCU_App/src/driverlib/eqep.h
eqep.h
//#####
//
// FILE:    eqep.h
//
// TITLE:   C28x eQEP driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/f28004x_adc.h
f28004x_adc.h
//#####
//
// FILE:    f28004x_adc.h
//
// TITLE:   ADC Register Definitions.
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $

```

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./CycloneMCU_App/src/driverlib/f28004x_analogsubsys.h
f28004x_analogsubsys.h
//#####
//
// FILE:      f28004x_analogsubsys.h
//
// TITLE:     ANALOGSUBSYS Register Definitions.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/f28004x_can.h
f28004x_can.h
//#####
//
// FILE:    f28004x_can.h
//
// TITLE:   CAN Register Definitions.
//
//#####
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./CycloneMCU_App/src/driverlib/f28004x_cla.h
f28004x_cla.h
//#####
//
// FILE:    f28004x_cla.h
//
// TITLE:   CLA Register Definitions.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/f28004x_cla_prom_crc32.h
f28004x_cla_prom_crc32.h
//#####
//
// FILE:    f28004x_cla_prom_crc32.h
//
// TITLE:    CLA_PROM_CRC32 Register Definitions.
//
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// $
//#####

./CycloneMCU_App/src/driverlib/f28004x_cmpss.h
f28004x_cmpss.h
//#####
//
// FILE:    f28004x_cmpss.h
//

```



```

// TITLE:   CMPSS Register Definitions.
//
//#####
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./CycloneMCU_App/src/driverlib/f28004x_cputimer.h
f28004x_cputimer.h
//#####
//
// FILE:      f28004x_cputimer.h
//
// TITLE:     CPUTIMER Register Definitions.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/f28004x_dac.h
f28004x_dac.h
//#####
//
// FILE:    f28004x_dac.h
//
// TITLE:   DAC Register Definitions.
//
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./CycloneMCU_App/src/driverlib/f28004x_dcc.h
f28004x_dcc.h
//#####
//
// FILE:    f28004x_dcc.h
//
// TITLE:   DCC Register Definitions.
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $
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./CycloneMCU_App/src/driverlib/f28004x_dcsn.h
f28004x_dcsn.h
//#####
//
// FILE:    f28004x_dcsn.h
//
// TITLE:    DCSM Register Definitions.
//
//#####
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./CycloneMCU_App/src/driverlib/f28004x_device.h
f28004x_device.h

```

```

//#####
//
// FILE:    F28004x_device.h
//
// TITLE:    F28004x Device Definitions.
//
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./CycloneMCU_App/src/driverlib/f28004x_dma.h
f28004x_dma.h
//#####
//
// FILE:    f28004x_dma.h
//
// TITLE:    F28004x Device DMA Register Definitions.
//
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f28004x_ecap.h
//#####
//
// FILE:    f28004x_ecap.h
//
// TITLE:   ECAP Register Definitions.
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f28004x_epwm.h
//#####
//
// FILE:      f28004x_epwm.h
//
// TITLE:     EPWM Register Definitions.
//
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// $
//#####

./CycloneMCU_App/src/driverlib/f28004x_epwm_xbar.h
f28004x_epwm_xbar.h
//#####
//
// FILE:      f28004x_epwm_xbar.h
//
// TITLE:     EPWM_XBAR Register Definitions.
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $
// $Release Date: Fri May 24 03:33:32 CDT 2019 $
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// $

```



```

//#####

./CycloneMCU_App/src/driverlib/f28004x_eqep.h
f28004x_eqep.h
//#####
//
// FILE:    f28004x_eqep.h
//
// TITLE:    EQEP Register Definitions.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/f28004x_erad.h
f28004x_erad.h
//#####
//
// FILE:    f28004x_erad.h
//
// TITLE:    ERAD Register Definitions.
//

```

```

//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/f28004x_flash.h
f28004x_flash.h
//#####
//
// FILE:    f28004x_flash.h
//
// TITLE:   FLASH Register Definitions.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/f28004x_fsi.h
f28004x_fsi.h
//#####
//
// FILE:    f28004x_fsi.h
//
// TITLE:    FSI Register Definitions.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/f28004x_gpio.h
f28004x_gpio.h
//#####
//
// FILE:    f28004x_gpio.h
//
// TITLE:   GPIO Register Definitions.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/f28004x_i2c.h
f28004x_i2c.h
//#####
//
// FILE:    f28004x_i2c.h
//
// TITLE:    I2C Register Definitions.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/f28004x_input_xbar.h
f28004x_input_xbar.h
//#####
//

```

```

// FILE:    f28004x_input_xbar.h
//
// TITLE:    INPUT_XBAR Register Definitions.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/f28004x_lin.h
f28004x_lin.h
//#####
//
// FILE:    f28004x_lin.h
//
// TITLE:    LIN Register Definitions.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/f28004x_memconfig.h
f28004x_memconfig.h
//#####
//
// FILE:      f28004x_memconfig.h
//
// TITLE:     MEMCONFIG Register Definitions.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/f28004x_nmiintrupt.h
f28004x_nmiintrupt.h
//#####
//
// FILE:      f28004x_nmiintrupt.h
//
// TITLE:     NMIINTRUPT Register Definitions.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/f28004x_output_xbar.h
f28004x_output_xbar.h
//#####
//
// FILE:    f28004x_output_xbar.h
//
// TITLE:    OUTPUT_XBAR Register Definitions.
//
//#####
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//#####

```

```

./CycloneMCU_App/src/driverlib/f28004x_pga.h
f28004x_pga.h
//#####
//
// FILE:    f28004x_pga.h
//
// TITLE:    PGA Register Definitions.
//
//#####
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./CycloneMCU_App/src/driverlib/f28004x_piectrl.h
f28004x_piectrl.h
//#####
//
// FILE:    f28004x_piectrl.h
//
// TITLE:    PIECTRL Register Definitions.
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $

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//#####

./CycloneMCU_App/src/driverlib/f28004x_pievect.h
f28004x_pievect.h
//#####
//
// FILE:    f28004x_pievect.h
//
// TITLE:   F28004x Device PIE Vector Table Definitions
//
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./CycloneMCU_App/src/driverlib/f28004x_pmbus.h
f28004x_pmbus.h
//#####
//
// FILE:    f28004x_pmbus.h
//
// TITLE:    PMBUS Register Definitions.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/f28004x_sci.h
f28004x_sci.h
//#####
//
// FILE:    f28004x_sci.h
//
// TITLE:   SCI Register Definitions.
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $
// $Release Date: Fri May 24 03:33:32 CDT 2019 $
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//#####

./CycloneMCU_App/src/driverlib/f28004x_sdfm.h
f28004x_sdfm.h
//#####
//
// FILE:    f28004x_sdfm.h
//
// TITLE:    SDFM Register Definitions.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/f28004x_spi.h
f28004x_spi.h
//#####
//
// FILE:    f28004x_spi.h
//

```

```

// TITLE:   SPI Register Definitions.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/f28004x_sysctrl.h
f28004x_sysctrl.h
//#####
//
// FILE:     f28004x_sysctrl.h
//
// TITLE:    SYSCTRL Register Definitions.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/f28004x_xbar.h
f28004x_xbar.h
//#####
//
// FILE:    f28004x_xbar.h
//
// TITLE:   XBAR Register Definitions.
//
//#####
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./CycloneMCU_App/src/driverlib/f28004x_xint.h
f28004x_xint.h
//#####
//
// FILE:      f28004x_xint.h
//
// TITLE:     XINT Register Definitions.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/flash.c
flash.c
//#####
//
// FILE:    flash.c
//
// TITLE:   C28x Flash driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/flash.h
flash.h

```

```

//#####
//
// FILE:    flash.h
//
// TITLE:   C28x Flash driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/fsi.c
fsi.c
//#####
//
// FILE:    fsi.c
//
// TITLE:   C28x FSI driver.
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $
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// $
//#####

./CycloneMCU_App/src/driverlib/fsi.h
fsi.h
//#####
//
// FILE:   fsi.h
//
// TITLE:  C28x FSI Driver API header file
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/gpio.c
gpio.c
//#####
//
// FILE:    gpio.c
//
// TITLE:   C28x GPIO driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/gpio.h
gpio.h
//#####
//
// FILE:    gpio.h
//
// TITLE:   C28x GPIO driver.
//
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```

```
//#####

./CycloneMCU_App/src/driverlib/hrcap.c
hrcap.c
//#####
//
// FILE:   hrcap.c
//
// TITLE:  C28x HRCAP driver.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/hrcap.h
hrcap.h
//#####
//
// FILE:   hrcap.h
//
// TITLE:  C28x HRCAP Driver.
//
```

```
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// $  
//#####
```

```
./CycloneMCU_App/src/driverlib/hrpwm.c  
hrpwm.c
```

```
//#####  
//  
// FILE:   hrpwm.c  
//  
// TITLE:  C28x HRPWM driver.  
//  
//#####  
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//#####

./CycloneMCU_App/src/driverlib/hrpwm.h
hrpwm.h
//#####
//
// FILE:   hrpwm.h
//
// TITLE:   C28x HRPWM Driver
//
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// $
//#####

./CycloneMCU_App/src/driverlib/i2c.c
i2c.c
//#####
//
// FILE: i2c.c
//
// TITLE: C28x I2C driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/i2c.h
i2c.h
//#####
//
// FILE:    i2c.h
//
// TITLE:   C28x I2C driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_adc.h
hw_adc.h
//#####
//

```

```

// FILE:    hw_adc.h
//
// TITLE:    Definitions for the ADC registers.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_asysctl.h
hw_asysctl.h
//#####
//
// FILE:    hw_asysctl.h
//
// TITLE:    Definitions for the ASYSCTL registers.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_can.h
hw_can.h
//#####
//
// FILE:      hw_can.h
//
// TITLE:     Definitions for the CAN registers.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_cla.h
hw_cla.h
//#####
//
// FILE:    hw_cla.h
//
// TITLE:   Definitions for the CLA registers.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_clapromcrc.h
hw_clapromcrc.h
//#####
//
// FILE:    hw_clapromcrc.h
//
// TITLE:    Definitions for the CLAPROMCRC registers.
//
//#####
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// $
//#####

```

```

./CycloneMCU_App/src/driverlib/inc/hw_cmpss.h
hw_cmpss.h
//#####
//
// FILE:    hw_cmpss.h
//
// TITLE:   Definitions for the CMPSS registers.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_cputimer.h
hw_cputimer.h
//#####
//
// FILE:    hw_cputimer.h
//
// TITLE:   Definitions for the CPUTIMER registers.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/inc/hw_dac.h
hw_dac.h
//#####
//
// FILE:      hw_dac.h
//
// TITLE:     Definitions for the DAC registers.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/inc/hw_dcc.h
hw_dcc.h
//#####
//
// FILE:    hw_dcc.h
//
// TITLE:   Definitions for the DCC registers.
//
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./CycloneMCU_App/src/driverlib/inc/hw_dcs.h
hw_dcs.h
//#####
//
// FILE:    hw_dcs.h
//
// TITLE:   Definitions for the DCS registers.
//
//#####
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./CycloneMCU_App/src/driverlib/inc/hw_dma.h
hw_dma.h
//#####
//
// FILE:    hw_dma.h
//
// TITLE:    Definitions for the DMA registers.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/inc/hw_ecap.h
hw_ecap.h
//#####
//
// FILE:    hw_ecap.h
//

```

```

// TITLE:   Definitions for the ECAP registers.
//
//#####
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./CycloneMCU_App/src/driverlib/inc/hw_epwm.h
hw_epwm.h
//#####
//
// FILE:      hw_epwm.h
//
// TITLE:     Definitions for the EPWM registers.
//
//#####
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./CycloneMCU_App/src/driverlib/inc/hw_epwmxbar.h
hw_epwmxbar.h
//#####
//
// FILE:    hw_epwmxbar.h
//
// TITLE:   Definitions for the XBAR registers.
//
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_eqep.h
hw_eqep.h
//#####
//
// FILE:    hw_eqep.h
//
// TITLE:   Definitions for the EQEP registers.
//
//#####
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// $
```

```
//#####
```

```
./CycloneMCU_App/src/driverlib/inc/hw_erad.h  
hw_erad.h
```

```
//#####
```

```
//  
// FILE: hw_erad.h  
//  
// TITLE: Definitions for the ERAD registers.  
//
```

```
//#####
```

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// $
```

```
//#####
```

```
./CycloneMCU_App/src/driverlib/inc/hw_flash.h  
hw_flash.h
```



```

//#####
//
// FILE:    hw_flash.h
//
// TITLE:    Definitions for the FLASH registers.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_fsi.h
hw_fsi.h
//#####
//
// FILE:    hw_fsi.h
//
// TITLE:    Definitions for the FSI registers.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/inc/hw_gpio.h
hw_gpio.h
//#####
//
// FILE:    hw_gpio.h
//
// TITLE:   Definitions for the GPIO registers.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_hrcap.h
hw_hrcap.h
//#####
//
// FILE:    hw_hrcap.h
//
// TITLE:   Definitions for the HRCAP registers.
//
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_hrpwm.h
hw_hrpwm.h
//#####
//
// FILE:      hw_hrpwm.h
//
// TITLE:     Definitions for the HRPWM registers.
//
//#####
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```

```

//#####

./CycloneMCU_App/src/driverlib/inc/hw_i2c.h
hw_i2c.h
//#####
//
// FILE:    hw_i2c.h
//
// TITLE:    Definitions for the I2C registers.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_inputxbar.h
hw_inputxbar.h
//#####
//
// FILE:    hw_inputxbar.h
//
// TITLE:    Definitions for the XBAR registers.
//

```

```
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// $
//#####
```

```
./CycloneMCU_App/src/driverlib/inc/hw_ints.h
hw_ints.h
```

```
//#####
//
// FILE:    hw_ints.h
//
// TITLE:   Definitions of interrupt numbers for use with interrupt.c.
//
//#####
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//#####
```

```
./CycloneMCU_App/src/driverlib/inc/hw_lin.h
hw_lin.h
```

```
//#####
//
// FILE:    hw_lin.h
```

```

//
// TITLE:   Definitions for the LIN registers.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/inc/hw_memcfg.h
hw_memcfg.h
//#####
//
// FILE:     hw_memcfg.h
//
// TITLE:    Definitions for the MEMCFG registers.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/inc/hw_memmap.h
hw_memmap.h
//#####
//
// FILE:    hw_memmap.h
//
// TITLE:    Macros defining the memory map of the C28x.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_nmi.h
hw_nmi.h
//#####
//
// FILE: hw_nmi.h
//
// TITLE: Definitions for the NMI registers.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_outputxbar.h
hw_outputxbar.h
//#####
//
// FILE:    hw_outputxbar.h
//
// TITLE:    Definitions for the XBAR registers.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_pga.h

```

```

hw_pga.h
//#####
//
// FILE:    hw_pga.h
//
// TITLE:   Definitions for the PGA registers.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_pie.h
hw_pie.h
//#####
//
// FILE:    hw_pie.h
//
// TITLE:   Definitions for the PIE registers.
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $
// $Release Date: Fri May 24 03:33:32 CDT 2019 $

```

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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_pmbus.h
hw_pmbus.h
//#####
//
// FILE:      hw_pmbus.h
//
// TITLE:     Definitions for the PMBUS registers.
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_sci.h
hw_sci.h
//#####
//
// FILE:    hw_sci.h
//
// TITLE:   Definitions for the SCI registers.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_sdfm.h
hw_sdfm.h
//#####
//
// FILE:    hw_sdfm.h
//
// TITLE:   Definitions for the SDFM registers.
//
//#####
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```

```

// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_spi.h
hw_spi.h
//#####
//
// FILE:    hw_spi.h
//
// TITLE:    Definitions for the SPI registers.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_sysctl.h
hw_sysctl.h
//#####
//
// FILE:    hw_sysctl.h
//
// TITLE:    Definitions for the SYSCTL registers.

```

```

//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_types.h
hw_types.h
//#####
//
// FILE:    hw_types.h
//
// TITLE:   Type definitions used in driverlib functions.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_xbar.h
hw_xbar.h
//#####
//
// FILE:    hw_xbar.h
//
// TITLE:   Definitions for the XBAR registers.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/inc/hw_xint.h
hw_xint.h
//#####
//
// FILE:    hw_xint.h
//
// TITLE:   Definitions for the XINT registers.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/interrupt.c
interrupt.c
//#####
//
// FILE:    interrupt.c
//
// TITLE:   C28x Interrupt (PIE) driver.
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $
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// $
//#####

./CycloneMCU_App/src/driverlib/interrupt.h
interrupt.h
//#####

```

```

//
// FILE:   interrupt.h
//
// TITLE:  C28x Interrupt (PIE) driver.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/lin.c
lin.c
//#####
//
// FILE:   lin.c
//
// TITLE:  C28x LIN driver.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/lin.h
lin.h
//#####
//
// FILE:   lin.h
//
// TITLE:  C28x LIN driver.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/memcfg.c
memcfg.c
//#####
//
// FILE:    memcfg.c
//
// TITLE:   C28x RAM config driver.
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/memcfg.h
memcfg.h
//#####
//
// FILE:    memcfg.h
//
// TITLE:   C28x RAM config driver.
//
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//#####

```

```

./CycloneMCU_App/src/driverlib/pga.c
pga.c
//#####
//
// FILE:   pga.c
//
// TITLE:  C28x PGA driver.
//
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// $
//#####

./CycloneMCU_App/src/driverlib/pga.h
pga.h
//#####
//
// FILE:   pga.h
//
// TITLE:  C28x PGA driver.
//
//#####

```



```

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// $
//#####

./CycloneMCU_App/src/driverlib/pin_map.h
pin_map.h
//#####
//
// FILE:   pin_map.h
//
// TITLE:  Definitions of pin mux info for gpio.c.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/pmbus.c
pmbus.c
//#####
//
// FILE:    pmbus.c
//
// TITLE:   C28x PMBUS driver
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/pmbus.h
pmbus.h
//#####
//
// FILE:    pmbus.h
//
// TITLE:   C28x PMBUS Driver
//
//#####
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//#####

./CycloneMCU_App/src/driverlib/sci.c
sci.c
//#####
//
// FILE:    sci.c
//
// TITLE:   C28x SCI driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/sci.h
sci.h
//#####
//
// FILE:    sci.h

```

```

//
// TITLE: C28x SCI driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/sdfm.c
sdfm.c
//#####
//
// FILE: sdfm.c
//
// TITLE: C28x SDFM Driver
//
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// $
//#####

./CycloneMCU_App/src/driverlib/sdfm.h
sdfm.h
//#####
//
// FILE:    sdfm.h
//
// TITLE:    C28x SDFM Driver
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/spi.c
spi.c
//#####
//
// FILE: spi.c
//
// TITLE: C28x SPI driver.
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $
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// $
//#####

./CycloneMCU_App/src/driverlib/spi.h
spi.h
//#####
//
// FILE:    spi.h
//
// TITLE:   C28x SPI driver.
//
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// $TI Release: F28004x Support Library v1.06.00.00 $
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// $
//#####

./CycloneMCU_App/src/driverlib/sysctl.c

```



```

sysctl.c
//#####
//
// FILE:    sysctl.c
//
// TITLE:   C28x system control driver.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/sysctl.h
sysctl.h
//#####
//
// FILE:    sysctl.h
//
// TITLE:   C28x system control driver.
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $
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// $
//#####

./CycloneMCU_App/src/driverlib/version.c
version.c
//#####
//
// FILE:    version.c
//
// TITLE:   API to return the version number of the driverlib.lib in use.
//
//#####
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// $
//#####

./CycloneMCU_App/src/driverlib/version.h
version.h
//#####
//
// FILE:    version.h
//
// TITLE:   API to return the version number of the driverlib.lib in use.
//
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//#####

./CycloneMCU_App/src/driverlib/xbar.c
xbar.c
//#####
//
// FILE:    xbar.c
//
// TITLE:   C28x X-BAR driver.
//
//#####
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```

// $
//#####

./CycloneMCU_App/src/driverlib/xbar.h
xbar.h
//#####
//
// FILE:    xbar.h
//
// TITLE:   C28x X-BAR driver.
//
//#####
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//#####

./CycloneMCU_App/src/hardware/drv8350.h
drv8350.h
//#####
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//#####

./CycloneMCU_App/src/hardware/hal.h
hal.h
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// $
//#####

./CycloneMCU_App/src/hardware/hal_data.h
hal_data.h
//#####
//
// FILE:    hal_data.h
//
// TITLE:   C28x InstaSPIN hardware abstraction layer (HAL) library
//
//
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//#####
```

```
./CycloneMCU_App/src/hardware/hal_obj.h
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```

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//#####
```

```
./CycloneMCU_App/src/hardware/labs.h
labs.h
```

```
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//#####

./CycloneMCU_App/src/hardware/pwmdac.h
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//#####
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//#####

./CycloneMCU_App/src/hardware/user.h
user.h
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//#####

./CycloneMCU_App/src/libraries/angle_gen/include/angle_gen.h
angle_gen.h
//#####
// $TI Release: MotorControl SDK v2.00.00.00 $

```

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```
./CycloneMCU_App/src/libraries/angle_gen/source/angle_gen.c
angle_gen.c
```

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//#####

./CycloneMCU_App/src/libraries/clarke/include/clarke.h
clarke.h
//
// FILE:   clarke.h
//
// TITLE:  C28x Clarke transform library
//
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//#####

./CycloneMCU_App/src/libraries/clarke/source/clarke.c
clarke.c
//#####
//
// FILE:   clarke.c
//
// TITLE:  C28x Clarke transform library
//
//#####
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//#####

./CycloneMCU_App/src/libraries/cpu_time/include/cpu_time.h
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//#####

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// $
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// $
//#####

./CycloneMCU_App/src/libraries/ctrl/include/ctrl_states.h
ctrl_states.h
//#####
//
// FILE:   ctrl_states.h
//
// TITLE:  C28x InstaSPIN controller (CTRL) state library
//
//#####
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//#####

./CycloneMCU_App/src/libraries/ctrl/source/ctrl.c
ctrl.c
//#####
//
// FILE:   ctrl.c
//
// TITLE:  C28x InstaSPIN control (CTRL) library (floating point)
//
//#####
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./CycloneMCU_App/src/libraries/datalog/include/datalog.h
datalog.h
//#####
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// $
//#####

./CycloneMCU_App/src/libraries/datalog/include/dlog_4ch_f.h
dlog_4ch_f.h
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dlog_4ch_f.c
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//#####
```

```
./CycloneMCU_App/src/libraries/est/include/est.h
est.h
```

```
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//#####
```

```
./CycloneMCU_App/src/libraries/est/include/est_Flux_states.h
est_Flux_states.h
```

```
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./CycloneMCU_App/src/libraries/est/include/est_Ls_states.h
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//#####

./CycloneMCU_App/src/libraries/est/include/est_Rr_states.h
est_Rr_states.h
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```

```

./CycloneMCU_App/src/libraries/est/include/est_Rs_states.h
est_Rs_states.h
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//#####

```

```

./CycloneMCU_App/src/libraries/est/include/est_states.h
est_states.h
//#####
//
// FILE:   est.h
//
// TITLE:  C28x InstaSPIN estimator module states
//
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./CycloneMCU_App/src/libraries/est/include/est_Traj_states.h
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```

```
./CycloneMCU_App/src/libraries/est/include/motor.h
motor.h
```

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// $
//#####
```

```
./CycloneMCU_App/src/libraries/est/include/userParams.h
userParams.h
```

```
//#####
```

```

//
// FILE:    userParams.h
//
// TITLE:   C28x InstaSPIN public interface for user initialization data for the
//          CTRL, DRV, and EST modules (floating point)
//
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//#####

./CycloneMCU_App/src/libraries/est/source/user.c
user.c
//#####
//
// FILE:    user.c
//
// TITLE:   C28x InstaSPIN function for setting initialization data for the
//          CTRL, HAL, and EST modules (floating point)
//
//#####
// $TI Release: MotorControl SDK v2.00.00.00 $
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./CycloneMCU_App/src/libraries/filter/filter_fo/include/filter_fo.h
filter_fo.h
//#####
//
// FILE:   filter_fo.h
//
// TITLE:  C28x InstaSPIN filter library, first-order
//
//#####
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./CycloneMCU_App/src/libraries/filter/filter_fo/source/filter_fo.c
filter_fo.c
//#####
//
// FILE:    filter_fo.c
//
// TITLE:   C28x InstaSPIN filter library, first-order
//
//#####
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filter_so.h
//#####
//
// FILE:    filter_so.h
//
// TITLE:   C28x InstaSPIN filter library, second-order
//
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```

```

// $
//#####

./CycloneMCU_App/src/libraries/filter/filter_so/source/filter_so.c
filter_so.c
//#####
//
// FILE:    filter_so.c
//
// TITLE:   C28x InstaSPIN filter library, second-order
//
//#####
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// $
//#####

./CycloneMCU_App/src/libraries/filter/offset/include/offset.h
offset.h
//#####
//
// FILE:    offset.h
//
// TITLE:   C28x InstaSPIN offset library (floating point)

```

```

//
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// $
//#####

./CycloneMCU_App/src/libraries/filter/offset/source/offset.c
offset.c
//#####
//
// FILE:   offset.c
//
// TITLE:  C28x InstaSPIN offset library (floating point)
//
//#####
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// $
//#####

./CycloneMCU_App/src/libraries/fwc/include/fwc.h
fwc.h
//#####
//
// FILE:    fwc.h
//
// TITLE:   C28x Field Weakening Control (FWC) library (floating point)
//
//#####
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// $
//#####

./CycloneMCU_App/src/libraries/fwc/source/fwc.c
fwc.c
//#####
//
// FILE: fwc.c
//
// TITLE: C28x Field Weakening Control (FWC) library (floating point)
//
//#####
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//#####

./CycloneMCU_App/src/libraries/ipark/include/ipark.h
ipark.h
//#####
//
// FILE:    ipark.h
//
// TITLE:   C28x InstaSPIN inverse park transform library
//
//#####
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// $
//#####

./CycloneMCU_App/src/libraries/ipark/source/ipark.c
ipark.c
//#####

```

```

//
// FILE:    ipark.c
//
// TITLE:   C28x InstaSPIN inverse park transform library
//
//#####
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// $
//#####

./CycloneMCU_App/src/libraries/math/include/math.h
math.h
//#####
//
// FILE:    math.h
//
// TITLE:   C28x math library (floating point)
//
//#####
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// $
//#####

./CycloneMCU_App/src/libraries/mtpa/include/mtpa.h
mtpa.h
//#####
//
// FILE:   mtpa.h
//
// TITLE:  C28x Maximum torque per ampere (MTPA) (floating point)
//
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//#####

./CycloneMCU_App/src/libraries/mtpa/source/mtpa.c
mtpa.c
//#####
//
// FILE:    mtpa.c
//
// TITLE:   C28x Maximum Torque Per Ampere (MTPA)
//
//#####
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//#####

./CycloneMCU_App/src/libraries/park/include/park.h
park.h
//#####
//
// FILE:    park.h
//
// TITLE:   C28x InstaSPIN park transform library (fixed point)
//
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// $
//#####

```

```

./CycloneMCU_App/src/libraries/park/source/park.c
park.c
//#####
//
// FILE:    park.c
//
// TITLE:   C28x InstaSPIN park transform library (floating point)
//
//#####
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//#####

./CycloneMCU_App/src/libraries/pi/include/pi.h
pi.h
//#####
//
// FILE:    pi.h
//
// TITLE:   C28x InstaSPIN Proportional-Integral (PI) controller library
//          (floating point)
//

```



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// $  
//#####
```

```
./CycloneMCU_App/src/libraries/pi/source/pi.c  
pi.c
```

```
//#####  
//  
// FILE:    pi.c  
//  
// TITLE:   C28x InstaSPIN Proportional-Integral (PI) controller library  
//          (floating point)  
//  
//#####  
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//#####

./CycloneMCU_App/src/libraries/svggen/include/svggen.h
svggen.h
//#####
//
// FILE:    svggen.h
//
// TITLE:   C28x InstaSPIN Space Vector Generator (SVGEN) library
//          (floating point)
//
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//#####

./CycloneMCU_App/src/libraries/svggen/include/svggen_current.h
svggen_current.h
//#####
//
// FILE:    svggen_current.h
//
// TITLE:   C28x InstaSPIN Space Vector Generator (SVGEN) library
//          (floating point)
//
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// $
//#####

./CycloneMCU_App/src/libraries/svggen/source/svggen.c
svggen.c
//#####
//
// FILE:    svggen.c
//
// TITLE:   C28x InstaSPIN Space Vector Generator (SVGEN) library
//          (floating point)
//
//#####
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// $
//#####

```

```

./CycloneMCU_App/src/libraries/svgen/source/svgen_current.c
svgen_current.c
//#####
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// $
//#####

```

```

./CycloneMCU_App/src/libraries/traj/include/traj.h
traj.h
//#####
//
// FILE:   traj.h
//
// TITLE:  C28x InstaSPIN trajectory library (floating point)
//
//#####
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// $
//#####

./CycloneMCU_App/src/libraries/traj/source/traj.c
traj.c
//#####
//
// FILE:   traj.c
//
// TITLE:  C28x InstaSPIN trajectory library (floating point)
//
//#####
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// $
//#####

./CycloneMCU_App/src/libraries/types/include/types.h
types.h
//#####
//
// FILE:    types.h
//
// TITLE:   C28x type definitions library
//
//#####
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// $
//#####

./CycloneMCU_App/src/libraries/vsf/include/vsf.h
vsf.h
//#####
//
// FILE:    vsf.h
//
// TITLE:   varibale switching pwm frequency
//
//#####
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// $
//#####

./CycloneMCU_App/src/libraries/vsf/source/vsf.c
vsf.c

```



```

//#####
//
// FILE:   vsf.c
//
// TITLE:  variable switching frequency (VSF)
//
//#####
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//#####

./CycloneMCU_App/src/libraries/vs_freq/include/vs_freq.h
vs_freq.h
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// $
//#####

./CycloneMCU_App/src/libraries/vs_freq/source/vs_freq.c
vs_freq.c
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// $
//#####

./CycloneMCU_App/src/main.c
main.c
//#####
//main.c
// *****

./CycloneMCU_App/src/mcu_board/drv8350.c
drv8350.c
//#####
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// $
//#####

```

```

./CycloneMCU_App/src/mcu_board/hal.c
hal.c
//#####
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// $
//#####

```

```

./CycloneMCU_App/src/mcu_board/mcu_board_sci.h
mcu_board_sci.h

```

```

/*
 * mcu_board_sci.h
 *
 * Created on: 5 Mar 2020
 * Author: pwebb
 */

```

```

./CycloneMCU_App/src/mcu_board/rayHAL.c
rayHAL.c

```

```

/*
 * rayHAL.c
 *
 * Created on: 16 Dec 2019
 * Author: pwebb

```

```

*/

./CycloneMCU_App/src/mcu_board/rayHAL.h
rayHAL.h
/*
 * rayHAL.h
 *
 * Created on: 16 Dec 2019
 * Author: pwebb
 */

./CycloneMCU_App/src/mcu_board/RayMCUPinAllocations.h
RayMCUPinAllocations.h
/*
 * RayMCUPinAllocations.h
 *
 * Created on: 22 Nov 2019
 * Author: pwebb
 */

./CycloneMCU_App/src/mcu_board/RayMCUTypes.h
RayMCUTypes.h
/*
 * RayMCUTypes.h
 *
 * Created on: 22 Nov 2019
 * Author: pwebb
 */

./CycloneMCU_App/src/mcu_boost/boostClock.c
boostClock.c
/*
 * boostClock.c
 *
 * Created on: 16 Dec 2019
 * Author: pwebb
 */

./CycloneMCU_App/src/mcu_boost/boostClock.h
boostClock.h
/*
 * boostClock.h
 *
 * Created on: 16 Dec 2019
 * Author: pwebb
 */

./CycloneMCU_App/src/mcu_boost/boostEnable.c
boostEnable.c
/*
 * boostEnable.c
 *
 * Created on: 16 Dec 2019
 * Author: pwebb

```

```

*/

./CycloneMCU_App/src/mcu_boost/boostEnable.h
boostEnable.h
/*
 * boostEnable.h
 *
 * Created on: 16 Dec 2019
 * Author: pwebb
 */

./CycloneMCU_App/src/mcu_boost/boostSupply.c
boostSupply.c
/*
 * boostSupply.c
 *
 * Created on: 16 Dec 2019
 * Author: pwebb
 */

./CycloneMCU_App/src/mcu_boost/boostSupply.h
boostSupply.h
/*
 * boostSupply.h
 *
 * Created on: 16 Dec 2019
 * Author: pwebb
 */

./CycloneMCU_App/src/mcu_cli/mcu_cli.c
mcu_cli.c
/*
 * mcu_cli.c
 *
 * Created on: 4 Mar 2020
 * Author: pwebb
 */

./CycloneMCU_App/src/mcu_cli/mcu_cli.h
mcu_cli.h
/*
 * mcu_cli.h
 *
 * Created on: 4 Mar 2020
 * Author: pwebb
 */

./CycloneMCU_App/src/mcu_cli/mcu_cli_commands.h
mcu_cli_commands.h
/*
 * mcu_cli_commands.h
 *
 * Created on: 5 Mar 2020
 * Author: pwebb

```

```

*/

./CycloneMCU_App/src/mcu_cli/mcu_cli_parser.h
mcu_cli_parser.h
/*
 * mcu_cli_parser.h
 *
 * Created on: 5 Mar 2020
 * Author: pwebb
 */

./CycloneMCU_App/src/mcu_data/mcu_data_store.c
mcu_data_store.c
/*
 * mcu_data_read.c
 *
 * Created on: 5 Mar 2020
 * Author: pwebb
 */

./CycloneMCU_App/src/mcu_data/mcu_data_store.h
mcu_data_store.h
/*
 * mcu_data_store.h
 *
 * Created on: 5 Mar 2020
 * Author: pwebb
 */

./CycloneMCU_App/src/mcu_eeprom/mcu_eeprom.c
mcu_eeprom.c
/*
 * mcu_eeprom.c
 *
 * Created on: 9 Mar 2020
 * Author: pwebb
 */

./CycloneMCU_App/src/mcu_eeprom/mcu_eeprom.h
mcu_eeprom.h
/*
 * mcu_eeprom.h
 *
 * Created on: 9 Mar 2020
 * Author: pwebb
 */
//TI Board EEPROM DETAILS

./CycloneMCU_App/src/mcu_errorHandler/mcu_errorHandler.c
mcu_errorHandler.c
/*
 * mcu_led.c
 *
 * Created on: 10 Jul 2020

```

```

*      Author: pwebb
*/

./CycloneMCU_App/src/mcu_errorHandler/mcu_errorHandler.h
mcu_errorHandler.h
/*
* mcu_led.h
*
* Created on: 10 Jul 2020
*      Author: pwebb
*/

./CycloneMCU_App/src/mcu_I2C/mcu_i2c.c
mcu_i2c.c
/*
* mcu_i2c.c
*
* Created on: 25 Feb 2020
*      Author: pwebb
*/

./CycloneMCU_App/src/mcu_I2C/mcu_i2c.h
mcu_i2c.h
/*
* mcu_i2c.h
*
* Created on: 25 Feb 2020
*      Author: pwebb
*/

./CycloneMCU_App/src/mcu_I2C/mcu_i2c_data.h
mcu_i2c_data.h
/*
* mcu_i2c_data.h
*
* Created on: 2 Mar 2020
*      Author: pwebb
*/
//
// Defines
//

./CycloneMCU_App/src/mcu_I2C/mcu_i2c_int.h
mcu_i2c_int.h
/*
* mcu_i2c_int.h
*
* Created on: 2 Mar 2020
*      Author: pwebb
*/

./CycloneMCU_App/src/mcu_psuIF/crc16.c
crc16.c
/*

```



```

* crc16.c
*
* Created on: 21 Feb 2020
* Author: pwebb
*/

./CycloneMCU_App/src/mcu_psuIF/crc16.h
crc16.h
/*
* crc16.h
*
* Created on: 21 Feb 2020
* Author: pwebb
*/

./CycloneMCU_App/src/mcu_psuIF/mcu_psu_messages.c
mcu_psu_messages.c
/*
* mcu_psu_messages.c
*
* Created on: 11 Mar 2020
* Author: pwebb
*/

./CycloneMCU_App/src/mcu_psuIF/mcu_psu_messages.h
mcu_psu_messages.h
/*
* mcu_psu_messages.h
*
* Created on: 11 Mar 2020
* Author: pwebb
*/

./CycloneMCU_App/src/mcu_psuIF/psu_interface.c
psu_interface.c
/*
* psu_interface.c
*
* Created on: 2 Jan 2020
* Author: pwebb
*/

./CycloneMCU_App/src/mcu_psuIF/psu_interface.h
psu_interface.h
/*
* psu_interface.h
*
* Created on: 2 Jan 2020
* Author: pwebb
*/

./CycloneMCU_App/src/mcu_timer/timer.c
timer.c
/*

```

```

* timer.c
*
* Created on: 16 Dec 2019
* Author: pwebb
*/

./CycloneMCU_App/src/mcu_timer/timer.h
timer.h
/*
* timer.h
*
* Created on: 16 Dec 2019
* Author: pwebb
*/

./CycloneMCU_App/src/MotorDriver/MotorDriver.c
MotorDriver.c
/*
* MotorDriver.c
*/
//
// solutions
//

./CycloneMCU_App/src/MotorDriver/MotorDriver.h
MotorDriver.h
/*
* motorDriver.h
*
* Created on: 2 Jul 2019
* Author: pwebb
*/

./CycloneMCU_App/src/MotorDriver/MotorDriver_cntrl.h
MotorDriver_cntrl.h
/*
* MotorDriver.c
*/
//
// solutions
//

./CycloneMCU_App/src/MotorDriver/MotorDriver_HAL.h
MotorDriver_HAL.h
/*
* MotorDriver_HAL.h
*
* Created on: 4 Mar 2020
* Author: pwebb
*/

./CycloneMCU_App/src/MotorDriver/MotorDriver_POS.h
MotorDriver_POS.h
/*

```

```

* motorDriver_POS.h
*
* Created on: 2 Jul 2019
* Author: pwebb
*/

./CycloneMCU_App/src/MotorDriver/MotorDriver_QEP.h
MotorDriver_QEP.h
/*
* motorDriver_QEP.h
*
* Created on: 2 Jul 2019
* Author: pwebb
*/
/**
* structure holding information about the quadrature encoder interface
*/

./CycloneMCU_App/src/MotorDriver/MotorDriver_Variables.h
MotorDriver_Variables.h
/*
* motorDriver.h
*
* Created on: 2 Jul 2019
* Author: pwebb
*/

./CycloneMCU_App/src/src_device/f28004x_globalvariabledefs.c
f28004x_globalvariabledefs.c
//#####
//
// FILE: f28004x_globalvariabledefs.c
//
// TITLE: F28004x Global Variables and Data Section Pragmas.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/device.c
device.c
//#####
//
// FILE:   device.c
//
// TITLE:  Device setup for examples.
//
//#####
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//#####

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device.h
//#####
//
// FILE:    device.h
//
// TITLE:   Device setup for examples.
//
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/adc.h
adc.h
//#####

```

```

//
// FILE:   adc.h
//
// TITLE:  C28x ADC driver.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/asysctl.h
asysctl.h
//#####
//
// FILE:   asysctl.h
//
// TITLE:  C28x driver for Analog System Control.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/can.h
can.h
//#####
//
// FILE:   can.h
//
// TITLE:  C28x CAN driver.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/cla.h
cla.h
//#####
//
// FILE:   cla.h
//
// TITLE:  CLA Driver Implementation File
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/clapromcrc.h
clapromcrc.h
//#####
//
// FILE:    clapromcrc.h
//
// TITLE:   C28x and CLA CLAPROMCRC driver.
//
//#####
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// $
//#####

```

```

./CycloneMCU_UpgradeKernel/src/device/driverlib/cmpss.h
cmpss.h
//#####
//
// FILE:    cmpss.h
//
// TITLE:   C28x CMPSS driver.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/cpu.h
cpu.h
//#####
//
// FILE:    cpu.h
//
// TITLE:   Useful C28x CPU defines.
//
//#####

```

```

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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/cputimer.h
cputimer.h
//#####
//
// FILE:    cputimer.h
//
// TITLE:    C28x CPU timer Driver
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/dac.h
dac.h
//#####
//
// FILE:    dac.h
//
// TITLE:   C28x DAC driver.
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/dcc.h
dcc.h
//#####
//
// FILE:    dcc.h
//
// TITLE:   C28x DCC driver.
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/dcsm.h
dcsm.h
//#####
//
// FILE:    dcsm.h
//
// TITLE:   C28x Driver for the DCSM security module.
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/debug.h
debug.h
//#####
//
// FILE:    debug.h

```

```

//
// TITLE: Assert definition macro for debug.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/dma.h
dma.h
//#####
//
// FILE: dma.h
//
// TITLE: C28x DMA driver.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/ecap.h
ecap.h
//#####
//
// FILE: ecap.h
//
// TITLE: C28x ECAP driver
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/epwm.h
epwm.h
//#####
//
// FILE:    epwm.h
//
// TITLE:    C28x EPWM Driver
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/eqep.h
eqep.h
//#####
//
// FILE:    eqep.h
//
// TITLE:   C28x eQEP driver.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/flash.h

```

```

flash.h
//#####
//
// FILE:    flash.h
//
// TITLE:   C28x Flash driver.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/fsi.h
fsi.h
//#####
//
// FILE:    fsi.h
//
// TITLE:   C28x FSI Driver API header file
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/gpio.h
gpio.h
//#####
//
// FILE:    gpio.h
//
// TITLE:   C28x GPIO driver.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/hrcap.h
hrcap.h
//#####
//
// FILE: hrcap.h
//
// TITLE: C28x HRCAP Driver.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/hrpwm.h
hrpwm.h
//#####
//
// FILE:    hrpwm.h
//
// TITLE:    C28x HRPWM Driver
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/i2c.h
i2c.h
//#####
//
// FILE:    i2c.h
//
// TITLE:   C28x I2C driver.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_adc.h
hw_adc.h
//#####
//
// FILE:    hw_adc.h
//
// TITLE:   Definitions for the ADC registers.

```

```

//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_asysctl.h
hw_asysctl.h
//#####
//
// FILE:    hw_asysctl.h
//
// TITLE:   Definitions for the ASYSCTL registers.
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_can.h
hw_can.h
//#####
//
// FILE:    hw_can.h
//
// TITLE:   Definitions for the CAN registers.
//
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_cla.h
hw_cla.h
//#####
//
// FILE:    hw_cla.h
//
// TITLE:   Definitions for the CLA registers.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_clapromcrc.h
hw_clapromcrc.h
//#####
//
// FILE:      hw_clapromcrc.h
//
// TITLE:     Definitions for the CLAPROMCRC registers.
//
//#####
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// $
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./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_cmpss.h
hw_cmpss.h
//#####

```

```

//
// FILE:    hw_cmpss.h
//
// TITLE:   Definitions for the CMPSS registers.
//
//#####
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./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_cputimer.h
hw_cputimer.h
//#####
//
// FILE:    hw_cputimer.h
//
// TITLE:   Definitions for the CPUTIMER registers.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_dac.h
hw_dac.h
//#####
//
// FILE:    hw_dac.h
//
// TITLE:   Definitions for the DAC registers.
//
//#####
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./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_dcc.h
hw_dcc.h
//#####
//
// FILE:    hw_dcc.h
//
// TITLE:   Definitions for the DCC registers.
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_dcsn.h
hw_dcsn.h
//#####
//
// FILE:    hw_dcsn.h
//
// TITLE:   Definitions for the DCSM registers.
//
//#####
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// $
//#####

```

```

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_dma.h
hw_dma.h
//#####
//
// FILE:    hw_dma.h
//
// TITLE:    Definitions for the DMA registers.
//
//#####
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//#####

```

```

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_ecap.h
hw_ecap.h
//#####
//
// FILE:    hw_ecap.h
//
// TITLE:    Definitions for the ECAP registers.
//
//#####

```



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./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_epwm.h
hw_epwm.h
//#####
//
// FILE:      hw_epwm.h
//
// TITLE:     Definitions for the EPWM registers.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_epwmxbar.h
hw_epwmxbar.h
//#####
//
// FILE:    hw_epwmxbar.h
//
// TITLE:   Definitions for the XBAR registers.
//
//#####
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./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_eqep.h
hw_eqep.h
//#####
//
// FILE:      hw_eqep.h
//
// TITLE:     Definitions for the EQEP registers.
//
//#####
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./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_erad.h
hw_erad.h
//#####
//
// FILE:    hw_erad.h
//
// TITLE:   Definitions for the ERAD registers.
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_flash.h
hw_flash.h
//#####
//
// FILE:    hw_flash.h

```

```

//
// TITLE:   Definitions for the FLASH registers.
//
//#####
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./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_fsi.h
hw_fsi.h
//#####
//
// FILE:   hw_fsi.h
//
// TITLE:   Definitions for the FSI registers.
//
//#####
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./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_gpio.h
hw_gpio.h
//#####
//
// FILE:    hw_gpio.h
//
// TITLE:   Definitions for the GPIO registers.
//
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./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_hrcap.h
hw_hrcap.h
//#####
//
// FILE: hw_hrcap.h
//
// TITLE: Definitions for the HRCAP registers.
//
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./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_hrpwm.h
hw_hrpwm.h
//#####
//
// FILE:    hw_hrpwm.h
//
// TITLE:    Definitions for the HRPWM registers.
//
//#####
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./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_i2c.h

```



```

hw_i2c.h
//#####
//
// FILE:    hw_i2c.h
//
// TITLE:   Definitions for the I2C registers.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_inputxbar.h
hw_inputxbar.h
//#####
//
// FILE:    hw_inputxbar.h
//
// TITLE:   Definitions for the XBAR registers.
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $
// $Release Date: Mon May 27 06:42:25 CDT 2019 $

```

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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_ints.h
hw_ints.h
//#####
//
// FILE:    hw_ints.h
//
// TITLE:   Definitions of interrupt numbers for use with interrupt.c.
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $
// $Release Date: Mon May 27 06:42:25 CDT 2019 $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_lin.h
hw_lin.h
//#####
//
// FILE:    hw_lin.h
//
// TITLE:   Definitions for the LIN registers.
//

```

```
//#####  
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// $  
//#####
```

```
./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_memcfg.h  
hw_memcfg.h
```

```
//#####  
//  
// FILE:    hw_memcfg.h  
//  
// TITLE:    Definitions for the MEMCFG registers.  
//  
//#####  
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_memmap.h
hw_memmap.h
//#####
//
// FILE:    hw_memmap.h
//
// TITLE:   Macros defining the memory map of the C28x.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_nmi.h
hw_nmi.h
//#####
//
// FILE:      hw_nmi.h
//
// TITLE:     Definitions for the NMI registers.
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_outputxbar.h
hw_outputxbar.h
//#####
//
// FILE:    hw_outputxbar.h
//
// TITLE:   Definitions for the XBAR registers.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_pga.h
hw_pga.h
//#####
//

```

```

// FILE:    hw_pga.h
//
// TITLE:    Definitions for the PGA registers.
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_pie.h
hw_pie.h
//#####
//
// FILE:    hw_pie.h
//
// TITLE:    Definitions for the PIE registers.
//
//#####
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./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_pmbus.h
hw_pmbus.h
//#####
//
// FILE:      hw_pmbus.h
//
// TITLE:     Definitions for the PMBUS registers.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_sci.h
hw_sci.h
//#####
//
// FILE:    hw_sci.h
//
// TITLE:   Definitions for the SCI registers.
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_sdfm.h
hw_sdfm.h
//#####
//
// FILE:    hw_sdfm.h
//
// TITLE:   Definitions for the SDFM registers.
//
//#####
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//#####

```

```

./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_spi.h
hw_spi.h
//#####
//
// FILE:    hw_spi.h
//
// TITLE:   Definitions for the SPI registers.
//
//#####
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./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_sysctl.h
hw_sysctl.h
//#####
//
// FILE:    hw_sysctl.h
//
// TITLE:   Definitions for the SYSCTL registers.
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $

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./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_types.h
hw_types.h
//#####
//
// FILE:   hw_types.h
//
// TITLE:  Type definitions used in driverlib functions.
//
//#####
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./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_xbar.h
hw_xbar.h
//#####
//
// FILE:    hw_xbar.h
//
// TITLE:   Definitions for the XBAR registers.
//
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./CycloneMCU_UpgradeKernel/src/device/driverlib/inc/hw_xint.h
hw_xint.h
//#####
//
// FILE:    hw_xint.h
//
// TITLE:   Definitions for the XINT registers.
//
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/interrupt.h
interrupt.h
//#####
//
// FILE:    interrupt.h
//
// TITLE:   C28x Interrupt (PIE) driver.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/lin.h
lin.h
//#####
//
// FILE:    lin.h
//

```

```

// TITLE: C28x LIN driver.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/memcfg.h
memcfg.h
//#####
//
// FILE: memcfg.h
//
// TITLE: C28x RAM config driver.
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/pga.h
pga.h
//#####
//
// FILE:   pga.h
//
// TITLE:  C28x PGA driver.
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/pin_map.h
pin_map.h
//#####
//
// FILE:   pin_map.h
//
// TITLE:  Definitions of pin mux info for gpio.c.
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/pmbus.h
pmbus.h
//#####
//
// FILE:    pmbus.h
//
// TITLE:   C28x PMBUS Driver
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/sci.h
sci.h

```

```

//#####
//
// FILE:    sci.h
//
// TITLE:   C28x SCI driver.
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/sdfm.h
sdfm.h
//#####
//
// FILE:    sdfm.h
//
// TITLE:   C28x SDFM Driver
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/spi.h
spi.h
//#####
//
// FILE:    spi.h
//
// TITLE:   C28x SPI driver.
//
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/sysctl.h
sysctl.h
//#####
//
// FILE:    sysctl.h
//
// TITLE:   C28x system control driver.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/version.h
version.h
//#####
//
// FILE:    version.h
//
// TITLE:   API to return the version number of the driverlib.lib in use.
//
//#####
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```

//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib/xbar.h
xbar.h
//#####
//
// FILE:    xbar.h
//
// TITLE:   C28x X-BAR driver.
//
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// $
//#####

./CycloneMCU_UpgradeKernel/src/device/driverlib.h
driverlib.h
//#####
//
// FILE:    driverlib.h
//
// TITLE:   C28x Driverlib Header File
//

```



```

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//#####

```

./CycloneMCU\_UpgradeKernel/src/device/flash\_api/f28004x/include/FlashAPI/Constants/Constants.h

Constants.h

```

/*-----*/
/* F021\Constants\Constants.h */
/* F021 Flash API v1.56 */
/*-----*/
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./CycloneMCU_UpgradeKernel/src/device/flash_api/f28004x/include/FlashAPI/Constants/F28004x.h
F28004x.h
/*-----*/
/* F021\Constants\F28004x.h */
/* F021 Flash API v1.56 */
/*-----*/
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// $
```

```
./CycloneMCU_UpgradeKernel/src/device/flash_api/f28004x/include/FlashAPI/F021.h
F021.h
```

```
/*-----*/
/* F021\F021.h */
/* F021 Flash API v1.56 */
/*-----*/
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// $
```

```
./CycloneMCU_UpgradeKernel/src/device/flash_api/f28004x/include/FlashAPI/F021_F2
8004x_C28x.h
F021_F28004x_C28x.h
```

```
/*-----*/
```

```

/* F021\F021_F28004x_C28x.h */
/* F021 Flash API v1.56 */
/*-----*/
// $TI Release: F28004x Support Library v1.06.00.00 $
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// $

```

```

./CycloneMCU_UpgradeKernel/src/device/flash_api/f28004x/include/FlashAPI/Init.h
Init.h

```

```

/*-----*/
/* F021\Init.h */
/* F021 Flash API v1.56 */
/*-----*/
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// $

./CycloneMCU_UpgradeKernel/src/device/flash_api/f28004x/include/FlashAPI/Registers.h
Registers.h
/*-----*/
/* F021\Registers.h */
/* F021 Flash API v1.56 */
/*-----*/
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// $
```

```
./CycloneMCU_UpgradeKernel/src/device/flash_api/f28004x/include/FlashAPI/Registe
rs_C28x.h
```

```
Registers_C28x.h
```

```
/*-----*/
/* F021\Registers_C28x.h */
/* F021 Flash API v1.56 */
/*-----*/
// $TI Release: F28004x Support Library v1.06.00.00 $
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// $
```

```
./CycloneMCU_UpgradeKernel/src/device/flash_api/f28004x/include/FlashAPI/Types.h
Types.h
```

```
/*-----*/
```

```

/* F02\Types.h */
/* F021 Flash API v1.56 */
/*-----*/
// $TI Release: F28004x Support Library v1.06.00.00 $
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// $

./CycloneMCU_UpgradeKernel/src/flashapi_ex2_boot.c
flashapi_ex2_boot.c
//#####
//
// FILE:    flashapi_ex2_boot.c
//
// TITLE:   Boot Source Code
//
// Functions:
//
// void    copyData(void)
// uint32_t getLongData(void)
// void    readReservedFn(void)
//
//#####
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```

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// $
//#####

./CycloneMCU_UpgradeKernel/src/flashapi_ex2_bootrom.h
flashapi_ex2_bootrom.h
//#####
//
// FILE:   flashapi_ex2_bootrom.h
//
// TITLE:  BootROM Definitions.
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/flashapi_ex2_commands.h
flashapi_ex2_commands.h
//#####
//
// FILE:    flashapi_ex2_commands.h
//
// TITLE:    SCI Kernel Commands
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/flashapi_ex2_erase.c
flashapi_ex2_erase.c
//#####
//
// FILE:    flashapi_ex2_erase.c
//
// TITLE:    Erase Source Code
//
// Functions:
//
//     void sharedErase(uint16_t command, uint32_t sectors)
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/flashapi_ex2_erase.h
flashapi_ex2_erase.h
//#####
//
// FILE:    flashapi_ex2_erase.h
//
// TITLE:    Erase Header
//
// Functions:
//
//     void sciErase(Uint32 sectors)
//
//#####
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// $
//#####

```

```

./CycloneMCU_UpgradeKernel/src/flashapi_ex2_flash_programming_c28.h
flashapi_ex2_flash_programming_c28.h
//#####
//
// FILE:    flashapi_ex2_flash_programming_C28.h
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/flashapi_ex2_ldfu.h
flashapi_ex2_ldfu.h
//#####
//
// FILE:    flashapi_ex2_LDFU.h
//
// TITLE:    Live DFU Header
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/flashapi_ex2_sci_boot.c
flashapi_ex2_sci_boot.c
//#####
//
// FILE:    flashapi_ex2_sci_boot.c
//
// TITLE:    SCI Boot Mode Routines
//
// Functions:
//
//   uint32_t sciBoot(void)
//   void sciaInit(void)
//   uint32_t sciaGetWordData(void)
//
// Notes:
//
//#####
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//#####

./CycloneMCU_UpgradeKernel/src/flashapi_ex2_sci_get_function.c
flashapi_ex2_sci_get_function.c
//#####
//
// FILE:    flashapi_ex2_sci_get_function.c
//
// TITLE:   SCI Kernel Routines
//
// Functions:
//
//     uint32_t sciBoot(void)
//     void sciaInit(void)
//     uint32_t sciaGetWordData(void)
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $
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// $
//#####

./CycloneMCU_UpgradeKernel/src/flashapi_ex2_sci_kernel.c
flashapi_ex2_sci_kernel.c
//#####
//
// FILE:   flashapi_ex2_sciKernel.c
//
// TITLE:  SCI Flash Kernel Example for F28004x
//
//!! \addtogroup cpu01_example_list
//!! <h1>SCI Flash Kernel Example for F28004x</h1>
//!!
//!! In this example, we set up a UART connection with a host using SCI, receive
//!! commands for CPU1 to perform which then sends ACK, NAK, and status packets
//!! back to the host after receiving and completing the tasks. Each command
//!! either expects no data from the command packet or specific data relative
//!! to the command.
//!!
//!! The example has seven build configurations, listed below.
//!!           BANK0_LDFU: Only supports the Live Device Firmware Update (LDFU)
//!!                        command for Bank 0; other commands are not supported
//!!                        by this build configuration. For more information about
//!!                        the LDFU command functionality, please read the
documentation
//!!                        in 'flashapi_ex2_LDFU.c'. Links the flash kernel to Bank
0
//!!                        (0x80000 - 0x82007 will be reserved). Uses Flash API
symbols

```

```

//!                in flash.
//!
//!                BANK0_LDFU_ROM: Only supports the Live Device Firmware Update (LDFU)
//!                command for Bank 0; other commands are not supported
//!                by this build configuration. For more information
about
//!                the LDFU command functionality, please read the
documentation
//!                in 'flashapi_ex2_LDFU.c'. Links the flash kernel to
Bank 0
//!                (0x80000 - 0x82007 will be reserved). Uses Flash API
symbols
//!                in ROM; Rev A of F28004x cannot be used with this
build
//!                configuration.
//!
//!                BANK0_NO_LDFU: Supports all commands except the LDFU command. Links
the
//!                flash kernel to Bank 0. Uses Flash API symbols in
flash.
//!                Commands must only be used to manipulate flash in
Bank 1.
//!
//!                BANK1_LDFU: Only supports the Live Device Firmware Update (LDFU)
//!                command for Bank 1; other commands are not supported
//!                by this build configuration. For more information about
//!                the LDFU command functionality, please read the
documentation
//!                in 'flashapi_ex2_LDFU.c'. Links the flash kernel to Bank
1
//!                (0x90000 - 0x92007 will be reserved).Uses Flash API
symbols in
//!                flash.
//!
//!                BANK1_LDFU_ROM: Only supports the Live Device Firmware Update (LDFU)
//!                command for Bank 1; other commands are not supported
//!                by this build configuration. For more information
about
//!                the command functionality, please read the
documentation
//!                in 'flashapi_ex2_LDFU.c'. Links the flash kernel to
Bank 1.
//!                (0x90000 - 0x92007 will be reserved) Uses Flash API
symbols in ROM; Rev A of F28004x cannot be used with
this
//!                build configuration.
//!
//!                BANK1_NO_LDFU: Supports all commands except the LDFU command. Links
the
//!                flash kernel to Bank 1. Uses Flash API symbols in
flash.
//!                Commands must only be used to manipulate flash in
Bank 0.

```



```

//!
//!          CPU1_RAM: Supports all commands except the LDFU command. Links the
flash
//!          kernel to RAM.
//!
//! This example is to be used with the Serial Flash Programmer. Applications
loaded to
//! flash through commands of the Serial Flash Programmer must be in the '--boot
--sci8
//! --ascii' hex format, which can be achieved by using the hex utility tool.
//!
//! \b External \b Connections \n
//! - Connect SCI Rx and SCI Tx to a COM port of the computer
//! running the Serial Flash Programmer project
//!
//! \b Watch \b Variables \n
//! - None
//!
//
//#####
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// $Release Date: Mon May 27 06:42:25 CDT 2019 $
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./CycloneMCU_UpgradeKernel/src/flashapi_ex2_verify.c
flashapi_ex2_verify.c
//#####
//
// FILE:    flashapi_ex2_verify.c
//
// TITLE:    Verify Source Code

```

```

//
//
//#####
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// $
//#####

./CycloneMCU_UpgradeKernel/src/flashapi_ex2_verify.h
flashapi_ex2_verify.h
//#####
//
// FILE:    flashapi_ex2_verify.h
//
// TITLE:   Verify Header
//
// Functions:
//
//     void verifyData()
//
//#####
// $TI Release: F28004x Support Library v1.06.00.00 $
// $Release Date: Mon May 27 06:42:25 CDT 2019 $
// $Copyright:

```

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// $
//#####
```

```
./Inc/Legacy/stm32_hal_legacy.h
stm32_hal_legacy.h
```

```
/**
*****
* @file      stm32_hal_legacy.h
* @author    MCD Application Team
* @brief     This file contains aliases definition for the STM32Cube HAL
constants
*           macros and functions maintained for legacy purpose.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h745xx.h

stm32h745xx.h

```
/**
*****
* @file      stm32h745xx.h
* @author    MCD Application Team
* @brief     CMSIS STM32H745xx Device Peripheral Access Layer Header File.
*
*           This file contains:
*           - Data structures and the address mapping for all peripherals
*           - Peripheral's registers declarations and bits definition
*           - Macros to access peripheral's registers hardware
*
*****
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*
*****
*/
```

./Inc/stm32h7xx.h

stm32h7xx.h

```
/**
*****
* @file      stm32h7xx.h
* @author    MCD Application Team
* @brief     CMSIS STM32H7xx Device Peripheral Access Layer Header File.
*
*           The file is the unique include file that the application programmer
*           is using in the C source code, usually in main.c. This file
contains:
*           - Configuration section that allows to select:
*           - The STM32H7xx device used in the target application
*           - To use or not the peripheral's drivers in application
code(i.e.
*           code will be based on direct access to peripheral's registers
*           rather than drivers API), this option is controlled by
*           "#define USE_HAL_DRIVER"
*
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*
*****
*/
```

./Inc/stm32h7xx\_hal.h

stm32h7xx\_hal.h

```
/**
*****
* @file      stm32h7xx_hal.h
* @author    MCD Application Team
* @brief     This file contains all the functions prototypes for the HAL
*            module driver.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_adc.h

stm32h7xx\_hal\_adc.h

```
/**
*****
* @file      stm32h7xx_hal_adc.h
* @author    MCD Application Team
* @brief     Header file of ADC HAL module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_adc\_ex.h

stm32h7xx\_hal\_adc\_ex.h

```
/**
*****
```

```

* @file    stm32h7xx_hal_adc_ex.h
* @author  MCD Application Team
* @brief   Header file of ADC HAL extended module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_cec.h
stm32h7xx_hal_cec.h
/**
*****
* @file    stm32h7xx_hal_cec.h
* @author  MCD Application Team
* @brief   Header file of CEC HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_comp.h
stm32h7xx_hal_comp.h
/**
*****
* @file    stm32h7xx_hal_comp.h
* @author  MCD Application Team
* @brief   Header file of COMP HAL module.
*****
* @attention
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```

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*****
*/

./Inc/stm32h7xx_hal_conf.h
stm32h7xx_hal_conf.h
/**
*****
* @file      stm32h7xx_hal_conf.h
* @author    MCD Application Team
* @brief     HAL configuration file.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_cortex.h
stm32h7xx_hal_cortex.h
/**
*****
* @file      stm32h7xx_hal_cortex.h
* @author    MCD Application Team
* @brief     Header file of CORTEX HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_crc.h
stm32h7xx_hal_crc.h
/**
*****
* @file      stm32h7xx_hal_crc.h
* @author    MCD Application Team
* @brief     Header file of CRC HAL module.

```

```

*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_crc_ex.h
stm32h7xx_hal_crc_ex.h
/**
*****
* @file      stm32h7xx_hal_crc_ex.h
* @author    MCD Application Team
* @brief     Header file of CRC HAL extended module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_cryp.h
stm32h7xx_hal_cryp.h
/**
*****
* @file      stm32h7xx_hal_cryp.h
* @author    MCD Application Team
* @brief     Header file of CRYP HAL module.
*****
* @attention
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*
*****

```



```

*/

./Inc/stm32h7xx_hal_cryp_ex.h
stm32h7xx_hal_cryp_ex.h
/**
*****
* @file      stm32h7xx_hal_cryp_ex.h
* @author    MCD Application Team
* @brief     Header file of CRYP HAL Extension module.
*****
* @attention
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*****
*/

```

```

./Inc/stm32h7xx_hal_dac.h
stm32h7xx_hal_dac.h
/**
*****
* @file      stm32h7xx_hal_dac.h
* @author    MCD Application Team
* @brief     Header file of DAC HAL module.
*****
* @attention
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*****
*/

```

```

./Inc/stm32h7xx_hal_dac_ex.h
stm32h7xx_hal_dac_ex.h
/**
*****
* @file      stm32h7xx_hal_dac_ex.h
* @author    MCD Application Team
* @brief     Header file of DAC HAL Extended module.
*****
* @attention
*

```

```

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*
*****
*/

./Inc/stm32h7xx_hal_dcmi.h
stm32h7xx_hal_dcmi.h
/**
*****
* @file      stm32h7xx_hal_dcmi.h
* @author    MCD Application Team
* @brief     Header file of DCMI HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_def.h
stm32h7xx_hal_def.h
/**
*****
* @file      stm32h7xx_hal_def.h
* @author    MCD Application Team
* @brief     This file contains HAL common defines, enumeration, macros and
*             structures definitions.
*****
* @attention
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*
*****
*/

```

```
./Inc/stm32h7xx_hal_dfstdm.h
stm32h7xx_hal_dfstdm.h
```

```
/**
*****
* @file      stm32h7xx_hal_dfstdm.h
* @author    MCD Application Team
* @brief     Header file of DFSDM HAL module.
*****
* @attention
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*
*****
*/
```

```
./Inc/stm32h7xx_hal_dfstdm_ex.h
stm32h7xx_hal_dfstdm_ex.h
```

```
/**
*****
* @file      stm32h7xx_hal_dfstdm_ex.h
* @author    MCD Application Team
* @brief     Header file of DFSDM HAL extended module.
*****
* @attention
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*
*****
*/
```

```
./Inc/stm32h7xx_hal_dma.h
stm32h7xx_hal_dma.h
```

```
/**
*****
* @file      stm32h7xx_hal_dma.h
* @author    MCD Application Team
* @brief     Header file of DMA HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_dma2d.h
stm32h7xx_hal_dma2d.h
/**
*****
* @file      stm32h7xx_hal_dma2d.h
* @author    MCD Application Team
* @brief    Header file of DMA2D HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_dma_ex.h
stm32h7xx_hal_dma_ex.h
/**
*****
* @file      stm32h7xx_hal_dma_ex.h
* @author    MCD Application Team
* @brief    Header file of DMA HAL extension module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_dsi.h
stm32h7xx_hal_dsi.h
/**

```

```

*****
* @file      stm32h7xx_hal_dsi.h
* @author    MCD Application Team
* @brief     Header file of DSI HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_dts.h
stm32h7xx_hal_dts.h
/**
*****
* @file      stm32h7xx_hal_dts.h
* @author    MCD Application Team
* @brief     Header file of DTS HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_eth.h
stm32h7xx_hal_eth.h
/**
*****
* @file      stm32h7xx_hal_eth.h
* @author    MCD Application Team
* @brief     Header file of ETH HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_eth_ex.h
stm32h7xx_hal_eth_ex.h
/**
*****
* @file      stm32h7xx_hal_eth_ex.h
* @author    MCD Application Team
* @brief     Header file of ETH HAL Extended module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_exti.h
stm32h7xx_hal_exti.h
/**
*****
* @file      stm32h7xx_hal_exti.h
* @author    MCD Application Team
* @brief     Header file of EXTI HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_fdcan.h
stm32h7xx_hal_fdcan.h
/**
*****
* @file      stm32h7xx_hal_fdcan.h
* @author    MCD Application Team

```

```

* @brief   Header file of FDCAN HAL module.
*****
* @attention
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*
*****
*/

```

```

./Inc/stm32h7xx_hal_flash.h
stm32h7xx_hal_flash.h

```

```

/**
*****
* @file     stm32h7xx_hal_flash.h
* @author   MCD Application Team
* @brief    Header file of FLASH HAL module.
*****
* @attention
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*
*****
*/

```

```

./Inc/stm32h7xx_hal_flash_ex.h
stm32h7xx_hal_flash_ex.h

```

```

/**
*****
* @file     stm32H7xx_hal_flash_ex.h
* @author   MCD Application Team
* @brief    Header file of FLASH HAL module.
*****
* @attention
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*

```

```

*****
*/

./Inc/stm32h7xx_hal_gfxmmu.h
stm32h7xx_hal_gfxmmu.h
/**
*****
* @file      stm32h7xx_hal_gfxmmu.h
* @author    MCD Application Team
* @brief     Header file of GFXMMU HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_gpio.h
stm32h7xx_hal_gpio.h
/**
*****
* @file      stm32h7xx_hal_gpio.h
* @author    MCD Application Team
* @brief     Header file of GPIO HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_gpio_ex.h
stm32h7xx_hal_gpio_ex.h
/**
*****
* @file      stm32h7xx_hal_gpio_ex.h
* @author    MCD Application Team
* @brief     Header file of GPIO HAL Extension module.
*****
* @attention

```



```

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*
*****
*/

./Inc/stm32h7xx_hal_hash.h
stm32h7xx_hal_hash.h
/**
*****
* @file      stm32h7xx_hal_hash.h
* @author    MCD Application Team
* @brief     Header file of HASH HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_hash_ex.h
stm32h7xx_hal_hash_ex.h
/**
*****
* @file      stm32h7xx_hal_hash_ex.h
* @author    MCD Application Team
* @brief     Header file of HASH HAL module.
*****
* @attention
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*
*****
*/

```

./Inc/stm32h7xx\_hal\_hcd.h  
stm32h7xx\_hal\_hcd.h

```
/**
*****
* @file      stm32h7xx_hal_hcd.h
* @author    MCD Application Team
* @brief     Header file of HCD HAL module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_hrtim.h  
stm32h7xx\_hal\_hrtim.h

```
/**
*****
* @file      stm32h7xx_hal_hrtim.h
* @author    MCD Application Team
* @brief     Header file of HRTIM HAL module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_hsem.h  
stm32h7xx\_hal\_hsem.h

```
/**
*****
* @file      stm32h7xx_hal_hsem.h
* @author    MCD Application Team
* @brief     Header file of HSEM HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_i2c.h
stm32h7xx_hal_i2c.h
/**
*****
* @file      stm32h7xx_hal_i2c.h
* @author    MCD Application Team
* @brief     Header file of I2C HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_i2c_ex.h
stm32h7xx_hal_i2c_ex.h
/**
*****
* @file      stm32h7xx_hal_i2c_ex.h
* @author    MCD Application Team
* @brief     Header file of I2C HAL Extended module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_i2s.h
stm32h7xx_hal_i2s.h
/**

```

```

*****
* @file      stm32h7xx_hal_i2s.h
* @author    MCD Application Team
* @brief     Header file of I2S HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_i2s_ex.h
stm32h7xx_hal_i2s_ex.h
/**
*****
* @file      stm32h7xx_hal_i2s_ex.h
* @author    MCD Application Team
* @brief     Header file of I2S HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_irda.h
stm32h7xx_hal_irda.h
/**
*****
* @file      stm32h7xx_hal_irda.h
* @author    MCD Application Team
* @brief     Header file of IRDA HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_irda_ex.h
stm32h7xx_hal_irda_ex.h
/**
*****
* @file      stm32h7xx_hal_irda_ex.h
* @author    MCD Application Team
* @brief     Header file of IRDA HAL Extended module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_iwdg.h
stm32h7xx_hal_iwdg.h
/**
*****
* @file      stm32h7xx_hal_iwdg.h
* @author    MCD Application Team
* @brief     Header file of IWDG HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_jpeg.h
stm32h7xx_hal_jpeg.h
/**
*****
* @file      stm32h7xx_hal_jpeg.h
* @author    MCD Application Team

```

```

* @brief   Header file of JPEG HAL module.
*****
* @attention
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*****
*/

```

```

./Inc/stm32h7xx_hal_lptim.h
stm32h7xx_hal_lptim.h

```

```

/**
*****
* @file     stm32h7xx_hal_lptim.h
* @author    MCD Application Team
* @brief     Header file of LPTIM HAL module.
*****
* @attention
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*****
*/

```

```

./Inc/stm32h7xx_hal_ltdc.h
stm32h7xx_hal_ltdc.h

```

```

/**
*****
* @file     stm32h7xx_hal_ltdc.h
* @author    MCD Application Team
* @brief     Header file of LTDC HAL module.
*****
* @attention
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```

```

*****
*/

./Inc/stm32h7xx_hal_ltdc_ex.h
stm32h7xx_hal_ltdc_ex.h
/**
*****
* @file      stm32h7xx_hal_ltdc_ex.h
* @author    MCD Application Team
* @brief     Header file of LTDC HAL Extension module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_mdios.h
stm32h7xx_hal_mdios.h
/**
*****
* @file      stm32h7xx_hal_mdios.h
* @author    MCD Application Team
* @brief     Header file of MDIOS HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_mdma.h
stm32h7xx_hal_mdma.h
/**
*****
* @file      stm32h7xx_hal_mdma.h
* @author    MCD Application Team
* @brief     Header file of DMA HAL module.
*****
* @attention

```

```

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*
*****
*/

./Inc/stm32h7xx_hal_mmc.h
stm32h7xx_hal_mmc.h
/**
*****
* @file      stm32h7xx_hal_mmc.h
* @author    MCD Application Team
* @brief     Header file of MMC HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_mmc_ex.h
stm32h7xx_hal_mmc_ex.h
/**
*****
* @file      stm32h7xx_hal_mmc_ex.h
* @author    MCD Application Team
* @brief     Header file of SD HAL extended module.
*****
* @attention
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*
*****
*/

```



./Inc/stm32h7xx\_hal\_nand.h  
stm32h7xx\_hal\_nand.h

```
/**
 *
 * *****
 * @file      stm32h7xx_hal_nand.h
 * @author    MCD Application Team
 * @brief     Header file of NAND HAL module.
 * *****
 * @attention
 *
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 *
 * *****
 */
```

./Inc/stm32h7xx\_hal\_nor.h  
stm32h7xx\_hal\_nor.h

```
/**
 *
 * *****
 * @file      stm32h7xx_hal_nor.h
 * @author    MCD Application Team
 * @brief     Header file of NOR HAL module.
 * *****
 * @attention
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 *
 * *****
 */
```

./Inc/stm32h7xx\_hal\_opamp.h  
stm32h7xx\_hal\_opamp.h

```
/**
 *
 * *****
 * @file      stm32h7xx_hal_opamp.h
 * @author    MCD Application Team
 * @brief     Header file of OPAMP HAL module.
 * *****
 * @attention
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*
*****
*/

./Inc/stm32h7xx_hal_opamp_ex.h
stm32h7xx_hal_opamp_ex.h
/**
*****
* @file      stm32h7xx_hal_opamp_ex.h
* @author    MCD Application Team
* @brief    Header file of OPAMP HAL Extended module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_ospi.h
stm32h7xx_hal_ospi.h
/**
*****
* @file      stm32h7xx_hal_ospi.h
* @author    MCD Application Team
* @brief    Header file of OSPI HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_otfdec.h
stm32h7xx_hal_otfdec.h
/**

```

```

*****
* @file      stm32h7xx_hal_otfdec.h
* @author    MCD Application Team
* @brief     Header file of OTFDEC HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_pcd.h
stm32h7xx_hal_pcd.h
/**
*****
* @file      stm32h7xx_hal_pcd.h
* @author    MCD Application Team
* @brief     Header file of PCD HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_pcd_ex.h
stm32h7xx_hal_pcd_ex.h
/**
*****
* @file      stm32h7xx_hal_pcd_ex.h
* @author    MCD Application Team
* @brief     Header file of PCD HAL Extension module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_pssi.h

stm32h7xx\_hal\_pssi.h

```
/**
*****
* @file      stm32h7xx_hal_pssi.h
* @author    MCD Application Team
* @brief     Header file of PSSI HAL module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_pwr.h

stm32h7xx\_hal\_pwr.h

```
/**
*****
* @file      stm32h7xx_hal_pwr.h
* @author    MCD Application Team
* @brief     Header file of PWR HAL module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_pwr\_ex.h

stm32h7xx\_hal\_pwr\_ex.h

```
/**
*****
* @file      stm32h7xx_hal_pwr_ex.h
* @author    MCD Application Team
```

```

* @brief    Header file of PWR HAL Extension module.
*****
* @attention
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*
*****
*/

```

```

./Inc/stm32h7xx_hal_qspi.h
stm32h7xx_hal_qspi.h
/**

```

```

*****
* @file      stm32h7xx_hal_qspi.h
* @author    MCD Application Team
* @brief     Header file of QSPI HAL module.
*****
* @attention
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*
*****
*/

```

```

./Inc/stm32h7xx_hal_ramecc.h
stm32h7xx_hal_ramecc.h
/**

```

```

*****
* @file      stm32h7xx_hal_ramecc.h
* @author    MCD Application Team
* @brief     Header file of RAMECC HAL module.
*****
* @attention
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*

```

```

*****
*/

./Inc/stm32h7xx_hal_rcc.h
stm32h7xx_hal_rcc.h
/**
*****
* @file    stm32h7xx_hal_rcc.h
* @author  MCD Application Team
* @brief   Header file of RCC HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_rcc_ex.h
stm32h7xx_hal_rcc_ex.h
/**
*****
* @file    stm32h7xx_hal_rcc_ex.h
* @author  MCD Application Team
* @brief   Header file of RCC HAL Extension module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_rng.h
stm32h7xx_hal_rng.h
/**
*****
* @file    stm32h7xx_hal_rng.h
* @author  MCD Application Team
* @brief   Header file of RNG HAL module.
*****
* @attention

```

```

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*
*****
*/

./Inc/stm32h7xx_hal_rng_ex.h
stm32h7xx_hal_rng_ex.h
/**
*****
* @file      stm32h7xx_hal_rng_ex.h
* @author    MCD Application Team
* @brief     Header file of RNG HAL Extension module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_rtc.h
stm32h7xx_hal_rtc.h
/**
*****
* @file      stm32h7xx_hal_rtc.h
* @author    MCD Application Team
* @brief     Header file of RTC HAL module.
*****
* @attention
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*
*****
*/

```

./Inc/stm32h7xx\_hal\_rtc\_ex.h  
stm32h7xx\_hal\_rtc\_ex.h

```
/**
*****
* @file      stm32h7xx_hal_rtc_ex.h
* @author    MCD Application Team
* @brief     Header file of RTC HAL Extension module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_sai.h  
stm32h7xx\_hal\_sai.h

```
/**
*****
* @file      stm32h7xx_hal_sai.h
* @author    MCD Application Team
* @brief     Header file of SAI HAL module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_sai\_ex.h  
stm32h7xx\_hal\_sai\_ex.h

```
/**
*****
* @file      stm32h7xx_hal_sai_ex.h
* @author    MCD Application Team
* @brief     Header file of SAI HAL extended module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_sd.h
stm32h7xx_hal_sd.h
/**
*****
* @file      stm32h7xx_hal_sd.h
* @author    MCD Application Team
* @brief     Header file of SD HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_sdram.h
stm32h7xx_hal_sdram.h
/**
*****
* @file      stm32h7xx_hal_sdram.h
* @author    MCD Application Team
* @brief     Header file of SDRAM HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_sd_ex.h
stm32h7xx_hal_sd_ex.h
/**

```

```

*****
* @file      stm32h7xx_hal_sd_ex.h
* @author    MCD Application Team
* @brief     Header file of SD HAL extended module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_smartcard.h
stm32h7xx_hal_smartcard.h
/**
*****
* @file      stm32h7xx_hal_smartcard.h
* @author    MCD Application Team
* @brief     Header file of SMARTCARD HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_smartcard_ex.h
stm32h7xx_hal_smartcard_ex.h
/**
*****
* @file      stm32h7xx_hal_smartcard_ex.h
* @author    MCD Application Team
* @brief     Header file of SMARTCARD HAL Extended module.
*****
* @attention
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*****
*/
```

./Inc/stm32h7xx\_hal\_smbus.h

stm32h7xx\_hal\_smbus.h

```
/**
*****
* @file      stm32h7xx_hal_smbus.h
* @author    MCD Application Team
* @brief     Header file of SMBUS HAL module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_spdifrx.h

stm32h7xx\_hal\_spdifrx.h

```
/**
*****
* @file      stm32h7xx_hal_spdifrx.h
* @author    MCD Application Team
* @brief     Header file of SPDIFRX HAL module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_spi.h

stm32h7xx\_hal\_spi.h

```
/**
*****
* @file      stm32h7xx_hal_spi.h
* @author    MCD Application Team
```

```

* @brief   Header file of SPI HAL module.
*****
* @attention
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*****
*/

```

```

./Inc/stm32h7xx_hal_spi_ex.h
stm32h7xx_hal_spi_ex.h

```

```

/**
*****
* @file     stm32h7xx_hal_spi_ex.h
* @author   MCD Application Team
* @brief    Header file of SPI HAL Extended module.
*****
* @attention
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*****
*/

```

```

./Inc/stm32h7xx_hal_sram.h
stm32h7xx_hal_sram.h

```

```

/**
*****
* @file     stm32h7xx_hal_sram.h
* @author   MCD Application Team
* @brief    Header file of SRAM HAL module.
*****
* @attention
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```

```

*****
*/

./Inc/stm32h7xx_hal_swpmi.h
stm32h7xx_hal_swpmi.h
/**
*****
* @file      stm32h7xx_hal_swpmi.h
* @author    MCD Application Team
* @brief     Header file of SWPMI HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_tim.h
stm32h7xx_hal_tim.h
/**
*****
* @file      stm32h7xx_hal_tim.h
* @author    MCD Application Team
* @brief     Header file of TIM HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_tim_ex.h
stm32h7xx_hal_tim_ex.h
/**
*****
* @file      stm32h7xx_hal_tim_ex.h
* @author    MCD Application Team
* @brief     Header file of TIM HAL Extended module.
*****
* @attention

```

```

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*
*****
*/

./Inc/stm32h7xx_hal_uart.h
stm32h7xx_hal_uart.h
/**
*****
* @file      stm32h7xx_hal_uart.h
* @author    MCD Application Team
* @brief     Header file of UART HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_uart_ex.h
stm32h7xx_hal_uart_ex.h
/**
*****
* @file      stm32h7xx_hal_uart_ex.h
* @author    MCD Application Team
* @brief     Header file of UART HAL Extended module.
*****
* @attention
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*****
*/

```

./Inc/stm32h7xx\_hal\_usart.h  
stm32h7xx\_hal\_usart.h

```
/**
*****
* @file      stm32h7xx_hal_usart.h
* @author    MCD Application Team
* @brief     Header file of USART HAL module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_usart\_ex.h  
stm32h7xx\_hal\_usart\_ex.h

```
/**
*****
* @file      stm32h7xx_hal_usart_ex.h
* @author    MCD Application Team
* @brief     Header file of USART HAL Extended module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_wwdg.h  
stm32h7xx\_hal\_wwdg.h

```
/**
*****
* @file      stm32h7xx_hal_wwdg.h
* @author    MCD Application Team
* @brief     Header file of WWDG HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_adc.h
stm32h7xx_ll_adc.h
/**
*****
* @file      stm32h7xx_ll_adc.h
* @author    MCD Application Team
* @brief     Header file of ADC LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_bdma.h
stm32h7xx_ll_bdma.h
/**
*****
* @file      stm32h7xx_ll_bdma.h
* @author    MCD Application Team
* @brief     Header file of BDMA LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_bus.h
stm32h7xx_ll_bus.h
/**

```



```

*****
* @file      stm32h7xx_ll_bus.h
* @author    MCD Application Team
* @version   $VERSION$
* @date      $DATE$
* @brief     Header file of BUS LL module.

./Inc/stm32h7xx_ll_comp.h
stm32h7xx_ll_comp.h
/**
*****
* @file      stm32h7xx_ll_comp.h
* @author    MCD Application Team
* @brief     Header file of COMP LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_cortex.h
stm32h7xx_ll_cortex.h
/**
*****
* @file      stm32h7xx_ll_cortex.h
* @author    MCD Application Team
* @brief     Header file of CORTEX LL module.

./Inc/stm32h7xx_ll_crc.h
stm32h7xx_ll_crc.h
/**
*****
* @file      stm32h7xx_ll_crc.h
* @author    MCD Application Team
* @brief     Header file of CRC LL module.
*****
* @attention
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*

```

```

*****
*/

./Inc/stm32h7xx_ll_crs.h
stm32h7xx_ll_crs.h
/**
*****
* @file    stm32h7xx_ll_crs.h
* @author  MCD Application Team
* @brief   Header file of CRS LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_dac.h
stm32h7xx_ll_dac.h
/**
*****
* @file    stm32h7xx_ll_dac.h
* @author  MCD Application Team
* @brief   Header file of DAC LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_delayblock.h
stm32h7xx_ll_delayblock.h
/**
*****
* @file    stm32h7xx_ll_delayblock.h
* @author  MCD Application Team
* @brief   Header file of Delay Block module.
*****
* @attention

```

```

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*
*****
*/

./Inc/stm32h7xx_ll_dma.h
stm32h7xx_ll_dma.h
/**
*****
* @file      stm32h7xx_ll_dma.h
* @author    MCD Application Team
* @brief     Header file of DMA LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_dma2d.h
stm32h7xx_ll_dma2d.h
/**
*****
* @file      stm32h7xx_ll_dma2d.h
* @author    MCD Application Team
* @brief     Header file of DMA2D LL module.
*****
* @attention
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*
*****
*/

```

./Inc/stm32h7xx\_ll\_dmamux.h  
stm32h7xx\_ll\_dmamux.h

```
/**
 *
 * *****
 * @file      stm32h7xx_ll_dmamux.h
 * @author    MCD Application Team
 * @brief     Header file of DMAMUX LL module.
 * *****
 * @attention
 *
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 *
 * *****
 */
```

./Inc/stm32h7xx\_ll\_exti.h  
stm32h7xx\_ll\_exti.h

```
/**
 *
 * *****
 * @file      stm32h7xx_ll_exti.h
 * @author    MCD Application Team
 * @brief     Header file of EXTI LL module.
 * *****
 * @attention
 *
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 *
 * *****
 */
```

./Inc/stm32h7xx\_ll\_fmc.h  
stm32h7xx\_ll\_fmc.h

```
/**
 *
 * *****
 * @file      stm32h7xx_ll_fmc.h
 * @author    MCD Application Team
 * @brief     Header file of FMC HAL module.
 * *****
 * @attention
 *
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```

```

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*
*****
*/

./Inc/stm32h7xx_ll_gpio.h
stm32h7xx_ll_gpio.h
/**
*****
* @file      stm32h7xx_ll_gpio.h
* @author    MCD Application Team
* @brief     Header file of GPIO LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_hrtim.h
stm32h7xx_ll_hrtim.h
/**
*****
* @file      stm32h7xx_ll_hrtim.h
* @author    MCD Application Team
* @brief     Header file of HRTIM LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_hsem.h
stm32h7xx_ll_hsem.h
/**

```

```

*****
* @file      stm32h7xx_ll_hsem.h
* @author    MCD Application Team
* @brief     Header file of HSEM LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_i2c.h
stm32h7xx_ll_i2c.h
/**
*****
* @file      stm32h7xx_ll_i2c.h
* @author    MCD Application Team
* @brief     Header file of I2C LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_iwdg.h
stm32h7xx_ll_iwdg.h
/**
*****
* @file      stm32h7xx_ll_iwdg.h
* @author    MCD Application Team
* @brief     Header file of IWDG LL module.
*****
* @attention
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```

```

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*
*****
*/

./Inc/stm32h7xx_ll_lptim.h
stm32h7xx_ll_lptim.h
/**
*****
* @file      stm32h7xx_ll_lptim.h
* @author    MCD Application Team
* @brief     Header file of LPTIM LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_lpuart.h
stm32h7xx_ll_lpuart.h
/**
*****
* @file      stm32h7xx_ll_lpuart.h
* @author    MCD Application Team
* @brief     Header file of LPUART LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_mdma.h
stm32h7xx_ll_mdma.h
/**
*****
* @file      stm32h7xx_ll_mdma.h
* @author    MCD Application Team

```

```

* @brief Header file of MDMA LL module.
*****
* @attention
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*
*****
*/

```

```

./Inc/stm32h7xx_ll_opamp.h
stm32h7xx_ll_opamp.h
/**

```

```

*****
* @file      stm32h7xx_ll_opamp.h
* @author    MCD Application Team
* @brief     Header file of OPAMP LL module.
*****
* @attention
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*
*****
*/

```

```

./Inc/stm32h7xx_ll_pwr.h
stm32h7xx_ll_pwr.h
/**

```

```

*****
* @file      stm32h7xx_ll_pwr.h
* @author    MCD Application Team
* @brief     Header file of PWR LL module.
*****
* @attention
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*

```



```

*****
*/

./Inc/stm32h7xx_ll_rcc.h
stm32h7xx_ll_rcc.h
/**
*****
* @file    stm32h7xx_ll_rcc.h
* @author  MCD Application Team
* @version $VERSION$
* @date    $DATE$
* @brief   Header file of RCC LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_rng.h
stm32h7xx_ll_rng.h
/**
*****
* @file    stm32h7xx_ll_rng.h
* @author  MCD Application Team
* @brief   Header file of RNG LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_rtc.h
stm32h7xx_ll_rtc.h
/**
*****
* @file    stm32h7xx_ll_rtc.h
* @author  MCD Application Team
* @brief   Header file of RTC LL module.

```

```

*****
* @attention
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*****
*/

./Inc/stm32h7xx_ll_sdmmc.h
stm32h7xx_ll_sdmmc.h
/**
*****
* @file      stm32h7xx_ll_sdmmc.h
* @author    MCD Application Team
* @brief     Header file of SDMMC HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_ll_spi.h
stm32h7xx_ll_spi.h
/**
*****
* @file      stm32h7xx_ll_spi.h
* @author    MCD Application Team
* @brief     Header file of SPI LL module.
*****
* @attention
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*
*****

```

```

*/

./Inc/stm32h7xx_ll_swpmi.h
stm32h7xx_ll_swpmi.h
/**
*****
* @file      stm32h7xx_ll_swpmi.h
* @author    MCD Application Team
* @brief     Header file of SWPMI LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_system.h
stm32h7xx_ll_system.h
/**
*****
* @file      stm32h7xx_ll_system.h
* @author    MCD Application Team
* @brief     Header file of SYSTEM LL module.

./Inc/stm32h7xx_ll_tim.h
stm32h7xx_ll_tim.h
/**
*****
* @file      stm32h7xx_ll_tim.h
* @author    MCD Application Team
* @brief     Header file of TIM LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_usart.h
stm32h7xx_ll_usart.h

```

```

/**
*****
* @file      stm32h7xx_ll_usart.h
* @author    MCD Application Team
* @brief     Header file of USART LL module.
*****
* @attention
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*
*****
*/

```

./Inc/stm32h7xx\_ll\_usb.h

stm32h7xx\_ll\_usb.h

```

/**
*****
* @file      stm32h7xx_ll_usb.h
* @author    MCD Application Team
* @brief     Header file of USB Low Layer HAL module.
*****
* @attention
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*
*****
*/

```

./Inc/stm32h7xx\_ll\_utils.h

stm32h7xx\_ll\_utils.h

```

/**
*****
* @file      stm32h7xx_ll_utils.h
* @author    MCD Application Team
* @brief     Header file of UTILS LL module.

```

./Inc/stm32h7xx\_ll\_wwdg.h

stm32h7xx\_ll\_wwdg.h

```

/**
*****
* @file      stm32h7xx_ll_wwdg.h
* @author    MCD Application Team

```

```

* @brief    Header file of WWDG LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32_assert_template.h
stm32_assert_template.h
/**
*****
* @file      stm32_assert.h
* @author    MCD Application Team
* @brief     STM32 assert template file.
*           This file should be copied to the application folder and renamed
*           to stm32_assert.h.
*****
* @attention
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*
*****
*/

./Inc/system_stm32h7xx.h
system_stm32h7xx.h
/**
*****
* @file      system_stm32h7xx.h
* @author    MCD Application Team
* @brief     CMSIS Cortex-Mx Device System Source File for STM32H7xx devices.
*****
* @attention
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```

```

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*
*****
*/

./Src/stm32h7xx_hal.c
stm32h7xx_hal.c
/**
*****
* @file      stm32h7xx_hal.c
* @author    MCD Application Team
* @brief     HAL module driver.
*            This is the common part of the HAL initialization
*
./Src/stm32h7xx_hal_adc.c
stm32h7xx_hal_adc.c
/**
*****
* @file      stm32h7xx_hal_adc.c
* @author    MCD Application Team
* @brief     This file provides firmware functions to manage the following
*            functionalities of the Analog to Digital Converter (ADC)
*            peripheral:
*            + Initialization and de-initialization functions
*              ++ Initialization and Configuration of ADC
*            + Operation functions
*              ++ Start, stop, get result of conversions of regular
*                 group, using 3 possible modes: polling, interruption or DMA.
*            + Control functions
*              ++ Channels configuration on regular group
*              ++ Analog Watchdog configuration
*            + State functions
*              ++ ADC state machine management
*              ++ Interrupts and flags management
*            Other functions (extended functions) are available in file
*            "stm32h7xx_hal_adc_ex.c".
*
./Src/stm32h7xx_hal_adc_ex.c
stm32h7xx_hal_adc_ex.c
/**
*****
* @file      stm32h7xx_hal_adc_ex.c
* @author    MCD Application Team
* @brief     This file provides firmware functions to manage the following
*            functionalities of the Analog to Digital Converter (ADC)
*            peripheral:
*            + Operation functions
*              ++ Start, stop, get result of conversions of ADC group injected,
*                 using 2 possible modes: polling, interruption.
*              ++ Calibration
*                +++ ADC automatic self-calibration
*                +++ Calibration factors get or set

```

```

*          ++ Multimode feature when available
*          + Control functions
*          ++ Channels configuration on ADC group injected
*          + State functions
*          ++ ADC group injected contexts queue management
*          Other functions (generic functions) are available in file
*          "stm32h7xx_hal_adc.c".
*

```

./Src/stm32h7xx\_hal\_cec.c

stm32h7xx\_hal\_cec.c

```

/**
*****
* @file      stm32h7xx_hal_cec.c
* @author    MCD Application Team
* @brief     CEC HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the High Definition Multimedia Interface
*            Consumer Electronics Control Peripheral (CEC).
*            + Initialization and de-initialization function
*            + IO operation function
*            + Peripheral Control function
*
*

```

./Src/stm32h7xx\_hal\_comp.c

stm32h7xx\_hal\_comp.c

```

/**
*****
* @file      stm32h7xx_hal_comp.c
* @author    MCD Application Team
* @brief     COMP HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the COMP peripheral:
*            + Initialization and de-initialization functions
*            + Start/Stop operation functions in polling mode
*            + Start/Stop operation functions in interrupt mode
*            + Peripheral control functions
*            + Peripheral state functions
*

```

./Src/stm32h7xx\_hal\_cortex.c

stm32h7xx\_hal\_cortex.c

```

/**
*****
* @file      stm32h7xx_hal_cortex.c
* @author    MCD Application Team
* @brief     CORTEX HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the CORTEX:
*            + Initialization and de-initialization functions
*            + Peripheral Control functions
*

```

./Src/stm32h7xx\_hal\_crc.c

stm32h7xx\_hal\_crc.c

```
/**
*****
* @file      stm32h7xx_hal_crc.c
* @author    MCD Application Team
* @brief     CRC HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Cyclic Redundancy Check (CRC) peripheral:
*            + Initialization and de-initialization functions
*            + Peripheral Control functions
*            + Peripheral State functions
*

```

./Src/stm32h7xx\_hal\_crc\_ex.c

stm32h7xx\_hal\_crc\_ex.c

```
/**
*****
* @file      stm32h7xx_hal_crc_ex.c
* @author    MCD Application Team
* @brief     Extended CRC HAL module driver.
*            This file provides firmware functions to manage the extended
*            functionalities of the CRC peripheral.
*

```

./Src/stm32h7xx\_hal\_cryp.c

stm32h7xx\_hal\_cryp.c

```
/**
*****
* @file      stm32h7xx_hal_cryp.c
* @author    MCD Application Team
* @brief     CRYP HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Cryptography (CRYP) peripheral:
*            + Initialization and de-initialization functions
*            + AES processing functions
*            + DES processing functions
*            + TDES processing functions
*            + DMA callback functions
*            + CRYP IRQ handler management
*            + Peripheral State functions
*

```

./Src/stm32h7xx\_hal\_cryp\_ex.c

stm32h7xx\_hal\_cryp\_ex.c

```
/**
*****
* @file      stm32h7xx_hal_cryp_ex.c
* @author    MCD Application Team
* @brief     Extended CRYP HAL module driver
*            This file provides firmware functions to manage the following
*            functionalities of CRYP extension peripheral:
*            + Extended AES processing functions
*

```



./Src/stm32h7xx\_hal\_dac.c

stm32h7xx\_hal\_dac.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_dac.c

\* @author MCD Application Team

\* @brief DAC HAL module driver.

\* This file provides firmware functions to manage the following  
\* functionalities of the Digital to Analog Converter (DAC) peripheral:

\* + Initialization and de-initialization functions

\* + IO operation functions

\* + Peripheral Control functions

\* + Peripheral State and Errors functions

\*

\*

./Src/stm32h7xx\_hal\_dac\_ex.c

stm32h7xx\_hal\_dac\_ex.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_dac\_ex.c

\* @author MCD Application Team

\* @brief Extended DAC HAL module driver.

\* This file provides firmware functions to manage the extended  
\* functionalities of the DAC peripheral.

\*

\*

./Src/stm32h7xx\_hal\_dcmi.c

stm32h7xx\_hal\_dcmi.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_dcmi.c

\* @author MCD Application Team

\* @brief DCMI HAL module driver

\* This file provides firmware functions to manage the following  
\* functionalities of the Digital Camera Interface (DCMI) peripheral:

\* + Initialization and de-initialization functions

\* + IO operation functions

\* + Peripheral Control functions

\* + Peripheral State and Error functions

\*

./Src/stm32h7xx\_hal\_dfldm.c

stm32h7xx\_hal\_dfldm.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_dfldm.c

\* @author MCD Application Team

\* @brief This file provides firmware functions to manage the following  
\* functionalities of the Digital Filter for Sigma-Delta Modulators  
\* (DFSDM) peripherals:

\* + Initialization and configuration of channels and filters

\* + Regular channels configuration

- \* + Injected channels configuration
- \* + Regular/Injected Channels DMA Configuration
- \* + Interrupts and flags management
- \* + Analog watchdog feature
- \* + Short-circuit detector feature
- \* + Extremes detector feature
- \* + Clock absence detector feature
- \* + Break generation on analog watchdog or short-circuit event
- \*

./Src/stm32h7xx\_hal\_dfldm\_ex.c

stm32h7xx\_hal\_dfldm\_ex.c

```
/**
*****
* @file      stm32h7xx_hal_dfldm_ex.c
* @author    MCD Application Team
* @brief     DFSDM Extended HAL module driver.
*            This file provides firmware functions to manage the following
*            functionality of the DFSDM Peripheral Controller:
*            + Set and get pulses skipping on channel.
*
*****
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*
*****
*/
```

./Src/stm32h7xx\_hal\_dma.c

stm32h7xx\_hal\_dma.c

```
/**
*****
* @file      stm32h7xx_hal_dma.c
* @author    MCD Application Team
* @brief     DMA HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Direct Memory Access (DMA) peripheral:
*            + Initialization and de-initialization functions
*            + IO operation functions
*            + Peripheral State and errors functions
*
*****
```

./Src/stm32h7xx\_hal\_dma2d.c

stm32h7xx\_hal\_dma2d.c

```
/**
*****
* @file      stm32h7xx_hal_dma2d.c
* @author    MCD Application Team
*
*****
```

```

* @brief DMA2D HAL module driver.
* This file provides firmware functions to manage the following
* functionalities of the DMA2D peripheral:
* + Initialization and de-initialization functions
* + IO operation functions
* + Peripheral Control functions
* + Peripheral State and Errors functions
*

./Src/stm32h7xx_hal_dma_ex.c
stm32h7xx_hal_dma_ex.c
/**
*****
* @file stm32h7xx_hal_dma_ex.c
* @author MCD Application Team
* @brief DMA Extension HAL module driver
* This file provides firmware functions to manage the following
* functionalities of the DMA Extension peripheral:
* + Extended features functions
*

./Src/stm32h7xx_hal_dsi.c
stm32h7xx_hal_dsi.c
/**
*****
* @file stm32h7xx_hal_dsi.c
* @author MCD Application Team
* @brief DSI HAL module driver.
* This file provides firmware functions to manage the following
* functionalities of the DSI peripheral:
* + Initialization and de-initialization functions
* + IO operation functions
* + Peripheral Control functions
* + Peripheral State and Errors functions

./Src/stm32h7xx_hal_dts.c
stm32h7xx_hal_dts.c
/**
*****
* @file stm32h7xx_hal_dts.c
* @author MCD Application Team
* @brief DTS HAL module driver.
* This file provides firmware functions to manage the following
* functionalities of the DTS peripheral:
* + Initialization and de-initialization functions
* + Start/Stop operation functions in polling mode.
* + Start/Stop operation functions in interrupt mode.
* + Peripheral Control functions
* + Peripheral State functions
*

./Src/stm32h7xx_hal_eth.c
stm32h7xx_hal_eth.c
/**

```

```

*****
* @file      stm32h7xx_hal_eth.c
* @author    MCD Application Team
* @brief     ETH HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Ethernet (ETH) peripheral:
*            + Initialization and deinitialization functions
*            + IO operation functions
*            + Peripheral Control functions
*            + Peripheral State and Errors functions
*
./Src/stm32h7xx_hal_eth_ex.c
stm32h7xx_hal_eth_ex.c
/**
*****
* @file      stm32h7xx_hal_eth_ex.c
* @author    MCD Application Team
* @brief     ETH HAL Extended module driver.
*
*****
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*
*****
*/

./Src/stm32h7xx_hal_exti.c
stm32h7xx_hal_exti.c
/**
*****
* @file      stm32h7xx_hal_exti.c
* @author    MCD Application Team
* @brief     EXTI HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the General Purpose Input/Output (EXTI)
peripheral:
*            + Initialization and de-initialization functions
*            + IO operation functions
*
./Src/stm32h7xx_hal_fdcan.c
stm32h7xx_hal_fdcan.c
/**
*****
* @file      stm32h7xx_hal_fdcan.c
* @author    MCD Application Team

```

```

* @brief  FDCAN HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the Flexible DataRate Controller Area Network
* (FDCAN) peripheral:
*
* + Initialization and de-initialization functions
*
* + IO operation functions
*
* + Peripheral Configuration and Control functions
*
* + Peripheral State and Error functions
*

```

```

./Src/stm32h7xx_hal_flash.c
stm32h7xx_hal_flash.c

```

```

/**
*****
* @file    stm32h7xx_hal_flash.c
* @author  MCD Application Team
* @brief  FLASH HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the internal FLASH memory:
*
* + Program operations functions
*
* + Memory Control functions
*
* + Peripheral Errors functions
*

```

```

./Src/stm32h7xx_hal_flash_ex.c
stm32h7xx_hal_flash_ex.c

```

```

/**
*****
* @file    stm32h7xx_hal_flash_ex.c
* @author  MCD Application Team
* @brief  Extended FLASH HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the FLASH extension peripheral:
*
* + Extended programming operations functions
*

```

```

./Src/stm32h7xx_hal_gfxmmu.c
stm32h7xx_hal_gfxmmu.c

```

```

/**
*****
* @file    stm32h7xx_hal_gfxmmu.c
* @author  MCD Application Team
* @brief  This file provides firmware functions to manage the following
* functionalities of the Graphic MMU (GFXMMU) peripheral:
*
* + Initialization and De-initialization.
*
* + LUT configuration.
*
* + Force flush and/or invalidate of cache.
*
* + Modify physical buffer addresses.
*
* + Modify cache and pre-fetch parameters.
*
* + Error management.
*

```

```

./Src/stm32h7xx_hal_gpio.c
stm32h7xx_hal_gpio.c

```

```

/**
*****
* @file      stm32h7xx_hal_gpio.c
* @author    MCD Application Team
* @brief     GPIO HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the General Purpose Input/Output (GPIO)
peripheral:
*            + Initialization and de-initialization functions
*            + IO operation functions
*
./Src/stm32h7xx_hal_hash.c
stm32h7xx_hal_hash.c
/**
*****
* @file      stm32h7xx_hal_hash.c
* @author    MCD Application Team
* @brief     HASH HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the HASH peripheral:
*            + Initialization and de-initialization methods
*            + HASH or HMAC processing in polling mode
*            + HASH or HMAC processing in interrupt mode
*            + HASH or HMAC processing in DMA mode
*            + Peripheral State methods
*            + HASH or HMAC processing suspension/resumption
*
./Src/stm32h7xx_hal_hash_ex.c
stm32h7xx_hal_hash_ex.c
/**
*****
* @file      stm32h7xx_hal_hash_ex.c
* @author    MCD Application Team
* @brief     Extended HASH HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the HASH peripheral for SHA-224 and SHA-256
*            algorithms:
*            + HASH or HMAC processing in polling mode
*            + HASH or HMAC processing in interrupt mode
*            + HASH or HMAC processing in DMA mode
*            Additionally, this file provides functions to manage HMAC
*            multi-buffer DMA-based processing for MD-5, SHA-1, SHA-224
*            and SHA-256.
*
./Src/stm32h7xx_hal_hcd.c
stm32h7xx_hal_hcd.c
/**
*****
* @file      stm32h7xx_hal_hcd.c
* @author    MCD Application Team

```

```

* @brief   HCD HAL module driver.
*          This file provides firmware functions to manage the following
*          functionalities of the USB Peripheral Controller:
*          + Initialization and de-initialization functions
*          + IO operation functions
*          + Peripheral Control functions
*          + Peripheral State functions
*

```

./Src/stm32h7xx\_hal\_hrtim.c

stm32h7xx\_hal\_hrtim.c

/\*\*

\*\*\*\*\*

```

* @file     stm32h7xx_hal_hrtim.c
* @author   MCD Application Team
* @brief    TIM HAL module driver.
*          This file provides firmware functions to manage the following
*          functionalities of the High Resolution Timer (HRTIM) peripheral:
*          + HRTIM Initialization
*          + Timer Time Base Unit Configuration
*          + Simple Time Base Start/Stop
*          + Simple Time Base Start/Stop Interrupt
*          + Simple Time Base Start/Stop DMA Request
*          + Simple Output Compare/PWM Channel Configuration
*          + Simple Output Compare/PWM Channel Start/Stop Interrupt
*          + Simple Output Compare/PWM Channel Start/Stop DMA Request
*          + Simple Input Capture Channel Configuration
*          + Simple Input Capture Channel Start/Stop Interrupt
*          + Simple Input Capture Channel Start/Stop DMA Request
*          + Simple One Pulse Channel Configuration
*          + Simple One Pulse Channel Start/Stop Interrupt
*          + HRTIM External Synchronization Configuration
*          + HRTIM Burst Mode Controller Configuration
*          + HRTIM Burst Mode Controller Enabling
*          + HRTIM External Events Conditioning Configuration
*          + HRTIM Faults Conditioning Configuration
*          + HRTIM Faults Enabling
*          + HRTIM ADC trigger Configuration
*          + Waveform Timer Configuration
*          + Waveform Event Filtering Configuration
*          + Waveform Dead Time Insertion Configuration
*          + Waveform Chopper Mode Configuration
*          + Waveform Compare Unit Configuration
*          + Waveform Capture Unit Configuration
*          + Waveform Output Configuration
*          + Waveform Counter Start/Stop
*          + Waveform Counter Start/Stop Interrupt
*          + Waveform Counter Start/Stop DMA Request
*          + Waveform Output Enabling
*          + Waveform Output Level Set/Get
*          + Waveform Output State Get
*          + Waveform Burst DMA Operation Configuration
*          + Waveform Burst DMA Operation Start
*          + Waveform Timer Counter Software Reset

```

- \* + Waveform Capture Software Trigger
- \* + Waveform Burst Mode Controller Software Trigger
- \* + Waveform Timer Pre-loadable Registers Update Enabling
- \* + Waveform Timer Pre-loadable Registers Software Update
- \* + Waveform Timer Delayed Protection Status Get
- \* + Waveform Timer Burst Status Get
- \* + Waveform Timer Push-Pull Status Get
- \* + Peripheral State Get

./Src/stm32h7xx\_hal\_hsem.c

stm32h7xx\_hal\_hsem.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_hsem.c  
 \* @author MCD Application Team  
 \* @brief HSEM HAL module driver.  
 \* This file provides firmware functions to manage the following  
 \* functionalities of the semaphore peripheral:  
 \* + Semaphore Take function (2-Step Procedure) , non blocking  
 \* + Semaphore FastTake function (1-Step Procedure) , non blocking  
 \* + Semaphore Status check  
 \* + Semaphore Clear Key Set and Get  
 \* + Release and release all functions  
 \* + Semaphore notification enabling and disabling and callnack

functions

\* + IRQ handler management  
 \*  
 \*

./Src/stm32h7xx\_hal\_i2c.c

stm32h7xx\_hal\_i2c.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_i2c.c  
 \* @author MCD Application Team  
 \* @brief I2C HAL module driver.  
 \* This file provides firmware functions to manage the following  
 \* functionalities of the Inter Integrated Circuit (I2C) peripheral:  
 \* + Initialization and de-initialization functions  
 \* + IO operation functions  
 \* + Peripheral State and Errors functions

./Src/stm32h7xx\_hal\_i2c\_ex.c

stm32h7xx\_hal\_i2c\_ex.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_i2c\_ex.c  
 \* @author MCD Application Team  
 \* @brief I2C Extended HAL module driver.  
 \* This file provides firmware functions to manage the following  
 \* functionalities of I2C Extended peripheral:  
 \* + Extended features functions

\*



./Src/stm32h7xx\_hal\_i2s.c

stm32h7xx\_hal\_i2s.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_i2s.c

\* @author MCD Application Team

\* @brief I2S HAL module driver.

\* This file provides firmware functions to manage the following  
\* functionalities of the Integrated Interchip Sound (I2S) peripheral:

\* + Initialization and de-initialization functions

\* + IO operation functions

\* + Peripheral State and Errors functions

./Src/stm32h7xx\_hal\_i2s\_ex.c

stm32h7xx\_hal\_i2s\_ex.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_i2s\_ex.c

\* @author MCD Application Team

\* @brief I2S HAL module driver.

\* This file provides firmware functions to manage the following  
\* functionalities of I2S extension peripheral:

\* + Extension features Functions

\*

./Src/stm32h7xx\_hal\_irda.c

stm32h7xx\_hal\_irda.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_irda.c

\* @author MCD Application Team

\* @brief IRDA HAL module driver.

\* This file provides firmware functions to manage the following  
\* functionalities of the IrDA (Infrared Data Association) Peripheral  
\* (IRDA)

\* + Initialization and de-initialization functions

\* + IO operation functions

\* + Peripheral State and Errors functions

\* + Peripheral Control functions

\*

./Src/stm32h7xx\_hal\_iwdg.c

stm32h7xx\_hal\_iwdg.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_iwdg.c

\* @author MCD Application Team

\* @brief IWDG HAL module driver.

\* This file provides firmware functions to manage the following  
\* functionalities of the Independent Watchdog (IWDG) peripheral:

\* + Initialization and Start functions

\* + IO operation functions

\*

./Src/stm32h7xx\_hal\_jpeg.c

stm32h7xx\_hal\_jpeg.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_jpeg.c

\* @author MCD Application Team

\* @brief JPEG HAL module driver.

\* This file provides firmware functions to manage the following functionalities of the JPEG encoder/decoder peripheral:

\* + Initialization and de-initialization functions

\* + JPEG processing functions encoding and decoding

\* + JPEG decoding Getting Info and encoding configuration setting

\* + JPEG enable/disable header parsing functions (for decoding)

\* + JPEG Input/Output Buffer configuration.

\* + JPEG callback functions

\* + JPEG Abort/Pause/Resume functions

\* + JPEG custom quantization tables setting functions

\* + IRQ handler management

\* + Peripheral State and Error functions

\*

./Src/stm32h7xx\_hal\_lptim.c

stm32h7xx\_hal\_lptim.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_lptim.c

\* @author MCD Application Team

\* @brief LPTIM HAL module driver.

\* This file provides firmware functions to manage the following functionalities of the Low Power Timer (LPTIM) peripheral:

\* + Initialization and de-initialization functions.

\* + Start/Stop operation functions in polling mode.

\* + Start/Stop operation functions in interrupt mode.

\* + Reading operation functions.

\* + Peripheral State functions.

\*

./Src/stm32h7xx\_hal\_ltdc.c

stm32h7xx\_hal\_ltdc.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_ltdc.c

\* @author MCD Application Team

\* @brief LTDC HAL module driver.

\* This file provides firmware functions to manage the following functionalities of the LTDC peripheral:

\* + Initialization and de-initialization functions

\* + IO operation functions

\* + Peripheral Control functions

\* + Peripheral State and Errors functions

\*

./Src/stm32h7xx\_hal\_ltdc\_ex.c

stm32h7xx\_hal\_ltdc\_ex.c

```
/**
 *
 * *****
 * @file      stm32h7xx_hal_ltdc_ex.c
 * @author    MCD Application Team
 * @brief     LTDC Extension HAL module driver.
 * *****
 *
 * @attention
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 *
 * *****
 */
```

./Src/stm32h7xx\_hal\_mdios.c

stm32h7xx\_hal\_mdios.c

```
/**
 *
 * *****
 * @file      stm32h7xx_hal_mdios.c
 * @author    MCD Application Team
 * @brief     MDIOS HAL module driver.
 *
 * This file provides firmware functions to manage the following
 * functionalities of the MDIOS Peripheral.
 *
 * + Initialization and de-initialization functions
 *
 * + IO operation functions
 *
 * + Peripheral Control functions
 *
 *
 */
```

./Src/stm32h7xx\_hal\_mdma.c

stm32h7xx\_hal\_mdma.c

```
/**
 *
 * *****
 * @file      stm32h7xx_hal_mdma.c
 * @author    MCD Application Team
 * @brief     This file provides firmware functions to manage the following
 *
 * functionalities of the Master Direct Memory Access (MDMA)
 * peripheral:
 *
 * + Initialization/de-initialization functions
 *
 * + I/O operation functions
 *
 * + Peripheral State and errors functions
 *
 */
```

./Src/stm32h7xx\_hal\_mmc.c

stm32h7xx\_hal\_mmc.c

```
/**
 *
 * *****
 * @file      stm32h7xx_hal_mmc.c
 * @author    MCD Application Team
 *
 */
```

```

* @brief   MMC card HAL module driver.
*          This file provides firmware functions to manage the following
*          functionalities of the Secure Digital (MMC) peripheral:
*          + Initialization and de-initialization functions
*          + IO operation functions
*          + Peripheral Control functions
*          + MMC card Control functions
*
./Src/stm32h7xx_hal_mmc_ex.c
stm32h7xx_hal_mmc_ex.c
/**
*****
* @file    stm32h7xx_hal_mmc_ex.c
* @author  MCD Application Team
* @brief   MMC card Extended HAL module driver.
*          This file provides firmware functions to manage the following
*          functionalities of the Secure Digital (MMC) peripheral:
*          + Extended features functions
*
./Src/stm32h7xx_hal_msp.c
stm32h7xx_hal_msp.c
/**
*****
* @file    stm32h7xx_hal_msp_template.c
* @author  MCD Application Team
* @brief   HAL MSP module.
*          This file template is located in the HAL folder and should be
copied
*          to the user folder.
*
./Src/stm32h7xx_hal_nand.c
stm32h7xx_hal_nand.c
/**
*****
* @file    stm32h7xx_hal_nand.c
* @author  MCD Application Team
* @brief   NAND HAL module driver.
*          This file provides a generic firmware to drive NAND memories
mounted
*          as external device.
*
./Src/stm32h7xx_hal_nor.c
stm32h7xx_hal_nor.c
/**
*****
* @file    stm32h7xx_hal_nor.c
* @author  MCD Application Team
* @brief   NOR HAL module driver.
*          This file provides a generic firmware to drive NOR memories mounted
*          as external device.

```

```

*

./Src/stm32h7xx_hal_opamp.c
stm32h7xx_hal_opamp.c
/**
*****
* @file      stm32h7xx_hal_opamp.c
* @author    MCD Application Team
* @brief     OPAMP HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the operational amplifier(s) peripheral:
*            + OPAMP configuration
*            + OPAMP calibration
*            Thanks to
*            + Initialization and de-initialization functions
*            + IO operation functions
*            + Peripheral Control functions
*            + Peripheral State functions
*

./Src/stm32h7xx_hal_opamp_ex.c
stm32h7xx_hal_opamp_ex.c
/**
*****
* @file      stm32h7xx_hal_opamp_ex.c
* @author    MCD Application Team
* @brief     Extended OPAMP HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the operational amplifier(s)(OPAMP1, OPAMP2 etc)
*            peripheral:
*            + Extended Initialization and de-initialization functions
*            + Extended Peripheral Control functions
*
*****
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*
*****
*/

./Src/stm32h7xx_hal_ospi.c
stm32h7xx_hal_ospi.c
/**
*****
* @file      stm32h7xx_hal_ospi.c
* @author    MCD Application Team
* @brief     OSPI HAL module driver.

```

./Src/stm32h7xx\_hal\_otfdec.c

stm32h7xx\_hal\_otfdec.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_otfdec.c

\* @author MCD Application Team

\* @brief OTFDEC HAL module driver.

\* This file provides firmware functions to manage the following  
\* functionalities of the On-The-Fly Decryption (OTFDEC) peripheral:

\* + Initialization and de-initialization functions

\* + Region setting/enable functions

\* + Peripheral State functions

\*

./Src/stm32h7xx\_hal\_pcd.c

stm32h7xx\_hal\_pcd.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_pcd.c

\* @author MCD Application Team

\* @brief PCD HAL module driver.

\* This file provides firmware functions to manage the following  
\* functionalities of the USB Peripheral Controller:

\* + Initialization and de-initialization functions

\* + IO operation functions

\* + Peripheral Control functions

\* + Peripheral State functions

\*

./Src/stm32h7xx\_hal\_pcd\_ex.c

stm32h7xx\_hal\_pcd\_ex.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_pcd\_ex.c

\* @author MCD Application Team

\* @brief PCD Extended HAL module driver.

\* This file provides firmware functions to manage the following  
\* functionalities of the USB Peripheral Controller:

\* + Extended features functions

\*

\*\*\*\*\*

\* @attention

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\*

\*\*\*\*\*

\*/

./Src/stm32h7xx\_hal\_pssi.c

stm32h7xx\_hal\_pssi.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_pssi.c

\* @author MCD Application Team

\* @brief PSSI HAL module driver.

\* This file provides firmware functions to manage the following

\* functionalities of the Parallel Synchronous Slave Interface (PSSI)

peripheral:

\* + Initialization and de-initialization functions

\* + IO operation functions

\* + Peripheral State and Errors functions

\*

./Src/stm32h7xx\_hal\_pwr.c

stm32h7xx\_hal\_pwr.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_pwr.c

\* @author MCD Application Team

\* @brief PWR HAL module driver.

\* This file provides firmware functions to manage the following

\* functionalities of the Power Controller (PWR) peripheral:

\* + Initialization and de-initialization functions.

\* + Peripheral Control functions.

\* + Interrupt Handling functions.

./Src/stm32h7xx\_hal\_pwr\_ex.c

stm32h7xx\_hal\_pwr\_ex.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_pwr\_ex.c

\* @author MCD Application Team

\* @brief Extended PWR HAL module driver.

\* This file provides firmware functions to manage the following

\* functionalities of PWR extension peripheral:

\* + Peripheral Extended features functions

./Src/stm32h7xx\_hal\_qspi.c

stm32h7xx\_hal\_qspi.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_qspi.c

\* @author MCD Application Team

\* @brief QSPI HAL module driver.

\* This file provides firmware functions to manage the following

\* functionalities of the QuadSPI interface (QSPI).

\* + Initialization and de-initialization functions

\* + Indirect functional mode management

\* + Memory-mapped functional mode management

\* + Auto-polling functional mode management

\* + Interrupts and flags management

```

*          + MDMA channel configuration for indirect functional mode
*          + Errors management and abort functionality
*
*

./Src/stm32h7xx_hal_ramecc.c
stm32h7xx_hal_ramecc.c
/**
*****
* @file      stm32h7xx_hal_ramecc.c
* @author    MCD Application Team
* @brief     RAMECC HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the RAM ECC monitoring (RAMECC) peripheral:
*            + Initialization and de-initialization functions
*            + Monitoring operation functions
*            + Error informations functions
*            + State and error functions

./Src/stm32h7xx_hal_rcc.c
stm32h7xx_hal_rcc.c
/**
*****
* @file      stm32h7xx_hal_rcc.c
* @author    MCD Application Team
* @brief     RCC HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Reset and Clock Control (RCC) peripheral:
*            + Initialization and de-initialization functions
*            + Peripheral Control functions

./Src/stm32h7xx_hal_rcc_ex.c
stm32h7xx_hal_rcc_ex.c
/**
*****
* @file      stm32h7xx_hal_rcc_ex.c
* @author    MCD Application Team
* @brief     Extended RCC HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities RCC extension peripheral:
*            + Extended Peripheral Control functions
*****
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*

```



```

*****
*/

./Src/stm32h7xx_hal_rng.c
stm32h7xx_hal_rng.c
/**
*****
* @file      stm32h7xx_hal_rng.c
* @author    MCD Application Team
* @brief     RNG HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Random Number Generator (RNG) peripheral:
*            + Initialization and configuration functions
*            + Peripheral Control functions
*            + Peripheral State functions
*
./Src/stm32h7xx_hal_rng_ex.c
stm32h7xx_hal_rng_ex.c
/**
*****
* @file      stm32h7xx_hal_rng_ex.c
* @author    MCD Application Team
* @brief     Extended RNG HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Random Number Generator (RNG) peripheral:
*            + Lock configuration functions
*            + Reset the RNG
*
*****
* @attention
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*
*****
*/

./Src/stm32h7xx_hal_rtc.c
stm32h7xx_hal_rtc.c
/**
*****
* @file      stm32h7xx_hal_rtc.c
* @author    MCD Application Team
* @brief     RTC HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Real-Time Clock (RTC) peripheral:
*            + Initialization/de-initialization
*            + Calendar (Time and Date) configuration

```

```

*           + Alarms (Alarm A and Alarm B) configuration
*           + WakeUp Timer configuration
*           + TimeStamp configuration
*           + Tamper configuration
*           + Backup Data Registers configuration
*           + RTC Tamper and TimeStamp Pins Selection
*           + Interrupts and flags management
*

```

./Src/stm32h7xx\_hal\_rtc\_ex.c

stm32h7xx\_hal\_rtc\_ex.c

```

/**
*****
* @file      stm32h7xx_hal_rtc_ex.c
* @author    MCD Application Team
* @brief     Extended RTC HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Real Time Clock (RTC) Extended peripheral:
*            + RTC Time Stamp functions
*            + RTC Tamper functions
*            + RTC Wake-up functions
*            + Extended Control functions
*            + Extended RTC features functions
*

```

./Src/stm32h7xx\_hal\_sai.c

stm32h7xx\_hal\_sai.c

```

/**
*****
* @file      stm32h7xx_hal_sai.c
* @author    MCD Application Team
* @brief     SAI HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Serial Audio Interface (SAI) peripheral:
*            + Initialization/de-initialization functions
*            + I/O operation functions
*            + Peripheral Control functions
*            + Peripheral State functions
*

```

./Src/stm32h7xx\_hal\_sai\_ex.c

stm32h7xx\_hal\_sai\_ex.c

```

/**
*****
* @file      stm32h7xx_hal_sai_ex.c
* @author    MCD Application Team
* @brief     SAI Extended HAL module driver.
*            This file provides firmware functions to manage the following
*            functionality of the SAI Peripheral Controller:
*            + Modify PDM microphone delays.
*
*****
* @attention
*

```

```

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*
*****
*/

./Src/stm32h7xx_hal_sd.c
stm32h7xx_hal_sd.c
/**
*****
* @file      stm32h7xx_hal_sd.c
* @author    MCD Application Team
* @brief     SD card HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the Secure Digital (SD) peripheral:
*
* + Initialization and de-initialization functions
*
* + IO operation functions
*
* + Peripheral Control functions
*
* + Peripheral State functions
*
./Src/stm32h7xx_hal_sdram.c
stm32h7xx_hal_sdram.c
/**
*****
* @file      stm32h7xx_hal_sdram.c
* @author    MCD Application Team
* @brief     SDRAM HAL module driver.
*
* This file provides a generic firmware to drive SDRAM memories
mounted
*
* as external device.
*
./Src/stm32h7xx_hal_sd_ex.c
stm32h7xx_hal_sd_ex.c
/**
*****
* @file      stm32h7xx_hal_sd_ex.c
* @author    MCD Application Team
* @brief     SD card Extended HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the Secure Digital (SD) peripheral:
*
* + Extended features functions
*
./Src/stm32h7xx_hal_smartcard.c
stm32h7xx_hal_smartcard.c
/**
*****

```

```

* @file      stm32h7xx_hal_smartcard.c
* @author    MCD Application Team
* @brief     SMARTCARD HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the SMARTCARD peripheral:
*            + Initialization and de-initialization functions
*            + IO operation functions
*            + Peripheral Control functions
*            + Peripheral State and Error functions
*
./Src/stm32h7xx_hal_smartcard_ex.c
stm32h7xx_hal_smartcard_ex.c
/**
*****
* @file      stm32h7xx_hal_smartcard_ex.c
* @author    MCD Application Team
* @brief     SMARTCARD HAL module driver.
*            This file provides extended firmware functions to manage the
following
*            functionalities of the SmartCard.
*            + Initialization and de-initialization functions
*            + Peripheral Control functions
*
./Src/stm32h7xx_hal_smbus.c
stm32h7xx_hal_smbus.c
/**
*****
* @file      stm32h7xx_hal_smbus.c
* @author    MCD Application Team
* @brief     SMBUS HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the System Management Bus (SMBus) peripheral,
*            based on I2C principles of operation :
*            + Initialization and de-initialization functions
*            + IO operation functions
*            + Peripheral State and Errors functions
*
./Src/stm32h7xx_hal_spdifrx.c
stm32h7xx_hal_spdifrx.c
/**
*****
* @file      stm32h7xx_hal_spdifrx.c
* @author    MCD Application Team
* @brief     This file provides firmware functions to manage the following
*            functionalities of the SPDIFRX audio interface:
*            + Initialization and Configuration
*            + Data transfers functions
*            + DMA transfers management
*            + Interrupts and flags management
./Src/stm32h7xx_hal_spi.c

```

stm32h7xx\_hal\_spi.c

```
/**
 *
 * *****
 * @file      stm32h7xx_hal_spi.c
 * @author    MCD Application Team
 * @brief     SPI HAL module driver.
 *            This file provides firmware functions to manage the following
 *            functionalities of the Serial Peripheral Interface (SPI)
 *            peripheral:
 *            + Initialization and de-initialization functions
 *            + IO operation functions
 *            + Peripheral Control functions
 *            + Peripheral State functions
 *
 */
```

./Src/stm32h7xx\_hal\_spi\_ex.c

stm32h7xx\_hal\_spi\_ex.c

```
/**
 *
 * *****
 * @file      stm32h7xx_hal_spi_ex.c
 * @author    MCD Application Team
 * @brief     Extended SPI HAL module driver.
 *            This file provides firmware functions to manage the following
 *            SPI peripheral extended functionalities :
 *            + IO operation functions
 *            + Peripheral Control functions
 *
 * *****
 * @attention
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 *
 * *****
 */
```

./Src/stm32h7xx\_hal\_sram.c

stm32h7xx\_hal\_sram.c

```
/**
 *
 * *****
 * @file      stm32h7xx_hal_sram.c
 * @author    MCD Application Team
 * @brief     SRAM HAL module driver.
 *            This file provides a generic firmware to drive SRAM memories
 *            mounted as external device.
 *
 */
```

./Src/stm32h7xx\_hal\_swpmi.c

stm32h7xx\_hal\_swpmi.c

```

/**
*****
* @file      stm32h7xx_hal_swpmi.c
* @author    MCD Application Team
* @brief     SWPMI HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Single Wire Protocol Master Interface
(SWPMI).
*            + Initialization and Configuration
*            + Data transfers functions
*            + DMA transfers management
*            + Interrupts and flags management

./Src/stm32h7xx_hal_tim.c
stm32h7xx_hal_tim.c
/**
*****
* @file      stm32h7xx_hal_tim.c
* @author    MCD Application Team
* @brief     TIM HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Timer (TIM) peripheral:
*            + TIM Time Base Initialization
*            + TIM Time Base Start
*            + TIM Time Base Start Interruption
*            + TIM Time Base Start DMA
*            + TIM Output Compare/PWM Initialization
*            + TIM Output Compare/PWM Channel Configuration
*            + TIM Output Compare/PWM Start
*            + TIM Output Compare/PWM Start Interruption
*            + TIM Output Compare/PWM Start DMA
*            + TIM Input Capture Initialization
*            + TIM Input Capture Channel Configuration
*            + TIM Input Capture Start
*            + TIM Input Capture Start Interruption
*            + TIM Input Capture Start DMA
*            + TIM One Pulse Initialization
*            + TIM One Pulse Channel Configuration
*            + TIM One Pulse Start
*            + TIM Encoder Interface Initialization
*            + TIM Encoder Interface Start
*            + TIM Encoder Interface Start Interruption
*            + TIM Encoder Interface Start DMA
*            + Commutation Event configuration with Interruption and DMA
*            + TIM OCRef clear configuration
*            + TIM External Clock configuration

./Src/stm32h7xx_hal_tim_ex.c
stm32h7xx_hal_tim_ex.c
/**
*****
* @file      stm32h7xx_hal_tim_ex.c
* @author    MCD Application Team
* @brief     TIM HAL module driver.

```

```

*          This file provides firmware functions to manage the following
*          functionalities of the Timer Extended peripheral:
*          + Time Hall Sensor Interface Initialization
*          + Time Hall Sensor Interface Start
*          + Time Complementary signal break and dead time configuration
*          + Time Master and Slave synchronization configuration
*          + Time Output Compare/PWM Channel Configuration (for channels 5
and 6)
*          + Timer remapping capabilities configuration

```

```

./Src/stm32h7xx_hal_uart.c
stm32h7xx_hal_uart.c

```

```

/**
*****
* @file      stm32h7xx_hal_uart.c
* @author    MCD Application Team
* @brief     UART HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Universal Asynchronous Receiver Transmitter
Peripheral (UART).
*            + Initialization and de-initialization functions
*            + IO operation functions
*            + Peripheral Control functions
*

```

```

./Src/stm32h7xx_hal_uart_ex.c
stm32h7xx_hal_uart_ex.c

```

```

/**
*****
* @file      stm32h7xx_hal_uart_ex.c
* @author    MCD Application Team
* @brief     Extended UART HAL module driver.
*            This file provides firmware functions to manage the following
extended
*            functionalities of the Universal Asynchronous Receiver Transmitter
Peripheral (UART).
*            + Initialization and de-initialization functions
*            + Peripheral Control functions
*

```

```

./Src/stm32h7xx_hal_usart.c
stm32h7xx_hal_usart.c

```

```

/**
*****
* @file      stm32h7xx_hal_usart.c
* @author    MCD Application Team
* @brief     USART HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Universal Synchronous/Asynchronous Receiver
Transmitter
*            Peripheral (USART).
*            + Initialization and de-initialization functions

```

```

*          + IO operation functions
*          + Peripheral Control functions
*          + Peripheral State and Error functions
*

./Src/stm32h7xx_hal_usart_ex.c
stm32h7xx_hal_usart_ex.c
/**
*****
* @file      stm32h7xx_hal_usart_ex.c
* @author    MCD Application Team
* @brief     Extended USART HAL module driver.
*            This file provides firmware functions to manage the following
extended
*            functionalities of the Universal Synchronous Receiver Transmitter
Peripheral (USART).
*            + Peripheral Control functions
*
*

./Src/stm32h7xx_hal_wwdg.c
stm32h7xx_hal_wwdg.c
/**
*****
* @file      stm32h7xx_hal_wwdg.c
* @author    MCD Application Team
* @brief     WWDG HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Window Watchdog (WWDG) peripheral:
*            + Initialization and Configuration functions
*            + IO operation functions

./Src/stm32h7xx_ll_adc.c
stm32h7xx_ll_adc.c
/**
*****
* @file      stm32h7xx_ll_adc.c
* @author    MCD Application Team
* @brief     ADC LL module driver
*****
* @attention
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*
*****
*/

./Src/stm32h7xx_ll_bdma.c

```



stm32h7xx\_ll\_bdma.c

```
/**
 *
 * *****
 * @file      stm32h7xx_ll_bdma.c
 * @author    MCD Application Team
 * @brief     BDMA LL module driver.
 * *****
 * @attention
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 *
 * *****
 */
```

./Src/stm32h7xx\_ll\_comp.c

stm32h7xx\_ll\_comp.c

```
/**
 *
 * *****
 * @file      stm32h7xx_ll_comp.c
 * @author    MCD Application Team
 * @brief     COMP LL module driver
 * *****
 * @attention
 *
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 *
 * *****
 */
```

./Src/stm32h7xx\_ll\_crc.c

stm32h7xx\_ll\_crc.c

```
/**
 *
 * *****
 * @file      stm32h7xx_ll_crc.c
 * @author    MCD Application Team
 * @brief     CRC LL module driver.
 * *****
 * @attention
 *
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 *
 * *****
 */
```

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*
```

```
*****  
*/
```

```
./Src/stm32h7xx_ll_crs.c  
stm32h7xx_ll_crs.c
```

```
/**  
*****  
* @file    stm32h7xx_ll_crs.h  
* @author  MCD Application Team  
* @brief  CRS LL module driver.  
*****  
* @attention  
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*  
*****  
*/
```

```
./Src/stm32h7xx_ll_dac.c  
stm32h7xx_ll_dac.c
```

```
/**  
*****  
* @file    stm32h7xx_ll_dac.c  
* @author  MCD Application Team  
* @brief  DAC LL module driver  
*****  
* @attention  
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*  
*****  
*/
```

```
./Src/stm32h7xx_ll_delayblock.c  
stm32h7xx_ll_delayblock.c
```

```
/**  
*****
```

```

* @file      stm32h7xx_ll_delayblock.c
* @author    MCD Application Team
* @brief     DelayBlock Low Layer HAL module driver.
*
*           This file provides firmware functions to manage the following
*           functionalities of the Delay Block peripheral:
*           + input clock frequency range 25MHz to 208MHz
*           + up to 12 oversampling phases
*

```

```

./Src/stm32h7xx_ll_dma.c
stm32h7xx_ll_dma.c

```

```

/**
*****
* @file      stm32h7xx_ll_dma.c
* @author    MCD Application Team
* @brief     DMA LL module driver.
*****
* @attention
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*
*****
*/

```

```

./Src/stm32h7xx_ll_dma2d.c
stm32h7xx_ll_dma2d.c

```

```

/**
*****
* @file      stm32h7xx_ll_dma2d.c
* @author    MCD Application Team
* @brief     DMA2D LL module driver.
*****
* @attention
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*****
*/

```

```

./Src/stm32h7xx_ll_exti.c
stm32h7xx_ll_exti.c

```

```

/**
*****
* @file      stm32h7xx_ll_exti.c
* @author    MCD Application Team
* @brief     EXTI LL module driver.
*****
* @attention
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*****
*/

```

./Src/stm32h7xx\_ll\_fmc.c  
stm32h7xx\_ll\_fmc.c

```

/**
*****
* @file      stm32h7xx_ll_fmc.c
* @author    MCD Application Team
* @brief     FMC Low Layer HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the Flexible Memory Controller (FMC) peripheral
memories:
*
*     + Initialization/de-initialization functions
*     + Peripheral Control functions
*     + Peripheral State functions
*

```

./Src/stm32h7xx\_ll\_gpio.c  
stm32h7xx\_ll\_gpio.c

```

/**
*****
* @file      stm32h7xx_ll_gpio.c
* @author    MCD Application Team
* @brief     GPIO LL module driver.
*****
* @attention
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*
*****

```

```

*/

./Src/stm32h7xx_ll_hrtim.c
stm32h7xx_ll_hrtim.c
/**
*****
* @file      stm32h7xx_ll_hrtim.c
* @author    MCD Application Team
* @brief     HRTIM LL module driver.
*****
* @attention
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*****
*/

```

```

./Src/stm32h7xx_ll_i2c.c
stm32h7xx_ll_i2c.c
/**
*****
* @file      stm32h7xx_ll_i2c.c
* @author    MCD Application Team
* @brief     I2C LL module driver.
*****
* @attention
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*****
*/

```

```

./Src/stm32h7xx_ll_lptim.c
stm32h7xx_ll_lptim.c
/**
*****
* @file      stm32h7xx_ll_lptim.c
* @author    MCD Application Team
* @brief     LPTIM LL module driver.
*****
* @attention
*

```

```

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*****
*/

./Src/stm32h7xx_ll_lpuart.c
stm32h7xx_ll_lpuart.c
/**
*****
* @file      stm32h7xx_ll_lpuart.c
* @author    MCD Application Team
* @brief     LPUART LL module driver.
*****
* @attention
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*
*****
*/

./Src/stm32h7xx_ll_mdma.c
stm32h7xx_ll_mdma.c
/**
*****
* @file      stm32h7xx_ll_mdma.c
* @author    MCD Application Team
* @brief     MDMA LL module driver.
*****
* @attention
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*
*****
*/

./Src/stm32h7xx_ll_opamp.c

```

stm32h7xx\_ll\_opamp.c

```
/**
*****
* @file      stm32h7xx_ll_opamp.c
* @author    MCD Application Team
* @brief     OPAMP LL module driver
*****
* @attention
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*
*****
*/
```

./Src/stm32h7xx\_ll\_pwr.c

stm32h7xx\_ll\_pwr.c

```
/**
*****
* @file      stm32h7xx_ll_pwr.c
* @author    MCD Application Team
* @brief     PWR LL module driver.
*****
* @attention
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*
*****
*/
```

./Src/stm32h7xx\_ll\_rcc.c

stm32h7xx\_ll\_rcc.c

```
/**
*****
* @file      stm32h7xx_ll_rcc.c
* @author    MCD Application Team
* @brief     RCC LL module driver.
*****
* @attention
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*
*****
```

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*
```

```
*****  
*/
```

```
./Src/stm32h7xx_ll_rng.c  
stm32h7xx_ll_rng.c
```

```
/**  
*****  
* @file    stm32h7xx_ll_rng.c  
* @author  MCD Application Team  
* @brief  RNG LL module driver.  
*****  
* @attention  
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*  
*****  
*/
```

```
./Src/stm32h7xx_ll_rtc.c  
stm32h7xx_ll_rtc.c
```

```
/**  
*****  
* @file    stm32h7xx_ll_rtc.c  
* @author  MCD Application Team  
* @brief  RTC LL module driver.  
*****  
* @attention  
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*  
*****  
*/
```

```
./Src/stm32h7xx_ll_sdmmc.c  
stm32h7xx_ll_sdmmc.c
```

```
/**  
*****
```



```

* @file      stm32h7xx_ll_sdmmc.c
* @author    MCD Application Team
* @brief     SDMMC Low Layer HAL module driver.
*
*
*           This file provides firmware functions to manage the following
*           functionalities of the SDMMC peripheral:
*
*             + Initialization/de-initialization functions
*             + I/O operation functions
*             + Peripheral Control functions
*             + Peripheral State functions
*

```

./Src/stm32h7xx\_ll\_spi.c

stm32h7xx\_ll\_spi.c

/\*\*

```

*****
* @file      stm32h7xx_ll_spi.c
* @author    MCD Application Team
* @brief     SPI LL module driver.
*****
* @attention
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*
*****
*/

```

./Src/stm32h7xx\_ll\_swpmi.c

stm32h7xx\_ll\_swpmi.c

/\*\*

```

*****
* @file      stm32h7xx_ll_swpmi.c
* @author    MCD Application Team
* @brief     SWPMI LL module driver.
*****
* @attention
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*
*****
*/

```

```
./Src/stm32h7xx_ll_tim.c
stm32h7xx_ll_tim.c
```

```
/**
*****
* @file      stm32h7xx_ll_tim.c
* @author    MCD Application Team
* @brief     TIM LL module driver.
*****
* @attention
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*
*****
*/
```

```
./Src/stm32h7xx_ll_usart.c
stm32h7xx_ll_usart.c
```

```
/**
*****
* @file      stm32h7xx_ll_usart.c
* @author    MCD Application Team
* @brief     USART LL module driver.
*****
* @attention
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*
*****
*/
```

```
./Src/stm32h7xx_ll_usb.c
stm32h7xx_ll_usb.c
```

```
/**
*****
* @file      stm32h7xx_ll_usb.c
* @author    MCD Application Team
* @brief     USB Low Layer HAL module driver.
*
*
* This file provides firmware functions to manage the following
* functionalities of the USB Peripheral Controller:
* + Initialization/de-initialization functions
* + I/O operation functions
*****
```

```

*          + Peripheral Control functions
*          + Peripheral State functions
*

./Src/stm32h7xx_ll_utils.c
stm32h7xx_ll_utils.c
/**
*****
* @file      stm32h7xx_ll_utils.c
* @author    MCD Application Team
* @brief     UTILS LL module driver.
*****
* @attention
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*
*****
*/
/* Includes -----*/

./Src/system_stm32h7xx.c
system_stm32h7xx.c
/**
*****
* @file      system_stm32h7xx_dualcore_boot_cm4_cm7.c
* @author    MCD Application Team
* @brief     CMSIS Cortex-Mx Device Peripheral Access Layer System Source File.
*
* This provides system initialization template function is case of
* an application using a dual core STM32H7 device where
* Cortex-M7 and Cortex-M4 boot are enabled at the FLASH option bytes
*
* This file provides two functions and one global variable to be called from
* user application:
* - SystemInit(): This function is called at startup just after reset and
* before branch to main program. This call is made inside
* the "startup_stm32h7xx.s" file.
*
* - SystemCoreClock variable: Contains the core clock, it can be used
* by the user application to setup the
SysTick
*
* timer or configure other parameters.
*
* - SystemCoreClockUpdate(): Updates the variable SystemCoreClock and
must
*
* be called whenever the core clock is changed
* during program execution.
*
*

```

```

*****
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*
*****
*/

./Inc/Legacy/stm32_hal_legacy.h
stm32_hal_legacy.h
/**
*****
* @file      stm32_hal_legacy.h
* @author    MCD Application Team
* @brief     This file contains aliases definition for the STM32Cube HAL
constants
*
*         macros and functions maintained for legacy purpose.
*****
* @attention
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*****
*/

./Inc/stm32h745xx.h
stm32h745xx.h
/**
*****
* @file      stm32h745xx.h
* @author    MCD Application Team
* @brief     CMSIS STM32H745xx Device Peripheral Access Layer Header File.
*
*
*         This file contains:
*
*         - Data structures and the address mapping for all peripherals
*         - Peripheral's registers declarations and bits definition
*         - Macros to access peripheral's registers hardware
*
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx.h
stm32h7xx.h
/**
*****
* @file      stm32h7xx.h
* @author    MCD Application Team
* @brief     CMSIS STM32H7xx Device Peripheral Access Layer Header File.
*
* The file is the unique include file that the application programmer
* is using in the C source code, usually in main.c. This file
contains:
*      - Configuration section that allows to select:
*      - The STM32H7xx device used in the target application
*      - To use or not the peripheral's drivers in application
code(i.e.
*      code will be based on direct access to peripheral's registers
*      rather than drivers API), this option is controlled by
*      "#define USE_HAL_DRIVER"
*
*****
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*
*****
*/

./Inc/stm32h7xx_hal.h
stm32h7xx_hal.h
/**
*****
* @file      stm32h7xx_hal.h
* @author    MCD Application Team
* @brief     This file contains all the functions prototypes for the HAL
*      module driver.
*****
* @attention
*

```

```

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*
*****
*/

```

./Inc/stm32h7xx\_hal\_adc.h

stm32h7xx\_hal\_adc.h

```

/**
*****
* @file      stm32h7xx_hal_adc.h
* @author    MCD Application Team
* @brief     Header file of ADC HAL module.
*****
* @attention
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*
*****
*/

```

./Inc/stm32h7xx\_hal\_adc\_ex.h

stm32h7xx\_hal\_adc\_ex.h

```

/**
*****
* @file      stm32h7xx_hal_adc_ex.h
* @author    MCD Application Team
* @brief     Header file of ADC HAL extended module.
*****
* @attention
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*
*****
*/

```

./Inc/stm32h7xx\_hal\_cec.h

stm32h7xx\_hal\_cec.h

```
/**
 *
 * *****
 * @file      stm32h7xx_hal_cec.h
 * @author    MCD Application Team
 * @brief     Header file of CEC HAL module.
 * *****
 * @attention
 *
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 *
 * *****
 */
```

./Inc/stm32h7xx\_hal\_comp.h

```
stm32h7xx_hal_comp.h
/**
 *
 * *****
 * @file      stm32h7xx_hal_comp.h
 * @author    MCD Application Team
 * @brief     Header file of COMP HAL module.
 * *****
 * @attention
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 *
 * *****
 */
```

./Inc/stm32h7xx\_hal\_conf.h

```
stm32h7xx_hal_conf.h
/**
 *
 * *****
 * @file      stm32h7xx_hal_conf.h
 * @author    MCD Application Team
 * @brief     HAL configuration file.
 * *****
 * @attention
 *
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 *
 * *****
 */
```

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*
```

```
*****  
*/
```

```
./Inc/stm32h7xx_hal_cortex.h  
stm32h7xx_hal_cortex.h
```

```
/**  
*****  
* @file    stm32h7xx_hal_cortex.h  
* @author  MCD Application Team  
* @brief  Header file of CORTEX HAL module.  
*****  
* @attention  
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*  
*****  
*/
```

```
./Inc/stm32h7xx_hal_crc.h  
stm32h7xx_hal_crc.h
```

```
/**  
*****  
* @file    stm32h7xx_hal_crc.h  
* @author  MCD Application Team  
* @brief  Header file of CRC HAL module.  
*****  
* @attention  
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*  
*****  
*/
```

```
./Inc/stm32h7xx_hal_crc_ex.h  
stm32h7xx_hal_crc_ex.h
```

```
/**  
*****
```



```

* @file    stm32h7xx_hal_crc_ex.h
* @author  MCD Application Team
* @brief   Header file of CRC HAL extended module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_cryp.h
stm32h7xx_hal_cryp.h
/**
*****
* @file    stm32h7xx_hal_cryp.h
* @author  MCD Application Team
* @brief   Header file of CRYPT HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_cryp_ex.h
stm32h7xx_hal_cryp_ex.h
/**
*****
* @file    stm32h7xx_hal_cryp_ex.h
* @author  MCD Application Team
* @brief   Header file of CRYPT HAL Extension module.
*****
* @attention
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```

```

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*
*****
*/

./Inc/stm32h7xx_hal_dac.h
stm32h7xx_hal_dac.h
/**
*****
* @file      stm32h7xx_hal_dac.h
* @author    MCD Application Team
* @brief     Header file of DAC HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_dac_ex.h
stm32h7xx_hal_dac_ex.h
/**
*****
* @file      stm32h7xx_hal_dac_ex.h
* @author    MCD Application Team
* @brief     Header file of DAC HAL Extended module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_dcmi.h
stm32h7xx_hal_dcmi.h
/**
*****
* @file      stm32h7xx_hal_dcmi.h
* @author    MCD Application Team
* @brief     Header file of DCMI HAL module.

```

```

*****
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*
*****
*/

```

./Inc/stm32h7xx\_hal\_def.h

stm32h7xx\_hal\_def.h

```

/**
*****
* @file      stm32h7xx_hal_def.h
* @author    MCD Application Team
* @brief     This file contains HAL common defines, enumeration, macros and
*            structures definitions.
*****
* @attention
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*
*****
*/

```

./Inc/stm32h7xx\_hal\_dfstdm.h

stm32h7xx\_hal\_dfstdm.h

```

/**
*****
* @file      stm32h7xx_hal_dfstdm.h
* @author    MCD Application Team
* @brief     Header file of DFSDM HAL module.
*****
* @attention
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*

```

```

*****
*/

./Inc/stm32h7xx_hal_dfldm_ex.h
stm32h7xx_hal_dfldm_ex.h
/**
*****
* @file    stm32h7xx_hal_dfldm_ex.h
* @author  MCD Application Team
* @brief   Header file of DFSDM HAL extended module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_dma.h
stm32h7xx_hal_dma.h
/**
*****
* @file    stm32h7xx_hal_dma.h
* @author  MCD Application Team
* @brief   Header file of DMA HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_dma2d.h
stm32h7xx_hal_dma2d.h
/**
*****
* @file    stm32h7xx_hal_dma2d.h
* @author  MCD Application Team
* @brief   Header file of DMA2D HAL module.
*****
* @attention

```

```

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*
*****
*/

./Inc/stm32h7xx_hal_dma_ex.h
stm32h7xx_hal_dma_ex.h
/**
*****
* @file      stm32h7xx_hal_dma_ex.h
* @author    MCD Application Team
* @brief     Header file of DMA HAL extension module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_dsi.h
stm32h7xx_hal_dsi.h
/**
*****
* @file      stm32h7xx_hal_dsi.h
* @author    MCD Application Team
* @brief     Header file of DSI HAL module.
*****
* @attention
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*
*****
*/

```

./Inc/stm32h7xx\_hal\_dts.h  
stm32h7xx\_hal\_dts.h

```
/**
*****
* @file      stm32h7xx_hal_dts.h
* @author    MCD Application Team
* @brief     Header file of DTS HAL module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_eth.h  
stm32h7xx\_hal\_eth.h

```
/**
*****
* @file      stm32h7xx_hal_eth.h
* @author    MCD Application Team
* @brief     Header file of ETH HAL module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_eth\_ex.h  
stm32h7xx\_hal\_eth\_ex.h

```
/**
*****
* @file      stm32h7xx_hal_eth_ex.h
* @author    MCD Application Team
* @brief     Header file of ETH HAL Extended module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_exti.h
stm32h7xx_hal_exti.h
/**
*****
* @file      stm32h7xx_hal_exti.h
* @author    MCD Application Team
* @brief     Header file of EXTI HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_fdcan.h
stm32h7xx_hal_fdcan.h
/**
*****
* @file      stm32h7xx_hal_fdcan.h
* @author    MCD Application Team
* @brief     Header file of FDCAN HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_flash.h
stm32h7xx_hal_flash.h
/**

```

```

*****
* @file      stm32h7xx_hal_flash.h
* @author    MCD Application Team
* @brief     Header file of FLASH HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_flash_ex.h
stm32h7xx_hal_flash_ex.h
/**
*****
* @file      stm32H7xx_hal_flash_ex.h
* @author    MCD Application Team
* @brief     Header file of FLASH HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_gfxmmu.h
stm32h7xx_hal_gfxmmu.h
/**
*****
* @file      stm32h7xx_hal_gfxmmu.h
* @author    MCD Application Team
* @brief     Header file of GFXMMU HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_gpio.h
stm32h7xx_hal_gpio.h
/**
*****
* @file      stm32h7xx_hal_gpio.h
* @author    MCD Application Team
* @brief     Header file of GPIO HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_gpio_ex.h
stm32h7xx_hal_gpio_ex.h
/**
*****
* @file      stm32h7xx_hal_gpio_ex.h
* @author    MCD Application Team
* @brief     Header file of GPIO HAL Extension module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_hash.h
stm32h7xx_hal_hash.h
/**
*****
* @file      stm32h7xx_hal_hash.h
* @author    MCD Application Team

```

```

* @brief    Header file of HASH HAL module.
*****
* @attention
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*
*****
*/

```

./Inc/stm32h7xx\_hal\_hash\_ex.h

stm32h7xx\_hal\_hash\_ex.h

```

/**
*****
* @file      stm32h7xx_hal_hash_ex.h
* @author    MCD Application Team
* @brief     Header file of HASH HAL module.
*****
* @attention
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*
*****
*/

```

./Inc/stm32h7xx\_hal\_hcd.h

stm32h7xx\_hal\_hcd.h

```

/**
*****
* @file      stm32h7xx_hal_hcd.h
* @author    MCD Application Team
* @brief     Header file of HCD HAL module.
*****
* @attention
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```

```

*****
*/

./Inc/stm32h7xx_hal_hrtim.h
stm32h7xx_hal_hrtim.h
/**
*****
* @file    stm32h7xx_hal_hrtim.h
* @author  MCD Application Team
* @brief   Header file of HRTIM HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_hsem.h
stm32h7xx_hal_hsem.h
/**
*****
* @file    stm32h7xx_hal_hsem.h
* @author  MCD Application Team
* @brief   Header file of HSEM HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_i2c.h
stm32h7xx_hal_i2c.h
/**
*****
* @file    stm32h7xx_hal_i2c.h
* @author  MCD Application Team
* @brief   Header file of I2C HAL module.
*****
* @attention

```

```

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*****
*/

./Inc/stm32h7xx_hal_i2c_ex.h
stm32h7xx_hal_i2c_ex.h
/**
*****
* @file      stm32h7xx_hal_i2c_ex.h
* @author    MCD Application Team
* @brief     Header file of I2C HAL Extended module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_i2s.h
stm32h7xx_hal_i2s.h
/**
*****
* @file      stm32h7xx_hal_i2s.h
* @author    MCD Application Team
* @brief     Header file of I2S HAL module.
*****
* @attention
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*****
*/

```

./Inc/stm32h7xx\_hal\_i2s\_ex.h  
stm32h7xx\_hal\_i2s\_ex.h

```
/**
*****
* @file      stm32h7xx_hal_i2s_ex.h
* @author    MCD Application Team
* @brief     Header file of I2S HAL module.
*****
* @attention
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*****
*/
```

./Inc/stm32h7xx\_hal\_irda.h  
stm32h7xx\_hal\_irda.h

```
/**
*****
* @file      stm32h7xx_hal_irda.h
* @author    MCD Application Team
* @brief     Header file of IRDA HAL module.
*****
* @attention
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*****
*/
```

./Inc/stm32h7xx\_hal\_irda\_ex.h  
stm32h7xx\_hal\_irda\_ex.h

```
/**
*****
* @file      stm32h7xx_hal_irda_ex.h
* @author    MCD Application Team
* @brief     Header file of IRDA HAL Extended module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_iwdg.h
stm32h7xx_hal_iwdg.h
/**
*****
* @file      stm32h7xx_hal_iwdg.h
* @author    MCD Application Team
* @brief     Header file of IWDG HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_jpeg.h
stm32h7xx_hal_jpeg.h
/**
*****
* @file      stm32h7xx_hal_jpeg.h
* @author    MCD Application Team
* @brief     Header file of JPEG HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_lptim.h
stm32h7xx_hal_lptim.h
/**

```

```

*****
* @file      stm32h7xx_hal_lptim.h
* @author    MCD Application Team
* @brief     Header file of LPTIM HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_ltdc.h
stm32h7xx_hal_ltdc.h
/**
*****
* @file      stm32h7xx_hal_ltdc.h
* @author    MCD Application Team
* @brief     Header file of LTDC HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_ltdc_ex.h
stm32h7xx_hal_ltdc_ex.h
/**
*****
* @file      stm32h7xx_hal_ltdc_ex.h
* @author    MCD Application Team
* @brief     Header file of LTDC HAL Extension module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_mdios.h
stm32h7xx_hal_mdios.h
/**
*****
* @file      stm32h7xx_hal_mdios.h
* @author    MCD Application Team
* @brief     Header file of MDIOS HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_mdma.h
stm32h7xx_hal_mdma.h
/**
*****
* @file      stm32h7xx_hal_mdma.h
* @author    MCD Application Team
* @brief     Header file of DMA HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_mmc.h
stm32h7xx_hal_mmc.h
/**
*****
* @file      stm32h7xx_hal_mmc.h
* @author    MCD Application Team

```



```

* @brief   Header file of MMC HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_mmc_ex.h
stm32h7xx_hal_mmc_ex.h
/**
*****
* @file     stm32h7xx_hal_mmc_ex.h
* @author    MCD Application Team
* @brief     Header file of SD HAL extended module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_nand.h
stm32h7xx_hal_nand.h
/**
*****
* @file     stm32h7xx_hal_nand.h
* @author    MCD Application Team
* @brief     Header file of NAND HAL module.
*****
* @attention
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*

```

```

*****
*/

./Inc/stm32h7xx_hal_nor.h
stm32h7xx_hal_nor.h
/**
*****
* @file      stm32h7xx_hal_nor.h
* @author    MCD Application Team
* @brief     Header file of NOR HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_opamp.h
stm32h7xx_hal_opamp.h
/**
*****
* @file      stm32h7xx_hal_opamp.h
* @author    MCD Application Team
* @brief     Header file of OPAMP HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_opamp_ex.h
stm32h7xx_hal_opamp_ex.h
/**
*****
* @file      stm32h7xx_hal_opamp_ex.h
* @author    MCD Application Team
* @brief     Header file of OPAMP HAL Extended module.
*****
* @attention

```

```

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*****
*/

./Inc/stm32h7xx_hal_ospi.h
stm32h7xx_hal_ospi.h
/**
*****
* @file      stm32h7xx_hal_ospi.h
* @author    MCD Application Team
* @brief     Header file of OSPI HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_otfdec.h
stm32h7xx_hal_otfdec.h
/**
*****
* @file      stm32h7xx_hal_otfdec.h
* @author    MCD Application Team
* @brief     Header file of OTFDEC HAL module.
*****
* @attention
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*
*****
*/

```

./Inc/stm32h7xx\_hal\_pcd.h  
stm32h7xx\_hal\_pcd.h

```
/**
*****
* @file      stm32h7xx_hal_pcd.h
* @author    MCD Application Team
* @brief     Header file of PCD HAL module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_pcd\_ex.h  
stm32h7xx\_hal\_pcd\_ex.h

```
/**
*****
* @file      stm32h7xx_hal_pcd_ex.h
* @author    MCD Application Team
* @brief     Header file of PCD HAL Extension module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_hal\_pssi.h  
stm32h7xx\_hal\_pssi.h

```
/**
*****
* @file      stm32h7xx_hal_pssi.h
* @author    MCD Application Team
* @brief     Header file of PSSI HAL module.
*****
* @attention
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```

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*
*****
*/

./Inc/stm32h7xx_hal_pwr.h
stm32h7xx_hal_pwr.h
/**
*****
* @file      stm32h7xx_hal_pwr.h
* @author    MCD Application Team
* @brief     Header file of PWR HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_pwr_ex.h
stm32h7xx_hal_pwr_ex.h
/**
*****
* @file      stm32h7xx_hal_pwr_ex.h
* @author    MCD Application Team
* @brief     Header file of PWR HAL Extension module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_qspi.h
stm32h7xx_hal_qspi.h
/**

```

```

*****
* @file      stm32h7xx_hal_qspi.h
* @author    MCD Application Team
* @brief     Header file of QSPI HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_ramecc.h
stm32h7xx_hal_ramecc.h
/**
*****
* @file      stm32h7xx_hal_ramecc.h
* @author    MCD Application Team
* @brief     Header file of RAMECC HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_rcc.h
stm32h7xx_hal_rcc.h
/**
*****
* @file      stm32h7xx_hal_rcc.h
* @author    MCD Application Team
* @brief     Header file of RCC HAL module.
*****
* @attention
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*
*****
*/
```

```
./Inc/stm32h7xx_hal_rcc_ex.h
```

```
stm32h7xx_hal_rcc_ex.h
```

```
/**
*****
* @file      stm32h7xx_hal_rcc_ex.h
* @author    MCD Application Team
* @brief     Header file of RCC HAL Extension module.
*****
* @attention
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*
*****
*/
```

```
./Inc/stm32h7xx_hal_rng.h
```

```
stm32h7xx_hal_rng.h
```

```
/**
*****
* @file      stm32h7xx_hal_rng.h
* @author    MCD Application Team
* @brief     Header file of RNG HAL module.
*****
* @attention
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*
*****
*/
```

```
./Inc/stm32h7xx_hal_rng_ex.h
```

```
stm32h7xx_hal_rng_ex.h
```

```
/**
*****
* @file      stm32h7xx_hal_rng_ex.h
* @author    MCD Application Team
```

```

* @brief   Header file of RNG HAL Extension module.
*****
* @attention
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*
*****
*/

```

```

./Inc/stm32h7xx_hal_rtc.h
stm32h7xx_hal_rtc.h

```

```

/**
*****
* @file     stm32h7xx_hal_rtc.h
* @author   MCD Application Team
* @brief    Header file of RTC HAL module.
*****
* @attention
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*
*****
*/

```

```

./Inc/stm32h7xx_hal_rtc_ex.h
stm32h7xx_hal_rtc_ex.h

```

```

/**
*****
* @file     stm32h7xx_hal_rtc_ex.h
* @author   MCD Application Team
* @brief    Header file of RTC HAL Extension module.
*****
* @attention
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*

```



```

*****
*/

./Inc/stm32h7xx_hal_sai.h
stm32h7xx_hal_sai.h
/**
*****
* @file      stm32h7xx_hal_sai.h
* @author    MCD Application Team
* @brief     Header file of SAI HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_sai_ex.h
stm32h7xx_hal_sai_ex.h
/**
*****
* @file      stm32h7xx_hal_sai_ex.h
* @author    MCD Application Team
* @brief     Header file of SAI HAL extended module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_sd.h
stm32h7xx_hal_sd.h
/**
*****
* @file      stm32h7xx_hal_sd.h
* @author    MCD Application Team
* @brief     Header file of SD HAL module.
*****
* @attention

```

```

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*
*****
*/

./Inc/stm32h7xx_hal_sdram.h
stm32h7xx_hal_sdram.h
/**
*****
* @file      stm32h7xx_hal_sdram.h
* @author    MCD Application Team
* @brief     Header file of SDRAM HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_sd_ex.h
stm32h7xx_hal_sd_ex.h
/**
*****
* @file      stm32h7xx_hal_sd_ex.h
* @author    MCD Application Team
* @brief     Header file of SD HAL extended module.
*****
* @attention
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*
*****
*/

```

```
./Inc/stm32h7xx_hal_smartcard.h
stm32h7xx_hal_smartcard.h
```

```
/**
*****
* @file      stm32h7xx_hal_smartcard.h
* @author    MCD Application Team
* @brief     Header file of SMARTCARD HAL module.
*****
* @attention
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*
*****
*/
```

```
./Inc/stm32h7xx_hal_smartcard_ex.h
stm32h7xx_hal_smartcard_ex.h
```

```
/**
*****
* @file      stm32h7xx_hal_smartcard_ex.h
* @author    MCD Application Team
* @brief     Header file of SMARTCARD HAL Extended module.
*****
* @attention
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*
*****
*/
```

```
./Inc/stm32h7xx_hal_smbus.h
stm32h7xx_hal_smbus.h
```

```
/**
*****
* @file      stm32h7xx_hal_smbus.h
* @author    MCD Application Team
* @brief     Header file of SMBUS HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_spdifrx.h
stm32h7xx_hal_spdifrx.h
/**
*****
* @file      stm32h7xx_hal_spdifrx.h
* @author    MCD Application Team
* @brief     Header file of SPDIFRX HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_spi.h
stm32h7xx_hal_spi.h
/**
*****
* @file      stm32h7xx_hal_spi.h
* @author    MCD Application Team
* @brief     Header file of SPI HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_spi_ex.h
stm32h7xx_hal_spi_ex.h
/**

```

```

*****
* @file      stm32h7xx_hal_spi_ex.h
* @author    MCD Application Team
* @brief     Header file of SPI HAL Extended module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_sram.h
stm32h7xx_hal_sram.h
/**
*****
* @file      stm32h7xx_hal_sram.h
* @author    MCD Application Team
* @brief     Header file of SRAM HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_swpmi.h
stm32h7xx_hal_swpmi.h
/**
*****
* @file      stm32h7xx_hal_swpmi.h
* @author    MCD Application Team
* @brief     Header file of SWPMI HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_tim.h
stm32h7xx_hal_tim.h
/**
*****
* @file      stm32h7xx_hal_tim.h
* @author    MCD Application Team
* @brief     Header file of TIM HAL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_hal_tim_ex.h
stm32h7xx_hal_tim_ex.h
/**
*****
* @file      stm32h7xx_hal_tim_ex.h
* @author    MCD Application Team
* @brief     Header file of TIM HAL Extended module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_uart.h
stm32h7xx_hal_uart.h
/**
*****
* @file      stm32h7xx_hal_uart.h
* @author    MCD Application Team

```

```

* @brief   Header file of UART HAL module.
*****
* @attention
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*****
*/

```

./Inc/stm32h7xx\_hal\_uart\_ex.h

stm32h7xx\_hal\_uart\_ex.h

```

/**
*****
* @file     stm32h7xx_hal_uart_ex.h
* @author   MCD Application Team
* @brief    Header file of UART HAL Extended module.
*****
* @attention
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*****
*/

```

./Inc/stm32h7xx\_hal\_usart.h

stm32h7xx\_hal\_usart.h

```

/**
*****
* @file     stm32h7xx_hal_usart.h
* @author   MCD Application Team
* @brief    Header file of USART HAL module.
*****
* @attention
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*

```

```

*****
*/

./Inc/stm32h7xx_hal_usart_ex.h
stm32h7xx_hal_usart_ex.h
/**
*****
* @file    stm32h7xx_hal_usart_ex.h
* @author  MCD Application Team
* @brief   Header file of USART HAL Extended module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_hal_wwdg.h
stm32h7xx_hal_wwdg.h
/**
*****
* @file    stm32h7xx_hal_wwdg.h
* @author  MCD Application Team
* @brief   Header file of WWDG HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_adc.h
stm32h7xx_ll_adc.h
/**
*****
* @file    stm32h7xx_ll_adc.h
* @author  MCD Application Team
* @brief   Header file of ADC LL module.
*****
* @attention

```



```

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*****
*/

./Inc/stm32h7xx_ll_bdma.h
stm32h7xx_ll_bdma.h
/**
*****
* @file    stm32h7xx_ll_bdma.h
* @author  MCD Application Team
* @brief   Header file of BDMA LL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_ll_bus.h
stm32h7xx_ll_bus.h
/**
*****
* @file    stm32h7xx_ll_bus.h
* @author  MCD Application Team
* @version $VERSION$
* @date    $DATE$
* @brief   Header file of BUS LL module.

./Inc/stm32h7xx_ll_comp.h
stm32h7xx_ll_comp.h
/**
*****
* @file    stm32h7xx_ll_comp.h
* @author  MCD Application Team
* @brief   Header file of COMP LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_cortex.h
stm32h7xx_ll_cortex.h
/**
*****
* @file    stm32h7xx_ll_cortex.h
* @author  MCD Application Team
* @brief   Header file of CORTEX LL module.

./Inc/stm32h7xx_ll_crc.h
stm32h7xx_ll_crc.h
/**
*****
* @file    stm32h7xx_ll_crc.h
* @author  MCD Application Team
* @brief   Header file of CRC LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_crs.h
stm32h7xx_ll_crs.h
/**
*****
* @file    stm32h7xx_ll_crs.h
* @author  MCD Application Team
* @brief   Header file of CRS LL module.
*****
* @attention
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*****
*/

./Inc/stm32h7xx_ll_dac.h
stm32h7xx_ll_dac.h
/**
*****
* @file      stm32h7xx_ll_dac.h
* @author    MCD Application Team
* @brief     Header file of DAC LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_delayblock.h
stm32h7xx_ll_delayblock.h
/**
*****
* @file      stm32h7xx_ll_delayblock.h
* @author    MCD Application Team
* @brief     Header file of Delay Block module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_dma.h
stm32h7xx_ll_dma.h
/**
*****
* @file      stm32h7xx_ll_dma.h
* @author    MCD Application Team

```

```

* @brief   Header file of DMA LL module.
*****
* @attention
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*
*****
*/

```

./Inc/stm32h7xx\_ll\_dma2d.h

stm32h7xx\_ll\_dma2d.h

```

/**
*****
* @file     stm32h7xx_ll_dma2d.h
* @author   MCD Application Team
* @brief    Header file of DMA2D LL module.
*****
* @attention
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*
*****
*/

```

./Inc/stm32h7xx\_ll\_dmamux.h

stm32h7xx\_ll\_dmamux.h

```

/**
*****
* @file     stm32h7xx_ll_dmamux.h
* @author   MCD Application Team
* @brief    Header file of DMAMUX LL module.
*****
* @attention
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*

```

```

*****
*/

./Inc/stm32h7xx_ll_exti.h
stm32h7xx_ll_exti.h
/**
*****
* @file      stm32h7xx_ll_exti.h
* @author    MCD Application Team
* @brief     Header file of EXTI LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_fsmc.h
stm32h7xx_ll_fsmc.h
/**
*****
* @file      stm32h7xx_ll_fsmc.h
* @author    MCD Application Team
* @brief     Header file of FMC HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_gpio.h
stm32h7xx_ll_gpio.h
/**
*****
* @file      stm32h7xx_ll_gpio.h
* @author    MCD Application Team
* @brief     Header file of GPIO LL module.
*****
* @attention

```

```

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*
*****
*/

./Inc/stm32h7xx_ll_hrtim.h
stm32h7xx_ll_hrtim.h
/**
*****
* @file      stm32h7xx_ll_hrtim.h
* @author    MCD Application Team
* @brief     Header file of HRTIM LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_hsem.h
stm32h7xx_ll_hsem.h
/**
*****
* @file      stm32h7xx_ll_hsem.h
* @author    MCD Application Team
* @brief     Header file of HSEM LL module.
*****
* @attention
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*
*****
*/

```

./Inc/stm32h7xx\_ll\_i2c.h  
stm32h7xx\_ll\_i2c.h

```
/**
*****
* @file      stm32h7xx_ll_i2c.h
* @author    MCD Application Team
* @brief     Header file of I2C LL module.
*****
* @attention
*
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*
*****
*/
```

./Inc/stm32h7xx\_ll\_iwdg.h  
stm32h7xx\_ll\_iwdg.h

```
/**
*****
* @file      stm32h7xx_ll_iwdg.h
* @author    MCD Application Team
* @brief     Header file of IWDG LL module.
*****
* @attention
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*
*****
*/
```

./Inc/stm32h7xx\_ll\_lptim.h  
stm32h7xx\_ll\_lptim.h

```
/**
*****
* @file      stm32h7xx_ll_lptim.h
* @author    MCD Application Team
* @brief     Header file of LPTIM LL module.
*****
* @attention
*
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```

```

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*
*****
*/

./Inc/stm32h7xx_ll_lpuart.h
stm32h7xx_ll_lpuart.h
/**
*****
* @file      stm32h7xx_ll_lpuart.h
* @author    MCD Application Team
* @brief     Header file of LPUART LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_mdma.h
stm32h7xx_ll_mdma.h
/**
*****
* @file      stm32h7xx_ll_mdma.h
* @author    MCD Application Team
* @brief     Header file of MDMA LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_opamp.h
stm32h7xx_ll_opamp.h
/**

```



```

*****
* @file      stm32h7xx_ll_opamp.h
* @author    MCD Application Team
* @brief     Header file of OPAMP LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_pwr.h
stm32h7xx_ll_pwr.h
/**
*****
* @file      stm32h7xx_ll_pwr.h
* @author    MCD Application Team
* @brief     Header file of PWR LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_rcc.h
stm32h7xx_ll_rcc.h
/**
*****
* @file      stm32h7xx_ll_rcc.h
* @author    MCD Application Team
* @version   $VERSION$
* @date      $DATE$
* @brief     Header file of RCC LL module.
*****
* @attention
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```

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*
```

```
*****  
*/
```

```
./Inc/stm32h7xx_ll_rng.h  
stm32h7xx_ll_rng.h
```

```
/**  
*****  
* @file    stm32h7xx_ll_rng.h  
* @author  MCD Application Team  
* @brief  Header file of RNG LL module.  
*****  
* @attention  
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*  
*****  
*/
```

```
./Inc/stm32h7xx_ll_rtc.h  
stm32h7xx_ll_rtc.h
```

```
/**  
*****  
* @file    stm32h7xx_ll_rtc.h  
* @author  MCD Application Team  
* @brief  Header file of RTC LL module.  
*****  
* @attention  
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*  
*****  
*/
```

```
./Inc/stm32h7xx_ll_sdmmc.h  
stm32h7xx_ll_sdmmc.h
```

```
/**  
*****
```

```

* @file    stm32h7xx_ll_sdmmc.h
* @author  MCD Application Team
* @brief   Header file of SDMMC HAL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_spi.h
stm32h7xx_ll_spi.h
/**
*****
* @file    stm32h7xx_ll_spi.h
* @author  MCD Application Team
* @brief   Header file of SPI LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_swpmi.h
stm32h7xx_ll_swpmi.h
/**
*****
* @file    stm32h7xx_ll_swpmi.h
* @author  MCD Application Team
* @brief   Header file of SWPMI LL module.
*****
* @attention
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```

```

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*
*****
*/

./Inc/stm32h7xx_ll_system.h
stm32h7xx_ll_system.h
/**
*****
* @file    stm32h7xx_ll_system.h
* @author  MCD Application Team
* @brief   Header file of SYSTEM LL module.

./Inc/stm32h7xx_ll_tim.h
stm32h7xx_ll_tim.h
/**
*****
* @file    stm32h7xx_ll_tim.h
* @author  MCD Application Team
* @brief   Header file of TIM LL module.
*****
* @attention
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*
*****
*/

./Inc/stm32h7xx_ll_usart.h
stm32h7xx_ll_usart.h
/**
*****
* @file    stm32h7xx_ll_usart.h
* @author  MCD Application Team
* @brief   Header file of USART LL module.
*****
* @attention
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*
*****
*/

```

./Inc/stm32h7xx\_ll\_usb.h

stm32h7xx\_ll\_usb.h

```
/**
 *
 * *****
 * @file      stm32h7xx_ll_usb.h
 * @author    MCD Application Team
 * @brief     Header file of USB Low Layer HAL module.
 * *****
 * @attention
 *
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 *
 * *****
 */
```

./Inc/stm32h7xx\_ll\_utils.h

stm32h7xx\_ll\_utils.h

```
/**
 *
 * *****
 * @file      stm32h7xx_ll_utils.h
 * @author    MCD Application Team
 * @brief     Header file of UTILS LL module.
 *
 * *****
 */
```

./Inc/stm32h7xx\_ll\_wwdg.h

stm32h7xx\_ll\_wwdg.h

```
/**
 *
 * *****
 * @file      stm32h7xx_ll_wwdg.h
 * @author    MCD Application Team
 * @brief     Header file of WWDG LL module.
 * *****
 * @attention
 *
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 *
 * *****
 */
```

./Inc/stm32\_assert\_template.h

stm32\_assert\_template.h

```
/**
```

```

*****
* @file      stm32_assert.h
* @author    MCD Application Team
* @brief     STM32 assert template file.
*            This file should be copied to the application folder and renamed
*            to stm32_assert.h.
*****
* @attention
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*
*****
*/

./Inc/system_stm32h7xx.h
system_stm32h7xx.h
/**
*****
* @file      system_stm32h7xx.h
* @author    MCD Application Team
* @brief     CMSIS Cortex-Mx Device System Source File for STM32H7xx devices.
*****
* @attention
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*
*****
*/

./Src/stm32h7xx_hal.c
stm32h7xx_hal.c
/**
*****
* @file      stm32h7xx_hal.c
* @author    MCD Application Team
* @brief     HAL module driver.
*            This is the common part of the HAL initialization
*
./Src/stm32h7xx_hal_adc.c
stm32h7xx_hal_adc.c
/**

```

```

*****
* @file      stm32h7xx_hal_adc.c
* @author    MCD Application Team
* @brief     This file provides firmware functions to manage the following
*            functionalities of the Analog to Digital Converter (ADC)
*            peripheral:
*            + Initialization and de-initialization functions
*              ++ Initialization and Configuration of ADC
*            + Operation functions
*              ++ Start, stop, get result of conversions of regular
*                group, using 3 possible modes: polling, interruption or DMA.
*            + Control functions
*              ++ Channels configuration on regular group
*              ++ Analog Watchdog configuration
*            + State functions
*              ++ ADC state machine management
*              ++ Interrupts and flags management
*            Other functions (extended functions) are available in file
*            "stm32h7xx_hal_adc_ex.c".
*
./Src/stm32h7xx_hal_adc_ex.c
stm32h7xx_hal_adc_ex.c
/**
*****
* @file      stm32h7xx_hal_adc_ex.c
* @author    MCD Application Team
* @brief     This file provides firmware functions to manage the following
*            functionalities of the Analog to Digital Converter (ADC)
*            peripheral:
*            + Operation functions
*              ++ Start, stop, get result of conversions of ADC group injected,
*                using 2 possible modes: polling, interruption.
*              ++ Calibration
*                +++ ADC automatic self-calibration
*                +++ Calibration factors get or set
*              ++ Multimode feature when available
*            + Control functions
*              ++ Channels configuration on ADC group injected
*            + State functions
*              ++ ADC group injected contexts queue management
*            Other functions (generic functions) are available in file
*            "stm32h7xx_hal_adc.c".
*
./Src/stm32h7xx_hal_cec.c
stm32h7xx_hal_cec.c
/**
*****
* @file      stm32h7xx_hal_cec.c
* @author    MCD Application Team
* @brief     CEC HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the High Definition Multimedia Interface

```

```

*      Consumer Electronics Control Peripheral (CEC).
*      + Initialization and de-initialization function
*      + IO operation function
*      + Peripheral Control function
*
*

```

./Src/stm32h7xx\_hal\_comp.c

stm32h7xx\_hal\_comp.c

```

/**
*****
* @file      stm32h7xx_hal_comp.c
* @author    MCD Application Team
* @brief     COMP HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the COMP peripheral:
*
* + Initialization and de-initialization functions
* + Start/Stop operation functions in polling mode
* + Start/Stop operation functions in interrupt mode
* + Peripheral control functions
* + Peripheral state functions

```

./Src/stm32h7xx\_hal\_cortex.c

stm32h7xx\_hal\_cortex.c

```

/**
*****
* @file      stm32h7xx_hal_cortex.c
* @author    MCD Application Team
* @brief     CORTEX HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the CORTEX:
*
* + Initialization and de-initialization functions
* + Peripheral Control functions
*

```

./Src/stm32h7xx\_hal\_crc.c

stm32h7xx\_hal\_crc.c

```

/**
*****
* @file      stm32h7xx_hal_crc.c
* @author    MCD Application Team
* @brief     CRC HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the Cyclic Redundancy Check (CRC) peripheral:
*
* + Initialization and de-initialization functions
* + Peripheral Control functions
* + Peripheral State functions
*

```

./Src/stm32h7xx\_hal\_crc\_ex.c

stm32h7xx\_hal\_crc\_ex.c

```

/**
*****
* @file      stm32h7xx_hal_crc_ex.c

```



```

* @author MCD Application Team
* @brief Extended CRC HAL module driver.
* This file provides firmware functions to manage the extended
* functionalities of the CRC peripheral.
*

```

./Src/stm32h7xx\_hal\_cryp.c

stm32h7xx\_hal\_cryp.c

/\*\*

\*\*\*\*\*

```

* @file stm32h7xx_hal_cryp.c
* @author MCD Application Team
* @brief CRYP HAL module driver.
* This file provides firmware functions to manage the following
* functionalities of the Cryptography (CRYP) peripheral:
* + Initialization and de-initialization functions
* + AES processing functions
* + DES processing functions
* + TDES processing functions
* + DMA callback functions
* + CRYP IRQ handler management
* + Peripheral State functions
*

```

./Src/stm32h7xx\_hal\_cryp\_ex.c

stm32h7xx\_hal\_cryp\_ex.c

/\*\*

\*\*\*\*\*

```

* @file stm32h7xx_hal_cryp_ex.c
* @author MCD Application Team
* @brief Extended CRYP HAL module driver
* This file provides firmware functions to manage the following
* functionalities of CRYP extension peripheral:
* + Extended AES processing functions
*

```

./Src/stm32h7xx\_hal\_dac.c

stm32h7xx\_hal\_dac.c

/\*\*

\*\*\*\*\*

```

* @file stm32h7xx_hal_dac.c
* @author MCD Application Team
* @brief DAC HAL module driver.
* This file provides firmware functions to manage the following
* functionalities of the Digital to Analog Converter (DAC) peripheral:
* + Initialization and de-initialization functions
* + IO operation functions
* + Peripheral Control functions
* + Peripheral State and Errors functions
*

```

./Src/stm32h7xx\_hal\_dac\_ex.c

stm32h7xx\_hal\_dac\_ex.c

```

/**
*****
* @file      stm32h7xx_hal_dac_ex.c
* @author    MCD Application Team
* @brief     Extended DAC HAL module driver.
*            This file provides firmware functions to manage the extended
*            functionalities of the DAC peripheral.
*
*
*/

./Src/stm32h7xx_hal_dcmi.c
stm32h7xx_hal_dcmi.c
/**
*****
* @file      stm32h7xx_hal_dcmi.c
* @author    MCD Application Team
* @brief     DCMI HAL module driver
*            This file provides firmware functions to manage the following
*            functionalities of the Digital Camera Interface (DCMI) peripheral:
*            + Initialization and de-initialization functions
*            + IO operation functions
*            + Peripheral Control functions
*            + Peripheral State and Error functions
*
*/

./Src/stm32h7xx_hal_dfldm.c
stm32h7xx_hal_dfldm.c
/**
*****
* @file      stm32h7xx_hal_dfldm.c
* @author    MCD Application Team
* @brief     This file provides firmware functions to manage the following
*            functionalities of the Digital Filter for Sigma-Delta Modulators
*            (DFSDM) peripherals:
*            + Initialization and configuration of channels and filters
*            + Regular channels configuration
*            + Injected channels configuration
*            + Regular/Injected Channels DMA Configuration
*            + Interrupts and flags management
*            + Analog watchdog feature
*            + Short-circuit detector feature
*            + Extremes detector feature
*            + Clock absence detector feature
*            + Break generation on analog watchdog or short-circuit event
*
*/

./Src/stm32h7xx_hal_dfldm_ex.c
stm32h7xx_hal_dfldm_ex.c
/**
*****
* @file      stm32h7xx_hal_dfldm_ex.c
* @author    MCD Application Team
* @brief     DFSDM Extended HAL module driver.
*            This file provides firmware functions to manage the following

```

```

*           functionality of the DFSDM Peripheral Controller:
*           + Set and get pulses skipping on channel.
*
*****
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*
*****
*/

```

```

./Src/stm32h7xx_hal_dma.c
stm32h7xx_hal_dma.c

```

```

/**
*****
* @file      stm32h7xx_hal_dma.c
* @author    MCD Application Team
* @brief     DMA HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the Direct Memory Access (DMA) peripheral:
*
* + Initialization and de-initialization functions
* + IO operation functions
* + Peripheral State and errors functions

```

```

./Src/stm32h7xx_hal_dma2d.c
stm32h7xx_hal_dma2d.c

```

```

/**
*****
* @file      stm32h7xx_hal_dma2d.c
* @author    MCD Application Team
* @brief     DMA2D HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the DMA2D peripheral:
*
* + Initialization and de-initialization functions
* + IO operation functions
* + Peripheral Control functions
* + Peripheral State and Errors functions
*

```

```

./Src/stm32h7xx_hal_dma_ex.c
stm32h7xx_hal_dma_ex.c

```

```

/**
*****
* @file      stm32h7xx_hal_dma_ex.c
* @author    MCD Application Team
* @brief     DMA Extension HAL module driver
*
* This file provides firmware functions to manage the following
* functionalities of the DMA Extension peripheral:

```

```

*          + Extended features functions
*

./Src/stm32h7xx_hal_dsi.c
stm32h7xx_hal_dsi.c
/**
*****
* @file      stm32h7xx_hal_dsi.c
* @author    MCD Application Team
* @brief     DSI HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the DSI peripheral:
*            + Initialization and de-initialization functions
*            + IO operation functions
*            + Peripheral Control functions
*            + Peripheral State and Errors functions

./Src/stm32h7xx_hal_dts.c
stm32h7xx_hal_dts.c
/**
*****
* @file      stm32h7xx_hal_dts.c
* @author    MCD Application Team
* @brief     DTS HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the DTS peripheral:
*            + Initialization and de-initialization functions
*            + Start/Stop operation functions in polling mode.
*            + Start/Stop operation functions in interrupt mode.
*            + Peripheral Control functions
*            + Peripheral State functions

./Src/stm32h7xx_hal_eth.c
stm32h7xx_hal_eth.c
/**
*****
* @file      stm32h7xx_hal_eth.c
* @author    MCD Application Team
* @brief     ETH HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Ethernet (ETH) peripheral:
*            + Initialization and deinitialization functions
*            + IO operation functions
*            + Peripheral Control functions
*            + Peripheral State and Errors functions

./Src/stm32h7xx_hal_eth_ex.c
stm32h7xx_hal_eth_ex.c
/**
*****
* @file      stm32h7xx_hal_eth_ex.c
* @author    MCD Application Team

```

```

* @brief    ETH HAL Extended module driver.
*
*****
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*
*****
*/

./Src/stm32h7xx_hal_exti.c
stm32h7xx_hal_exti.c
/**
*****
* @file      stm32h7xx_hal_exti.c
* @author    MCD Application Team
* @brief     EXTI HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the General Purpose Input/Output (EXTI)
peripheral:
*
*            + Initialization and de-initialization functions
*            + IO operation functions
*
./Src/stm32h7xx_hal_fdcan.c
stm32h7xx_hal_fdcan.c
/**
*****
* @file      stm32h7xx_hal_fdcan.c
* @author    MCD Application Team
* @brief     FDCAN HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Flexible DataRate Controller Area Network
*            (FDCAN) peripheral:
*            + Initialization and de-initialization functions
*            + IO operation functions
*            + Peripheral Configuration and Control functions
*            + Peripheral State and Error functions
*
./Src/stm32h7xx_hal_flash.c
stm32h7xx_hal_flash.c
/**
*****
* @file      stm32h7xx_hal_flash.c
* @author    MCD Application Team
* @brief     FLASH HAL module driver.
*            This file provides firmware functions to manage the following

```

```

*      functionalities of the internal FLASH memory:
*      + Program operations functions
*      + Memory Control functions
*      + Peripheral Errors functions
*

```

```

./Src/stm32h7xx_hal_flash_ex.c
stm32h7xx_hal_flash_ex.c
/**

```

```

*****
* @file      stm32h7xx_hal_flash_ex.c
* @author    MCD Application Team
* @brief     Extended FLASH HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the FLASH extension peripheral:
*            + Extended programming operations functions
*

```

```

./Src/stm32h7xx_hal_gfxmmu.c
stm32h7xx_hal_gfxmmu.c
/**

```

```

*****
* @file      stm32h7xx_hal_gfxmmu.c
* @author    MCD Application Team
* @brief     This file provides firmware functions to manage the following
*            functionalities of the Graphic MMU (GFXMMU) peripheral:
*            + Initialization and De-initialization.
*            + LUT configuration.
*            + Force flush and/or invalidate of cache.
*            + Modify physical buffer addresses.
*            + Modify cache and pre-fetch parameters.
*            + Error management.
*

```

```

./Src/stm32h7xx_hal_gpio.c
stm32h7xx_hal_gpio.c
/**

```

```

*****
* @file      stm32h7xx_hal_gpio.c
* @author    MCD Application Team
* @brief     GPIO HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the General Purpose Input/Output (GPIO)
peripheral:
*            + Initialization and de-initialization functions
*            + IO operation functions
*

```

```

./Src/stm32h7xx_hal_hash.c
stm32h7xx_hal_hash.c
/**

```

```

*****
* @file      stm32h7xx_hal_hash.c
* @author    MCD Application Team

```

```

* @brief  HASH HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the HASH peripheral:
*
*   + Initialization and de-initialization methods
*
*   + HASH or HMAC processing in polling mode
*
*   + HASH or HMAC processing in interrupt mode
*
*   + HASH or HMAC processing in DMA mode
*
*   + Peripheral State methods
*
*   + HASH or HMAC processing suspension/resumption
*

```

./Src/stm32h7xx\_hal\_hash\_ex.c

stm32h7xx\_hal\_hash\_ex.c

/\*\*

\*\*\*\*\*

```

* @file    stm32h7xx_hal_hash_ex.c
* @author  MCD Application Team
* @brief  Extended HASH HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the HASH peripheral for SHA-224 and SHA-256
* algorithms:
*
*   + HASH or HMAC processing in polling mode
*
*   + HASH or HMAC processing in interrupt mode
*
*   + HASH or HMAC processing in DMA mode
*
* Additionally, this file provides functions to manage HMAC
* multi-buffer DMA-based processing for MD-5, SHA-1, SHA-224
* and SHA-256.
*
*
*

```

./Src/stm32h7xx\_hal\_hcd.c

stm32h7xx\_hal\_hcd.c

/\*\*

\*\*\*\*\*

```

* @file    stm32h7xx_hal_hcd.c
* @author  MCD Application Team
* @brief  HCD HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the USB Peripheral Controller:
*
*   + Initialization and de-initialization functions
*
*   + IO operation functions
*
*   + Peripheral Control functions
*
*   + Peripheral State functions
*

```

./Src/stm32h7xx\_hal\_hrtim.c

stm32h7xx\_hal\_hrtim.c

/\*\*

\*\*\*\*\*

```

* @file    stm32h7xx_hal_hrtim.c
* @author  MCD Application Team
* @brief  TIM HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the High Resolution Timer (HRTIM) peripheral:

```

```

*      + HRTIM Initialization
*      + Timer Time Base Unit Configuration
*      + Simple Time Base Start/Stop
*      + Simple Time Base Start/Stop Interrupt
*      + Simple Time Base Start/Stop DMA Request
*      + Simple Output Compare/PWM Channel Configuration
*      + Simple Output Compare/PWM Channel Start/Stop Interrupt
*      + Simple Output Compare/PWM Channel Start/Stop DMA Request
*      + Simple Input Capture Channel Configuration
*      + Simple Input Capture Channel Start/Stop Interrupt
*      + Simple Input Capture Channel Start/Stop DMA Request
*      + Simple One Pulse Channel Configuration
*      + Simple One Pulse Channel Start/Stop Interrupt
*      + HRTIM External Synchronization Configuration
*      + HRTIM Burst Mode Controller Configuration
*      + HRTIM Burst Mode Controller Enabling
*      + HRTIM External Events Conditioning Configuration
*      + HRTIM Faults Conditioning Configuration
*      + HRTIM Faults Enabling
*      + HRTIM ADC trigger Configuration
*      + Waveform Timer Configuration
*      + Waveform Event Filtering Configuration
*      + Waveform Dead Time Insertion Configuration
*      + Waveform Chopper Mode Configuration
*      + Waveform Compare Unit Configuration
*      + Waveform Capture Unit Configuration
*      + Waveform Output Configuration
*      + Waveform Counter Start/Stop
*      + Waveform Counter Start/Stop Interrupt
*      + Waveform Counter Start/Stop DMA Request
*      + Waveform Output Enabling
*      + Waveform Output Level Set/Get
*      + Waveform Output State Get
*      + Waveform Burst DMA Operation Configuration
*      + Waveform Burst DMA Operation Start
*      + Waveform Timer Counter Software Reset
*      + Waveform Capture Software Trigger
*      + Waveform Burst Mode Controller Software Trigger
*      + Waveform Timer Pre-loadable Registers Update Enabling
*      + Waveform Timer Pre-loadable Registers Software Update
*      + Waveform Timer Delayed Protection Status Get
*      + Waveform Timer Burst Status Get
*      + Waveform Timer Push-Pull Status Get
*      + Peripheral State Get

```

./Src/stm32h7xx\_hal\_hsem.c

stm32h7xx\_hal\_hsem.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_hsem.c

\* @author MCD Application Team

\* @brief HSEM HAL module driver.

\* This file provides firmware functions to manage the following  
\* functionalities of the semaphore peripheral:



```

*          + Semaphore Take function (2-Step Procedure) , non blocking
*          + Semaphore FastTake function (1-Step Procedure) , non blocking
*          + Semaphore Status check
*          + Semaphore Clear Key Set and Get
*          + Release and release all functions
*          + Semaphore notification enabling and disabling and callnack
functions
*          + IRQ handler management
*
*

```

./Src/stm32h7xx\_hal\_i2c.c

stm32h7xx\_hal\_i2c.c

```

/**
*****
* @file      stm32h7xx_hal_i2c.c
* @author    MCD Application Team
* @brief     I2C HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Inter Integrated Circuit (I2C) peripheral:
*            + Initialization and de-initialization functions
*            + IO operation functions
*            + Peripheral State and Errors functions
*

```

./Src/stm32h7xx\_hal\_i2c\_ex.c

stm32h7xx\_hal\_i2c\_ex.c

```

/**
*****
* @file      stm32h7xx_hal_i2c_ex.c
* @author    MCD Application Team
* @brief     I2C Extended HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of I2C Extended peripheral:
*            + Extended features functions
*

```

./Src/stm32h7xx\_hal\_i2s.c

stm32h7xx\_hal\_i2s.c

```

/**
*****
* @file      stm32h7xx_hal_i2s.c
* @author    MCD Application Team
* @brief     I2S HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Integrated Interchip Sound (I2S) peripheral:
*            + Initialization and de-initialization functions
*            + IO operation functions
*            + Peripheral State and Errors functions

```

./Src/stm32h7xx\_hal\_i2s\_ex.c

stm32h7xx\_hal\_i2s\_ex.c

```

/**
*****

```

```

* @file      stm32h7xx_hal_i2s_ex.c
* @author    MCD Application Team
* @brief     I2S HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of I2S extension peripheral:
*            + Extension features Functions
*

```

./Src/stm32h7xx\_hal\_irda.c  
stm32h7xx\_hal\_irda.c

```

/**
*****
* @file      stm32h7xx_hal_irda.c
* @author    MCD Application Team
* @brief     IRDA HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the IrDA (Infrared Data Association) Peripheral
*            (IRDA)
*            + Initialization and de-initialization functions
*            + IO operation functions
*            + Peripheral State and Errors functions
*            + Peripheral Control functions
*

```

./Src/stm32h7xx\_hal\_iwdg.c  
stm32h7xx\_hal\_iwdg.c

```

/**
*****
* @file      stm32h7xx_hal_iwdg.c
* @author    MCD Application Team
* @brief     IWDG HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Independent Watchdog (IWDG) peripheral:
*            + Initialization and Start functions
*            + IO operation functions
*

```

./Src/stm32h7xx\_hal\_jpeg.c  
stm32h7xx\_hal\_jpeg.c

```

/**
*****
* @file      stm32h7xx_hal_jpeg.c
* @author    MCD Application Team
* @brief     JPEG HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the JPEG encoder/decoder peripheral:
*            + Initialization and de-initialization functions
*            + JPEG processing functions encoding and decoding
*            + JPEG decoding Getting Info and encoding configuration setting
*            + JPEG enable/disable header parsing functions (for decoding)
*            + JPEG Input/Output Buffer configuration.
*            + JPEG callback functions
*            + JPEG Abort/Pause/Resume functions
*            + JPEG custom quantization tables setting functions

```

```

*          + IRQ handler management
*          + Peripheral State and Error functions
*

./Src/stm32h7xx_hal_lptim.c
stm32h7xx_hal_lptim.c
/**
*****
* @file      stm32h7xx_hal_lptim.c
* @author    MCD Application Team
* @brief     LPTIM HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Low Power Timer (LPTIM) peripheral:
*            + Initialization and de-initialization functions.
*            + Start/Stop operation functions in polling mode.
*            + Start/Stop operation functions in interrupt mode.
*            + Reading operation functions.
*            + Peripheral State functions.
*

./Src/stm32h7xx_hal_ltdc.c
stm32h7xx_hal_ltdc.c
/**
*****
* @file      stm32h7xx_hal_ltdc.c
* @author    MCD Application Team
* @brief     LTDC HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the LTDC peripheral:
*            + Initialization and de-initialization functions
*            + IO operation functions
*            + Peripheral Control functions
*            + Peripheral State and Errors functions
*

./Src/stm32h7xx_hal_ltdc_ex.c
stm32h7xx_hal_ltdc_ex.c
/**
*****
* @file      stm32h7xx_hal_ltdc_ex.c
* @author    MCD Application Team
* @brief     LTDC Extension HAL module driver.
*****
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*
*****

```

```

*/

./Src/stm32h7xx_hal_mdios.c
stm32h7xx_hal_mdios.c
/**
*****
* @file      stm32h7xx_hal_mdios.c
* @author    MCD Application Team
* @brief     MDIOS HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the MDIOS Peripheral.
*            + Initialization and de-initialization functions
*            + IO operation functions
*            + Peripheral Control functions
*
*
*

./Src/stm32h7xx_hal_mdma.c
stm32h7xx_hal_mdma.c
/**
*****
* @file      stm32h7xx_hal_mdma.c
* @author    MCD Application Team
* @brief     This file provides firmware functions to manage the following
*            functionalities of the Master Direct Memory Access (MDMA)
peripheral:
*            + Initialization/de-initialization functions
*            + I/O operation functions
*            + Peripheral State and errors functions

./Src/stm32h7xx_hal_mmc.c
stm32h7xx_hal_mmc.c
/**
*****
* @file      stm32h7xx_hal_mmc.c
* @author    MCD Application Team
* @brief     MMC card HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Secure Digital (MMC) peripheral:
*            + Initialization and de-initialization functions
*            + IO operation functions
*            + Peripheral Control functions
*            + MMC card Control functions
*
*

./Src/stm32h7xx_hal_mmc_ex.c
stm32h7xx_hal_mmc_ex.c
/**
*****
* @file      stm32h7xx_hal_mmc_ex.c
* @author    MCD Application Team
* @brief     MMC card Extended HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Secure Digital (MMC) peripheral:

```

```

*          + Extended features functions
*

./Src/stm32h7xx_hal_msp.c
stm32h7xx_hal_msp.c
/**
*****
* @file      stm32h7xx_hal_msp_template.c
* @author    MCD Application Team
* @brief     HAL MSP module.
*           This file template is located in the HAL folder and should be
copied
*           to the user folder.
*
*

./Src/stm32h7xx_hal_nand.c
stm32h7xx_hal_nand.c
/**
*****
* @file      stm32h7xx_hal_nand.c
* @author    MCD Application Team
* @brief     NAND HAL module driver.
*           This file provides a generic firmware to drive NAND memories
mounted
*           as external device.
*
*

./Src/stm32h7xx_hal_nor.c
stm32h7xx_hal_nor.c
/**
*****
* @file      stm32h7xx_hal_nor.c
* @author    MCD Application Team
* @brief     NOR HAL module driver.
*           This file provides a generic firmware to drive NOR memories mounted
*           as external device.
*
*

./Src/stm32h7xx_hal_opamp.c
stm32h7xx_hal_opamp.c
/**
*****
* @file      stm32h7xx_hal_opamp.c
* @author    MCD Application Team
* @brief     OPAMP HAL module driver.
*           This file provides firmware functions to manage the following
*           functionalities of the operational amplifier(s) peripheral:
*           + OPAMP configuration
*           + OPAMP calibration
*           Thanks to
*           + Initialization and de-initialization functions
*           + IO operation functions
*           + Peripheral Control functions
*           + Peripheral State functions

```

```

*

./Src/stm32h7xx_hal_opamp_ex.c
stm32h7xx_hal_opamp_ex.c
/**
*****
* @file      stm32h7xx_hal_opamp_ex.c
* @author    MCD Application Team
* @brief     Extended OPAMP HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the operational amplifier(s)(OPAMP1, OPAMP2 etc)
*            peripheral:
*            + Extended Initialization and de-initialization functions
*            + Extended Peripheral Control functions
*
*****
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*
*****
*/

./Src/stm32h7xx_hal_ospi.c
stm32h7xx_hal_ospi.c
/**
*****
* @file      stm32h7xx_hal_ospi.c
* @author    MCD Application Team
* @brief     OSPI HAL module driver.

./Src/stm32h7xx_hal_otfdec.c
stm32h7xx_hal_otfdec.c
/**
*****
* @file      stm32h7xx_hal_otfdec.c
* @author    MCD Application Team
* @brief     OTFDEC HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the On-The-Fly Decryption (OTFDEC) peripheral:
*            + Initialization and de-initialization functions
*            + Region setting/enable functions
*            + Peripheral State functions
*
*****

./Src/stm32h7xx_hal_pcd.c
stm32h7xx_hal_pcd.c
/**

```

```

*****
* @file      stm32h7xx_hal_pcd.c
* @author    MCD Application Team
* @brief     PCD HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the USB Peripheral Controller:
*            + Initialization and de-initialization functions
*            + IO operation functions
*            + Peripheral Control functions
*            + Peripheral State functions
*
./Src/stm32h7xx_hal_pcd_ex.c
stm32h7xx_hal_pcd_ex.c
/**
*****
* @file      stm32h7xx_hal_pcd_ex.c
* @author    MCD Application Team
* @brief     PCD Extended HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the USB Peripheral Controller:
*            + Extended features functions
*
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*
*****
*/

./Src/stm32h7xx_hal_pssi.c
stm32h7xx_hal_pssi.c
/**
*****
* @file      stm32h7xx_hal_pssi.c
* @author    MCD Application Team
* @brief     PSSI HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Parallel Synchronous Slave Interface (PSSI)
peripheral:
*            + Initialization and de-initialization functions
*            + IO operation functions
*            + Peripheral State and Errors functions
*
./Src/stm32h7xx_hal_pwr.c
stm32h7xx_hal_pwr.c

```

```

/**
*****
* @file      stm32h7xx_hal_pwr.c
* @author    MCD Application Team
* @brief     PWR HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Power Controller (PWR) peripheral:
*            + Initialization and de-initialization functions.
*            + Peripheral Control functions.
*            + Interrupt Handling functions.

./Src/stm32h7xx_hal_pwr_ex.c
stm32h7xx_hal_pwr_ex.c
/**
*****
* @file      stm32h7xx_hal_pwr_ex.c
* @author    MCD Application Team
* @brief     Extended PWR HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of PWR extension peripheral:
*            + Peripheral Extended features functions

./Src/stm32h7xx_hal_qspi.c
stm32h7xx_hal_qspi.c
/**
*****
* @file      stm32h7xx_hal_qspi.c
* @author    MCD Application Team
* @brief     QSPI HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the QuadSPI interface (QSPI).
*            + Initialization and de-initialization functions
*            + Indirect functional mode management
*            + Memory-mapped functional mode management
*            + Auto-polling functional mode management
*            + Interrupts and flags management
*            + MDMA channel configuration for indirect functional mode
*            + Errors management and abort functionality
*
*

./Src/stm32h7xx_hal_ramecc.c
stm32h7xx_hal_ramecc.c
/**
*****
* @file      stm32h7xx_hal_ramecc.c
* @author    MCD Application Team
* @brief     RAMECC HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the RAM ECC monitoring (RAMECC) peripheral:
*            + Initialization and de-initialization functions
*            + Monitoring operation functions
*            + Error informations functions
*            + State and error functions

```



./Src/stm32h7xx\_hal\_rcc.c

stm32h7xx\_hal\_rcc.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_rcc.c

\* @author MCD Application Team

\* @brief RCC HAL module driver.

\* This file provides firmware functions to manage the following  
\* functionalities of the Reset and Clock Control (RCC) peripheral:

\* + Initialization and de-initialization functions

\* + Peripheral Control functions

\*

./Src/stm32h7xx\_hal\_rcc\_ex.c

stm32h7xx\_hal\_rcc\_ex.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_rcc\_ex.c

\* @author MCD Application Team

\* @brief Extended RCC HAL module driver.

\* This file provides firmware functions to manage the following  
\* functionalities RCC extension peripheral:

\* + Extended Peripheral Control functions

\*

\*\*\*\*\*

\* @attention

\*

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\*

\*\*\*\*\*

\*/

./Src/stm32h7xx\_hal\_rng.c

stm32h7xx\_hal\_rng.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_rng.c

\* @author MCD Application Team

\* @brief RNG HAL module driver.

\* This file provides firmware functions to manage the following  
\* functionalities of the Random Number Generator (RNG) peripheral:

\* + Initialization and configuration functions

\* + Peripheral Control functions

\* + Peripheral State functions

\*

./Src/stm32h7xx\_hal\_rng\_ex.c

stm32h7xx\_hal\_rng\_ex.c

```
/**
*****
* @file      stm32h7xx_hal_rng_ex.c
* @author    MCD Application Team
* @brief     Extended RNG HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Random Number Generator (RNG) peripheral:
*            + Lock configuration functions
*            + Reset the RNG
*
*****
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*
*****
*/
```

./Src/stm32h7xx\_hal\_rtc.c

stm32h7xx\_hal\_rtc.c

```
/**
*****
* @file      stm32h7xx_hal_rtc.c
* @author    MCD Application Team
* @brief     RTC HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Real-Time Clock (RTC) peripheral:
*            + Initialization/de-initialization
*            + Calendar (Time and Date) configuration
*            + Alarms (Alarm A and Alarm B) configuration
*            + WakeUp Timer configuration
*            + TimeStamp configuration
*            + Tamper configuration
*            + Backup Data Registers configuration
*            + RTC Tamper and TimeStamp Pins Selection
*            + Interrupts and flags management
*
*****
```

./Src/stm32h7xx\_hal\_rtc\_ex.c

stm32h7xx\_hal\_rtc\_ex.c

```
/**
*****
* @file      stm32h7xx_hal_rtc_ex.c
* @author    MCD Application Team
* @brief     Extended RTC HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Real Time Clock (RTC) Extended peripheral:
*
*****
```

```

*          + RTC Time Stamp functions
*          + RTC Tamper functions
*          + RTC Wake-up functions
*          + Extended Control functions
*          + Extended RTC features functions
*

```

```

./Src/stm32h7xx_hal_sai.c
stm32h7xx_hal_sai.c

```

```

/**
*****
* @file      stm32h7xx_hal_sai.c
* @author    MCD Application Team
* @brief     SAI HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the Serial Audio Interface (SAI) peripheral:
*
* + Initialization/de-initialization functions
* + I/O operation functions
* + Peripheral Control functions
* + Peripheral State functions
*

```

```

./Src/stm32h7xx_hal_sai_ex.c
stm32h7xx_hal_sai_ex.c

```

```

/**
*****
* @file      stm32h7xx_hal_sai_ex.c
* @author    MCD Application Team
* @brief     SAI Extended HAL module driver.
*
* This file provides firmware functions to manage the following
* functionality of the SAI Peripheral Controller:
*
* + Modify PDM microphone delays.
*
*****
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*
*****
*/

```

```

./Src/stm32h7xx_hal_sd.c
stm32h7xx_hal_sd.c

```

```

/**
*****
* @file      stm32h7xx_hal_sd.c
* @author    MCD Application Team
* @brief     SD card HAL module driver.

```

```

*          This file provides firmware functions to manage the following
*          functionalities of the Secure Digital (SD) peripheral:
*          + Initialization and de-initialization functions
*          + IO operation functions
*          + Peripheral Control functions
*          + Peripheral State functions
*

./Src/stm32h7xx_hal_sdram.c
stm32h7xx_hal_sdram.c
/**
*****
* @file      stm32h7xx_hal_sdram.c
* @author    MCD Application Team
* @brief     SDRAM HAL module driver.
*          This file provides a generic firmware to drive SDRAM memories
mounted
*          as external device.
*

./Src/stm32h7xx_hal_sd_ex.c
stm32h7xx_hal_sd_ex.c
/**
*****
* @file      stm32h7xx_hal_sd_ex.c
* @author    MCD Application Team
* @brief     SD card Extended HAL module driver.
*          This file provides firmware functions to manage the following
*          functionalities of the Secure Digital (SD) peripheral:
*          + Extended features functions
*

./Src/stm32h7xx_hal_smartcard.c
stm32h7xx_hal_smartcard.c
/**
*****
* @file      stm32h7xx_hal_smartcard.c
* @author    MCD Application Team
* @brief     SMARTCARD HAL module driver.
*          This file provides firmware functions to manage the following
*          functionalities of the SMARTCARD peripheral:
*          + Initialization and de-initialization functions
*          + IO operation functions
*          + Peripheral Control functions
*          + Peripheral State and Error functions
*

./Src/stm32h7xx_hal_smartcard_ex.c
stm32h7xx_hal_smartcard_ex.c
/**
*****
* @file      stm32h7xx_hal_smartcard_ex.c
* @author    MCD Application Team
* @brief     SMARTCARD HAL module driver.

```

```
*          This file provides extended firmware functions to manage the
following
```

```
*          functionalities of the SmartCard.
*          + Initialization and de-initialization functions
*          + Peripheral Control functions
*
```

```
./Src/stm32h7xx_hal_smbus.c
```

```
stm32h7xx_hal_smbus.c
```

```
/**
```

```
*****
```

```
* @file      stm32h7xx_hal_smbus.c
* @author    MCD Application Team
* @brief     SMBUS HAL module driver.
*           This file provides firmware functions to manage the following
*           functionalities of the System Management Bus (SMBus) peripheral,
*           based on I2C principles of operation :
*           + Initialization and de-initialization functions
*           + IO operation functions
*           + Peripheral State and Errors functions
*
```

```
./Src/stm32h7xx_hal_spdifrx.c
```

```
stm32h7xx_hal_spdifrx.c
```

```
/**
```

```
*****
```

```
* @file      stm32h7xx_hal_spdifrx.c
* @author    MCD Application Team
* @brief     This file provides firmware functions to manage the following
*           functionalities of the SPDIFRX audio interface:
*           + Initialization and Configuration
*           + Data transfers functions
*           + DMA transfers management
*           + Interrupts and flags management
```

```
./Src/stm32h7xx_hal_spi.c
```

```
stm32h7xx_hal_spi.c
```

```
/**
```

```
*****
```

```
* @file      stm32h7xx_hal_spi.c
* @author    MCD Application Team
* @brief     SPI HAL module driver.
*           This file provides firmware functions to manage the following
*           functionalities of the Serial Peripheral Interface (SPI)
```

```
peripheral:
```

```
*           + Initialization and de-initialization functions
*           + IO operation functions
*           + Peripheral Control functions
*           + Peripheral State functions
*
```

```
./Src/stm32h7xx_hal_spi_ex.c
```

```
stm32h7xx_hal_spi_ex.c
```

```
/**
```

```

*****
* @file      stm32h7xx_hal_spi_ex.c
* @author    MCD Application Team
* @brief     Extended SPI HAL module driver.
*            This file provides firmware functions to manage the following
*            SPI peripheral extended functionalities :
*            + IO operation functions
*            + Peripheral Control functions
*
*****
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*****
*/

./Src/stm32h7xx_hal_sram.c
stm32h7xx_hal_sram.c
/**
*****
* @file      stm32h7xx_hal_sram.c
* @author    MCD Application Team
* @brief     SRAM HAL module driver.
*            This file provides a generic firmware to drive SRAM memories
*            mounted as external device.
*
*/

./Src/stm32h7xx_hal_swpmi.c
stm32h7xx_hal_swpmi.c
/**
*****
* @file      stm32h7xx_hal_swpmi.c
* @author    MCD Application Team
* @brief     SWPMI HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Single Wire Protocol Master Interface
(SWPMI).
*            + Initialization and Configuration
*            + Data transfers functions
*            + DMA transfers management
*            + Interrupts and flags management

./Src/stm32h7xx_hal_tim.c
stm32h7xx_hal_tim.c
/**
*****
* @file      stm32h7xx_hal_tim.c

```

```

* @author MCD Application Team
* @brief TIM HAL module driver.
* This file provides firmware functions to manage the following
* functionalities of the Timer (TIM) peripheral:
* + TIM Time Base Initialization
* + TIM Time Base Start
* + TIM Time Base Start Interruption
* + TIM Time Base Start DMA
* + TIM Output Compare/PWM Initialization
* + TIM Output Compare/PWM Channel Configuration
* + TIM Output Compare/PWM Start
* + TIM Output Compare/PWM Start Interruption
* + TIM Output Compare/PWM Start DMA
* + TIM Input Capture Initialization
* + TIM Input Capture Channel Configuration
* + TIM Input Capture Start
* + TIM Input Capture Start Interruption
* + TIM Input Capture Start DMA
* + TIM One Pulse Initialization
* + TIM One Pulse Channel Configuration
* + TIM One Pulse Start
* + TIM Encoder Interface Initialization
* + TIM Encoder Interface Start
* + TIM Encoder Interface Start Interruption
* + TIM Encoder Interface Start DMA
* + Commutation Event configuration with Interruption and DMA
* + TIM OCRef clear configuration
* + TIM External Clock configuration

```

./Src/stm32h7xx\_hal\_tim\_ex.c

stm32h7xx\_hal\_tim\_ex.c

/\*\*

\*\*\*\*\*

```

* @file stm32h7xx_hal_tim_ex.c
* @author MCD Application Team
* @brief TIM HAL module driver.
* This file provides firmware functions to manage the following
* functionalities of the Timer Extended peripheral:
* + Time Hall Sensor Interface Initialization
* + Time Hall Sensor Interface Start
* + Time Complementary signal break and dead time configuration
* + Time Master and Slave synchronization configuration
* + Time Output Compare/PWM Channel Configuration (for channels 5
and 6)
* + Timer remapping capabilities configuration

```

./Src/stm32h7xx\_hal\_uart.c

stm32h7xx\_hal\_uart.c

/\*\*

\*\*\*\*\*

```

* @file stm32h7xx_hal_uart.c
* @author MCD Application Team
* @brief UART HAL module driver.
* This file provides firmware functions to manage the following

```

\* functionalities of the Universal Asynchronous Receiver Transmitter  
Peripheral (UART).

- \* + Initialization and de-initialization functions
- \* + IO operation functions
- \* + Peripheral Control functions
- \*
- \*

./Src/stm32h7xx\_hal\_uart\_ex.c

stm32h7xx\_hal\_uart\_ex.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_uart\_ex.c

\* @author MCD Application Team

\* @brief Extended UART HAL module driver.

\* This file provides firmware functions to manage the following

extended

\* functionalities of the Universal Asynchronous Receiver Transmitter  
Peripheral (UART).

- \* + Initialization and de-initialization functions
- \* + Peripheral Control functions
- \*
- \*

./Src/stm32h7xx\_hal\_usart.c

stm32h7xx\_hal\_usart.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_usart.c

\* @author MCD Application Team

\* @brief USART HAL module driver.

\* This file provides firmware functions to manage the following

\* functionalities of the Universal Synchronous/Asynchronous Receiver

Transmitter

\* Peripheral (USART).

- \* + Initialization and de-initialization functions
- \* + IO operation functions
- \* + Peripheral Control functions
- \* + Peripheral State and Error functions
- \*

./Src/stm32h7xx\_hal\_usart\_ex.c

stm32h7xx\_hal\_usart\_ex.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_hal\_usart\_ex.c

\* @author MCD Application Team

\* @brief Extended USART HAL module driver.

\* This file provides firmware functions to manage the following

extended

\* functionalities of the Universal Synchronous Receiver Transmitter  
Peripheral (USART).

- \* + Peripheral Control functions
- \*



```

*

./Src/stm32h7xx_hal_wwdg.c
stm32h7xx_hal_wwdg.c
/**
*****
* @file      stm32h7xx_hal_wwdg.c
* @author    MCD Application Team
* @brief     WWDG HAL module driver.
*            This file provides firmware functions to manage the following
*            functionalities of the Window Watchdog (WWDG) peripheral:
*            + Initialization and Configuration functions
*            + IO operation functions

./Src/stm32h7xx_ll_adc.c
stm32h7xx_ll_adc.c
/**
*****
* @file      stm32h7xx_ll_adc.c
* @author    MCD Application Team
* @brief     ADC LL module driver
*****
* @attention
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*
*****
*/

./Src/stm32h7xx_ll_bdma.c
stm32h7xx_ll_bdma.c
/**
*****
* @file      stm32h7xx_ll_bdma.c
* @author    MCD Application Team
* @brief     BDMA LL module driver.
*****
* @attention
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*
*****

```

```

*/

./Src/stm32h7xx_ll_comp.c
stm32h7xx_ll_comp.c
/**
*****
* @file      stm32h7xx_ll_comp.c
* @author    MCD Application Team
* @brief     COMP LL module driver
*****
* @attention
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*
*****
*/

```

```

./Src/stm32h7xx_ll_crc.c
stm32h7xx_ll_crc.c
/**
*****
* @file      stm32h7xx_ll_crc.c
* @author    MCD Application Team
* @brief     CRC LL module driver.
*****
* @attention
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*
*****
*/

```

```

./Src/stm32h7xx_ll_crs.c
stm32h7xx_ll_crs.c
/**
*****
* @file      stm32h7xx_ll_crs.h
* @author    MCD Application Team
* @brief     CRS LL module driver.
*****
* @attention
*

```

```

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*
*****
*/

./Src/stm32h7xx_ll_dac.c
stm32h7xx_ll_dac.c
/**
*****
* @file      stm32h7xx_ll_dac.c
* @author    MCD Application Team
* @brief     DAC LL module driver
*****
* @attention
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*
*****
*/

./Src/stm32h7xx_ll_delayblock.c
stm32h7xx_ll_delayblock.c
/**
*****
* @file      stm32h7xx_ll_delayblock.c
* @author    MCD Application Team
* @brief     DelayBlock Low Layer HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the Delay Block peripheral:
*
* + input clock frequency range 25MHz to 208MHz
* + up to 12 oversampling phases
*
*****

./Src/stm32h7xx_ll_dma.c
stm32h7xx_ll_dma.c
/**
*****
* @file      stm32h7xx_ll_dma.c
* @author    MCD Application Team
* @brief     DMA LL module driver.
*****

```

```

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*
*****
*/

./Src/stm32h7xx_ll_dma2d.c
stm32h7xx_ll_dma2d.c
/**
*****
* @file      stm32h7xx_ll_dma2d.c
* @author    MCD Application Team
* @brief     DMA2D LL module driver.
*****
* @attention
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*
*****
*/

./Src/stm32h7xx_ll_exti.c
stm32h7xx_ll_exti.c
/**
*****
* @file      stm32h7xx_ll_exti.c
* @author    MCD Application Team
* @brief     EXTI LL module driver.
*****
* @attention
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*
*****
*/

```

./Src/stm32h7xx\_ll\_fmc.c

stm32h7xx\_ll\_fmc.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_ll\_fmc.c  
\* @author MCD Application Team  
\* @brief FMC Low Layer HAL module driver.  
\*

\* This file provides firmware functions to manage the following  
\* functionalities of the Flexible Memory Controller (FMC) peripheral

memories:

\* + Initialization/de-initialization functions  
\* + Peripheral Control functions  
\* + Peripheral State functions  
\*

./Src/stm32h7xx\_ll\_gpio.c

stm32h7xx\_ll\_gpio.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_ll\_gpio.c  
\* @author MCD Application Team  
\* @brief GPIO LL module driver.  
\*\*\*\*\*

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\*

\*\*\*\*\*

\*/

./Src/stm32h7xx\_ll\_hrtim.c

stm32h7xx\_ll\_hrtim.c

/\*\*

\*\*\*\*\*

\* @file stm32h7xx\_ll\_hrtim.c  
\* @author MCD Application Team  
\* @brief HRTIM LL module driver.  
\*\*\*\*\*

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*
*****
*/

./Src/stm32h7xx_ll_i2c.c
stm32h7xx_ll_i2c.c
/**
*****
* @file      stm32h7xx_ll_i2c.c
* @author    MCD Application Team
* @brief     I2C LL module driver.
*****
* @attention
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*
*****
*/

./Src/stm32h7xx_ll_lptim.c
stm32h7xx_ll_lptim.c
/**
*****
* @file      stm32h7xx_ll_lptim.c
* @author    MCD Application Team
* @brief     LPTIM LL module driver.
*****
* @attention
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*****
*/

./Src/stm32h7xx_ll_lpuart.c
stm32h7xx_ll_lpuart.c
/**
*****
* @file      stm32h7xx_ll_lpuart.c
* @author    MCD Application Team
* @brief     LPUART LL module driver.

```

```

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*
*****
*/

```

./Src/stm32h7xx\_ll\_mdma.c

stm32h7xx\_ll\_mdma.c

/\*\*

```

*****
* @file      stm32h7xx_ll_mdma.c
* @author    MCD Application Team
* @brief     MDMA LL module driver.
*****
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*****
*/

```

./Src/stm32h7xx\_ll\_opamp.c

stm32h7xx\_ll\_opamp.c

/\*\*

```

*****
* @file      stm32h7xx_ll_opamp.c
* @author    MCD Application Team
* @brief     OPAMP LL module driver
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*****

```

```

*/

./Src/stm32h7xx_ll_pwr.c
stm32h7xx_ll_pwr.c
/**
*****
* @file      stm32h7xx_ll_pwr.c
* @author    MCD Application Team
* @brief     PWR LL module driver.
*****
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*****
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```

```

./Src/stm32h7xx_ll_rcc.c
stm32h7xx_ll_rcc.c
/**
*****
* @file      stm32h7xx_ll_rcc.c
* @author    MCD Application Team
* @brief     RCC LL module driver.
*****
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*****
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```

```

./Src/stm32h7xx_ll_rng.c
stm32h7xx_ll_rng.c
/**
*****
* @file      stm32h7xx_ll_rng.c
* @author    MCD Application Team
* @brief     RNG LL module driver.
*****
* @attention
*

```



```

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*****
*/

./Src/stm32h7xx_ll_rtc.c
stm32h7xx_ll_rtc.c
/**
*****
* @file      stm32h7xx_ll_rtc.c
* @author    MCD Application Team
* @brief     RTC LL module driver.
*****
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*****
*/

./Src/stm32h7xx_ll_sdmmc.c
stm32h7xx_ll_sdmmc.c
/**
*****
* @file      stm32h7xx_ll_sdmmc.c
* @author    MCD Application Team
* @brief     SDMMC Low Layer HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the SDMMC peripheral:
*
* + Initialization/de-initialization functions
* + I/O operation functions
* + Peripheral Control functions
* + Peripheral State functions
*
*****

./Src/stm32h7xx_ll_spi.c
stm32h7xx_ll_spi.c
/**
*****
* @file      stm32h7xx_ll_spi.c
* @author    MCD Application Team

```

```

* @brief    SPI LL module driver.
*****
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*/

```

```

./Src/stm32h7xx_ll_swpmi.c
stm32h7xx_ll_swpmi.c
/**

```

```

*****
* @file      stm32h7xx_ll_swpmi.c
* @author    MCD Application Team
* @brief     SWPMI LL module driver.
*****
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*****
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```

```

./Src/stm32h7xx_ll_tim.c
stm32h7xx_ll_tim.c
/**

```

```

*****
* @file      stm32h7xx_ll_tim.c
* @author    MCD Application Team
* @brief     TIM LL module driver.
*****
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```

```

*****
*/

./Src/stm32h7xx_ll_usart.c
stm32h7xx_ll_usart.c
/**
*****
* @file    stm32h7xx_ll_usart.c
* @author  MCD Application Team
* @brief   USART LL module driver.
*****
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*****
*/

./Src/stm32h7xx_ll_usb.c
stm32h7xx_ll_usb.c
/**
*****
* @file    stm32h7xx_ll_usb.c
* @author  MCD Application Team
* @brief   USB Low Layer HAL module driver.
*
* This file provides firmware functions to manage the following
* functionalities of the USB Peripheral Controller:
*
* + Initialization/de-initialization functions
* + I/O operation functions
* + Peripheral Control functions
* + Peripheral State functions
*

./Src/stm32h7xx_ll_utils.c
stm32h7xx_ll_utils.c
/**
*****
* @file    stm32h7xx_ll_utils.c
* @author  MCD Application Team
* @brief   UTILS LL module driver.
*****
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*
*****
*/
/* Includes -----*/

./Src/system_stm32h7xx.c
system_stm32h7xx.c
/**
*****
* @file      system_stm32h7xx_dualcore_boot_cm4_cm7.c
* @author    MCD Application Team
* @brief     CMSIS Cortex-Mx Device Peripheral Access Layer System Source File.
*           This provides system initialization template function is case of
*           an application using a dual core STM32H7 device where
*           Cortex-M7 and Cortex-M4 boot are enabled at the FLASH option bytes
*
* This file provides two functions and one global variable to be called from
* user application:
*   - SystemInit(): This function is called at startup just after reset and
*                   before branch to main program. This call is made inside
*                   the "startup_stm32h7xx.s" file.
*
*   - SystemCoreClock variable: Contains the core clock, it can be used
*                               by the user application to setup the
SysTick
*                               timer or configure other parameters.
*
*   - SystemCoreClockUpdate(): Updates the variable SystemCoreClock and
must
*                               be called whenever the core clock is changed
*                               during program execution.
*
*****
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*/

./fused-src/gtest/gtest.h
gtest.h
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//
// Author: wan@google.com (Zhanyong Wan)
//
// The Google C++ Testing Framework (Google Test)
//
// This header file defines the public API for Google Test. It should be
// included by any test program that uses Google Test.
//
// IMPORTANT NOTE: Due to limitation of the C++ language, we have to
// leave some internal implementation details in this header file.
// They are clearly marked by comments like this:
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// program!
//
// Acknowledgment: Google Test borrowed the idea of automatic test
// registration from Barthelemy Dagenais' (barthelemy@prologique.com)
// easyUnit framework.

./include/gtest/gtest-death-test.h
gtest-death-test.h
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// Author: wan@google.com (Zhanyong Wan)
//
// The Google C++ Testing Framework (Google Test)
//
// This header file defines the public API for death tests. It is
// #included by gtest.h so a user doesn't need to include this
// directly.
```

```
./include/gtest/gtest-message.h
gtest-message.h
```

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//
// Author: wan@google.com (Zhanyong Wan)
//
// The Google C++ Testing Framework (Google Test)
//
// This header file defines the Message class.
//
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gtest-param-test.h
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//
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// Author: wan@google.com (Zhanyong Wan)

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//
// Author: wan@google.com (Zhanyong Wan)
//
// Utilities for testing Google Test itself and code that uses Google Test
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gtest-test-part.h
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// Author: mheule@google.com (Markus Heule)
//

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// easyUnit framework.

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//
// Author: wan@google.com (Zhanyong Wan)
//
// Google C++ Testing Framework definitions useful in production code.

./include/gtest/internal/gtest-death-test-internal.h
gtest-death-test-internal.h

```

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//
// Authors: wan@google.com (Zhanyong Wan), eefacm@gmail.com (Sean McAfee)
//
// The Google C++ Testing Framework (Google Test)
//
// This header file defines internal utilities needed for implementing
// death tests. They are subject to change without notice.

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//
// Author: keith.ray@gmail.com (Keith Ray)
//
// Google Test filepath utilities
//
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// Authors: wan@google.com (Zhanyong Wan), eefacm@gmail.com (Sean McAfee)
//
// The Google C++ Testing Framework (Google Test)
//
// This header file declares functions and macros used internally by
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//
// Authors: Dan Egnor (egnor@google.com)
//
// A "smart" pointer type with reference tracking. Every pointer to a
// particular object is kept on a circular linked list. When the last pointer
// to an object is destroyed or reassigned, the object is deleted.
//
// Used properly, this deletes the object when the last reference goes away.
// There are several caveats:
// - Like all reference counting schemes, cycles lead to leaks.
// - Each smart pointer is actually two pointers (8 bytes instead of 4).
// - Every time a pointer is assigned, the entire list of pointers to that
//   object is traversed. This class is therefore NOT SUITABLE when there
//   will often be more than two or three pointers to a particular object.
// - References are only tracked as long as linked_ptr<> objects are copied.
//   If a linked_ptr<> is converted to a raw pointer and back, BAD THINGS
//   will happen (double deletion).

```

```

//
// A good use of this class is storing object references in STL containers.
// You can safely put linked_ptr<> in a vector<>.
// Other uses may not be as good.
//
// Note: If you use an incomplete type with linked_ptr<>, the class
// *containing* linked_ptr<> must have a constructor and destructor (even
// if they do nothing!).
//
// Bill Gibbons suggested we use something like this.
//
// Thread Safety:
// Unlike other linked_ptr implementations, in this implementation
// a linked_ptr object is thread-safe in the sense that:
// - it's safe to copy linked_ptr objects concurrently,
// - it's safe to copy *from* a linked_ptr and read its underlying
//   raw pointer (e.g. via get()) concurrently, and
// - it's safe to write to two linked_ptrs that point to the same
//   shared object concurrently.
// TODO(wan@google.com): rename this to safe_linked_ptr to avoid
// confusion with normal linked_ptr.

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gtest-param-util-generated.h
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//
// Author: vladl@google.com (Vlad Losev)

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// Authors: wan@google.com (Zhanyong Wan)
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//
// Authors: wan@google.com (Zhanyong Wan), eefacm@gmail.com (Sean McAfee)
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// The Google C++ Testing Framework (Google Test)
//
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// Author: wan@google.com (Zhanyong Wan)

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// Author: vladl@google.com (Vlad Losev)

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//
// Authors: vladl@google.com (Vlad Losev)
//
// The Google C++ Testing Framework (Google Test)
//
// This header file provides classes and functions used internally
// for testing Google Test itself.
```

```
./test/gtest-typed-test_test.h
gtest-typed-test_test.h
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./test/production.h
production.h
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//
// Author: wan@google.com (Zhanyong Wan)
//
// This is part of the unit test for include/gtest/gtest_prod.h.

```

```
./xcode/Samples/FrameworkSample/widget.h
widget.h
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//
// Author: preston.a.jackson@gmail.com (Preston Jackson)
//
// Google Test - FrameworkSample
// widget.h
//
```